

## V635

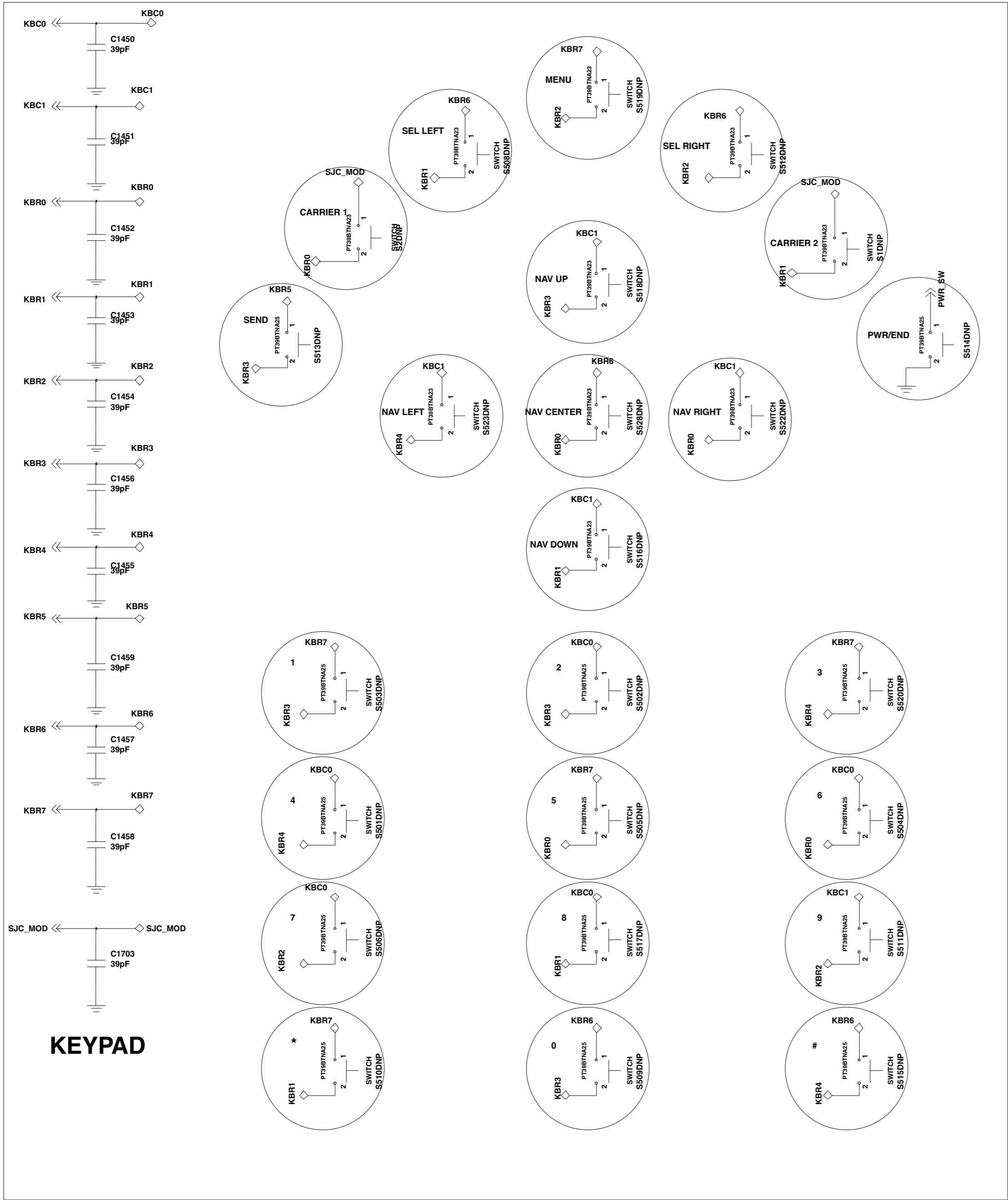


GSM SERVICE SUPPORT GROUP	2004.11.26
Level 3 Schematics	Rev. 1.0
V635	8488789Y01_P3C
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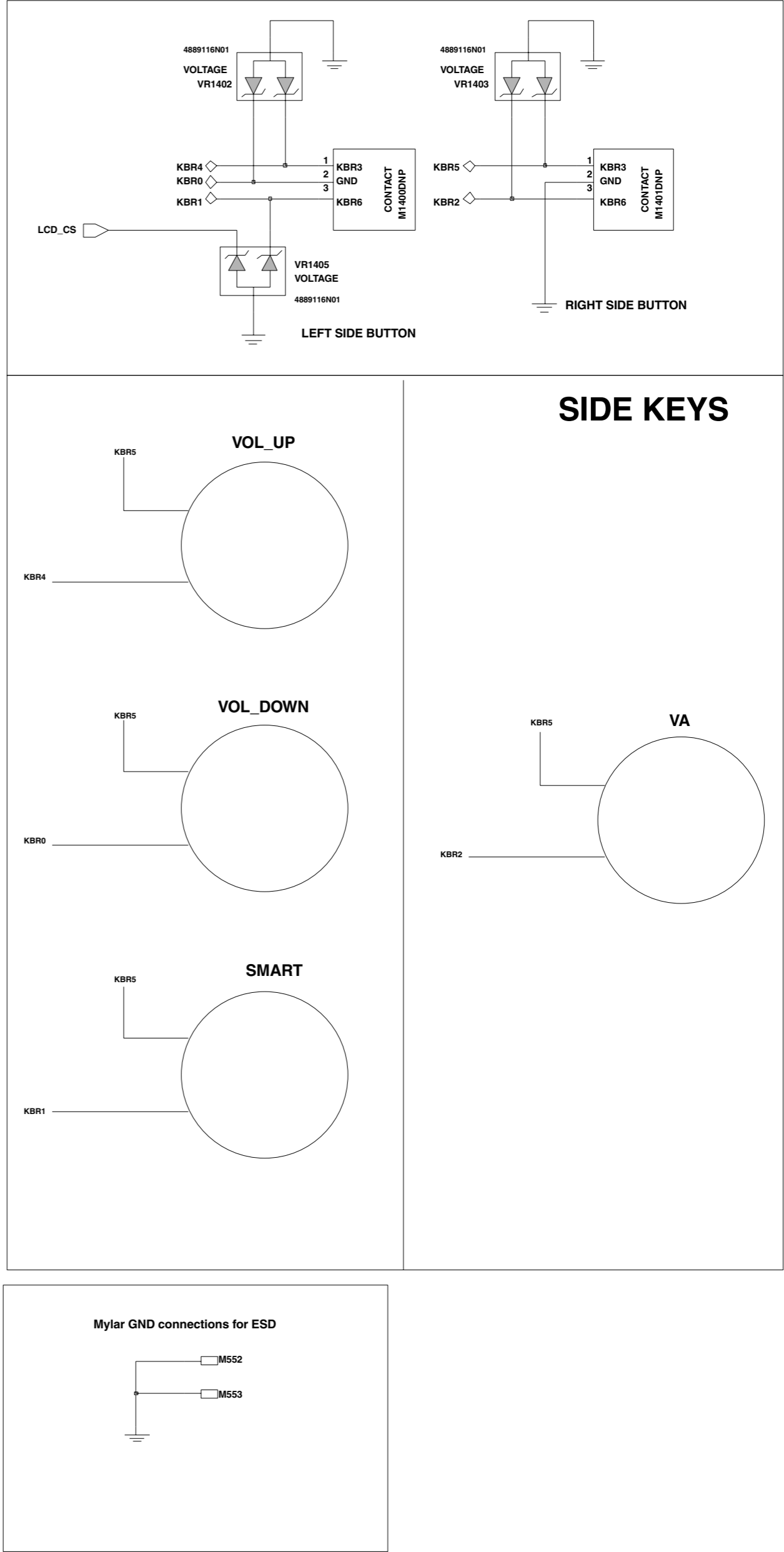


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MOTOROLA CONFIDENTIAL PROPRIETARY  
CONNECTIVITY



KEYPAD



SIDE KEYS

Mylar GND connections for ESD

**Stereo Headset Jack**

The schematic diagram illustrates the internal circuitry of a device's stereo headset jack and internal microphone. The top section, labeled "Stereo Headset Jack", features a J1200DNP connector with pins for SPK\_L, MIC\_L, SW, SPK\_R, and GND. These pins are connected to a network of components including resistors (R1711, R1241), capacitors (C1040, C1240, C1074, C1038), inductors (L1077, L1074, L1242), and a diode (VR1240). The circuit is designed to interface with external components like HJACK\_DET, HJACK\_MIC, HJACK\_SPKR, and HJACK\_SPKL. The bottom section, labeled "Internal Microphone", shows a J1200 component connected to a network of capacitors (C1200, C1202DNP, C1038) and a diode (VR1243), which then connects to an INT\_MIC+ pin. A 5550DNP switch is also shown, connected to HS\_INT and INT pins.

**SIM BLOCK**

VSM

C1243  
0.1uF

5VSM

1k

50pF

74VCR CONTACT

P1N1 P1N2 P1N3 P1N4 P2N1 P2N2 P2N3 P2N4

SIM\_RST  
SIM\_CLK

SIM\_DIO

50pF

**SIM**

**Alert Speaker**

VS1260 480V78E17 6.8V

L1580 25.001k

C1362 33pF

C1360 10pF

ALERT+

VS1261 480V78E17 6.8V

L1581 30.9k

C1363 33pF

C1361 10pF

ALERT-

[illegible]

**Accessory Connector**  
**CE Connector (0987636K06)**

**Legend:**  
 B0 - Normally Closed  
 B1 - Normally Open  
 S - Select  
 A - Common

**Notes:**  
 Camera needs to be included.  
 CLI needs to be included.

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CLI needs to be included.

[illegible]

