Freescale Semiconductor

Advance Information

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MMM6025



Package Information Case 1603-2 9.85 × 9.0 × 1.4 mm HDI (Organic Multi-Chip Module)

Ordering Information

Device	Operating Temp. Range	Package
MMM6025	-20° to $70^{\circ}C$	HDI Module
MMM6025R2	–20° to 70°C	HDI Module Tape and Reel

MMM6025

Quad-Band GSM/GPRS Power Amplifier Front-End Module with PA and Antenna Switch

1 Introduction

The MMM6025 is a 50 Ω Tx Power Amplifier Front-End Module for quad- and tri-band GSM/GPRS handset applications, functioning over the GSM850, EGSM, DCS, and PCS transmit and receive frequency bands. It is compatible with GSM/GPRS Class 12 operating modes. To simplify radio front-end design requirements, power amplification, power coupling, power detection, low pass filtering, and antenna switching functions are integrated into the Power Amplifier Front-End Module. Transmit/receive path and enable functions are controlled through 0/2.8 V logic inputs.

Contents

1 Introduction 1
2 Functional Block Diagram 2
3 Electrical Characteristics 2
4 RF Specifications 4
5 Signal Descriptions 11
6 Package Information 14
7 Product Documentation 16

This document contains information on a new product. Specifications and information herein are subject to change without notice.

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Functional Block Diagram

2 Functional Block Diagram

Figure 1 is a functional block diagram of the quad-band (GSM850, EGSM, DCS, and PCS) power amplifier module.



Figure 1. Functional Block Diagram

3 Electrical Characteristics

Table 1. Maximum Ratings

Rating	Symbol	Value	Unit
Drain Supply Voltages	Vdd	7.0	V
RF Input Power	P _{in}	11	dBm
Operating (Ambient) Temperature Range	T _A	-20 to 70	°C
Storage Temperature	Tstg	-40 to 125	°C
Junction Temperature	TJ	125	°C

Note: Maximum Ratings and ESD

1. Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the limits in the Electrical Characteristics or Recommended Operating Conditions tables.

2. ESD (electrostatic discharge) immunity meets Human Body Model (HBM) ≤ 150 V and Machine Model (MM) ≤ 50 V. Additional ESD data available upon request.



Electrical Characteristics

Characteristic	Symbol	Min	Тур	Мах	Unit
Drain Supply Voltage	Vdd	2.8	3.2	4.5	V
Power Control Ramp Voltage	V _{RAMP}	0.1	-	2.2	V
RF Input Power	P _{in}	5	-	11	dBm

Table 2. Recommended Operating Conditions

Table 3. DC Specifications

Characteristic	Symbol	Min	Тур	Max	Unit
Voltage PA I (Drain Supplies) Transmitter Off Transmitter On	Vdd _(TX_off) Vdd _(TX_on)	- 2.8	3.6 3.2	7.0 4.5	Vdc
PA Output Control Voltage	V _{APC}	0.1	0.2 to 2.1	2.2	V
PA Output Control Current	I _{APC}	-3.0	-	3.5	mA
Detector PA Enable Voltage ¹ High Low	Ven_det_pa(h) Ven_det_pa(l)	2.5 0	-	2.9 0.3	Vdc
Detector PA Enable Current ¹ Source Sink	I _{EN_DET_PA(H)} I _{EN_DET_PA(L)}	-	-	2.0 10	mA μA
Mode Select Voltage US (selects GSM850 or PCS) EU (selects EGSM or DCS)	V _{EUB_} US(H) V _{EUB_} US(L)	2.5 0	-	2.9 0.3	Vdc
RX Band Select Current Source Sink	I _{EUB_US(H)} I _{EUB_US(L)}	-	-	0.1 10	mA μA
TX Antenna Enable Voltage High Low	V _{EN_ANT_TX} (H) V _{EN_ANT_TX} (L)	2.5 0	-	2.9 0.3	Vdc
TX Antenna Enable Current Source Sink	I _{EN_ANT_TX(H)} I _{EN_ANT_TX(L)}	-	-	0.1 10	mA μA
Voltage PA 2 (Regulated Supply)	V _{REG28}	2.65	-	2.9	V
Current Draw	I _{REG28}	-	-	18	mA
BAND SELECT High (DCS/PCS Bands Selected) Low (GSM850/EGSM Bands Selected)	V _{LOWB} _HIGH(H) V _{LOWB} _HIGH(L)	2.5 0		2.9 0.3	V



Table 3. DC Specifications (continued)

Characteristic	Symbol	Min	Тур	Max	Unit
BAND SELECT Current Level High Low	I _{LOWB_} HIGH(H) I _{LOWB_} HIGH(L)	-	-	10 10	μA
Total module leakage current (Standby condition) $EN_DET_PA = 0.3 V$ $EUB_US = EN_ANT_TX = +0.3 V$ $LOWB_HIGH = 0.3 V$ $V_{APC} = 0.1 V$ $V_{REG_IN} = 0 V \text{ or } 2.775 V$ Temp = 23 to 27°C Vdd = 3.8 V	I _{Vdd(off)}	-	5.0	7.0	μΑ

The MMM6025 pinout is compatible with that of MMM6022, *except* pin 8. MMM6025 signal EN_DET_PA (pin 16) is equivalent to a combination of both MMM6022 signals EN_TX and EN_DET.

The following table provides additional details on MMM6025 orderable parts.

Table 4. Orderable Parts Details

Device	Operating Temp. Range (TA)	Package	RoHS Compliant	Pb-Free	MSLLevel	SolderTemp.
MMM6025	–20° to 70°C	HDI Module	Yes	Yes	3	250 °C
MMM6025R2	–20° to 70°C	HDI Module Tape and Reel	Yes	Yes	3	250 °C

RF Specifications 4

This section details specifications for the EGSM, DCS, GSM850, and PCS bands.

Table 5. EGSM Band Specifications

Characteristic	Symbol	Min	Тур	Max	Unit		
EGSM BAND (P _{in} = 5.0 to 11 dBm, Vdd = 3.2 Vdc, V_{APC} = 0.1 to 2.2 V pulsed, 25% duty cycle, LOWB_HIGH = Low, EUB_US = Low, T _C = 25°C ±5°C, unless otherwise noted.)							
Operating Frequency Transmit Receive	f	880 925	-	915 960	MHz		
Power Out	P _{o(max)}	33	-	-	dBm		
Power Out Low Voltage (Vdd = 2.8 V)	P _{o(min)}	32	-	-	dBm		
Power Added Efficiency (Saturated Pout)	PAE	40	44	-	%		
Power Control Range ¹	P _{o(range)}	35	-	-	dB		
Power Control Slope ²	$\Delta V_{det} / \Delta V_{APC}$	-	-	3.0	V/V		



Characteristic	Symbol	Min	Тур	Max	Unit
Power Control Frequency 3.0 dB BW	PC3dB	1.0	-	-	MHz
Power Control Response Time ³	t _{PC}	-	-	1.5	μs
Forward Isolation ⁴	ISO1 ISO2	-	-	-41 -22	dBm
Power Detector Voltage ¹ Low Power High Power	V _{DET(low)} V _{DET(high)}	40 1.0	- -	150 2.05	mV V
Harmonics ⁵ (P _{out} Max = 33 dBm)	2f _{0 -} 15f ₀	-	-	-33	dBm
GSM RX Band Noise ⁵ (Vdd = 4.2 V, Saturated P_{out})	n _{RX} (925 to 935 MHz)	-	-	-73	dBm/ 100 kHz
(P _{out} = 33 dBm)	n _{RX} (935 to 960 MHz)	-	-	-84	dBm/ 100 kHz
P_{out} over Temp (Vdd = 2.8 V, T_A = -20 to 70°C)	Po_ot(min)	31.5	-	-	dBm
Insertion Loss from Antenna to RX_GSM ⁵	IL_RX	-	-	1.4	dB
Tx - Rx Isolation ⁵ ANTENNA to RX_CEL ANTENNA to RX_GSM ANTENNA to RX_DCS ANTENNA to RX_PCS	ISO_ANT_RX_CEL ISO_ANT_RX_GSM ISO_ANT_RX_DCS ISO_ANT_RX_PCS	27 27 27 27 27	- - -	- - - -	dB
Input VSWR	VSWR	-	-	2:1	
Power Out Change due to Coupling Variations (VSWR = 3:1 @ ANT Port) ⁶	CPLV	-1.5	-	1.5	dB
Load Mismatch Stress (Ruggedness) ⁷	Rugg	No perform	-	adation and nage	no module
Stability - Spurious Output ⁸	Stab	-	-	-36	dBm
Closed Loop Power Variation over Temperature ⁹	P _{out} _Temp Ambient 1 (P _{out} = 6 dBm) P _{out} _Temp Ambient 2 (P _{out} = 20 dBm)	-1.5 1.0	-	1.5 1.0	dB dB
	P _{out} _Temp Ambient 3 (P _{out} = 33 dBm)	-0.5	-	0.5	dB

Table 5. EGSM Band Specifications (continued)

¹ Power output must be monotonic with power detector voltage. (Vdd = 2.8V to 4.5V and Temperature = -20 to $+70^{\circ}C$)

² Vdd = 2.8V to 4.5V and Temperature = -20 to +70°C

³ Measurement made from 50% of V_{APC} to 10%/90% of V_{DET_OUT} final value, with pulsed V_{APC} with a peak voltage in the range of 0.1 to 2.2 V and both rise and fall edges.

⁴ Measured at antenna port, ISO1: P_{in} = -10 dBm, EN_ANT_TX = 0 V, V_{APC} = 0.1 V; ISO2: P_{in} = 11 dBm, EN_ANT_TX = 2.723 V, V_{APC} = 0.1 V. (Vdd = 2.8V to 4.5V and Temperature = -20 to +70°C)

⁵ Vdd = 2.8V to 4.5V and Temperature = -20 to +70°C

Maintaining constant Vdet-Vref at all mismatch phase angles. Measured at antenna port: Pout = 33 dBm 6

⁷ Output VSWR = 20:1 all phase angles, Vdd = 2.8V to 4.5V, Pin = 5dBm to 11dBm, Temperature = -20 to +70°C, Power less than or equal to 33dBm.



- ⁸ Output VSWR = 10:1 all phase angles, Vdd = 2.8V to 4.5V, Pin = 5dBm to 11dBm, Temperature = -20 to +70°C, Power less than or equal to 33dBm.
- ⁹ Vdiff_var = 20*LOG(Delta(T)/Delta(25°C)), where T is -20 to 65°C, Delta(T)= [Vdiff(T) with RF] [Vdiff(T) without RF], and Delta(25°C)= [Vdiff(25°C) with RF] [Vdiff(25°C) without RF]

Characteristic	Symbol	Min	Тур	Max	Unit
DCS BAND ($P_{in} = 5.0$ to 11 dBm, Vdd = 3.2 Vdc, V _{AF} LOWB_HIGH = High, EUB_US = Low, T _C = 25°C ±5			e,		1
Operating Frequency Transmit Receive	f	1710 1805	-	1785 1880	MHz
Power Out	P _{o(max)}	30	-	-	dBm
Power Out Low Voltage (Vdd = 2.8 V)	P _{o(min)}	29	-	-	dBm
Power Added Efficiency (Saturated Pout)	PAE	28	33	-	%
Power Control Range ¹	P _{o(range)}	35	-	-	dB
Power Control Slope ²	$\Delta V_{det} / \Delta V_{APC}$	-	-	3.5	V/V
Power Control Frequency 3.0 dB BW	PC3dB	1.0	-	-	MHz
Power Control Response Time ³	t _{PC}	-	-	1.5	μs
Forward Isolation ⁴	ISO1 ISO2	-	-	-53 -25	dBm
Power Detector Voltage ¹ Low Power High Power	VDET(low) VDET(high)	40 1.0	-	150 2.05	mV V
Harmonics ⁶ (P _{out} Max = 30 dBm)	2f _{0 -} 15f ₀	-	-	-33	dBm
RX Band Noise ^{5,6} (P _{out} = 30 dBm)	n _{RX}	-	-	-77	dBm/ 100 kHz
P_{out} over Temp (Vdd = 2.8 V, $T_A = -20$ to 70°C)	Po_ot(min)	28.5	-	-	dBm
Insertion Loss from Antenna to RX_DCS ⁶	IL_RX	-	-	1.7	dB
Tx - Rx Isolation ⁶ ANTENNA to RX_CEL ANTENNA to RX_GSM ANTENNA to RX_DCS ANTENNA to RX_PCS	ISO_ANT_RX_CEL ISO_ANT_RX_GSM ISO_ANT_RX_DCS ISO_ANT_RX_PCS	27 27 27 27 27	- - -	- - -	dB
Input VSWR	VSWR	-	-	2:1	
Power Out Change due to Coupling Variations (VSWR = 3:1 @ ANT Port) ⁷	CPLV	-1.5	-	1.5	dB
Load Mismatch Stress (Ruggedness) ⁸	Rugg	No per		l degradatio e damage	n and no

Table 6. DCS Band Specifications



Characteristic	Symbol	Min	Тур	Max	Unit
Stability - Spurious Output ⁹	Stab	-	-	-36	dBm
Closed Loop Power Variation over Temperature ¹⁰	P _{out} _Temp Ambient 1 (P _{out} = 3 dBm)	-1.5	-	1.5	dB
	P _{out} _Temp Ambient 2 (P _{out} = 15 dBm)	1.0	-	1.0	dB
	P _{out} _Temp Ambient 3 (P _{out} = 30 dBm)	-0.5	-	0.5	dB

¹ Power output must be monotonic with power detector voltage. (Vdd = 2.8V to 4.5V and Temperature = -20 to +70°C)

² Vdd = 2.8V to 4.5V and Temperature = -20 to $+70^{\circ}$ C

³ Measurement made from 50% of V_{APC} to 10%/90% of V_{DET OUT} final value, with pulsed V_{APC} with a peak voltage in the range of 0.1 to 2.2 V and both rise and fall edges.

⁴ Measured at antenna port: ISO1: P_{in} = -10 dBm, EN_ANT_TX = 0 V, V_{APC} = 0.1 V; ISO2: P_{in} = 11 dBm, EN_ANT_TX = 2.723 V, V_{APC} = 0.1 V. (Vdd = 2.8V to 4.5V and Temperature = -20 to +70°C)

- ⁵ DCS RX band = 1805 to 1880 MHz. Vdd = 4.2V, Saturated Pout.
- ⁶ Vdd = 2.8V to 4.5V and Temperature = -20 to +70°C
- ⁷ Maintaining constant Vdet-Vref at all mismatch phase angles. Measured at antenna port: $P_{out} = 30 \text{ dBm}$
- ⁸ VSWR = 20:1 all phase angles, Vdd = 2.8V to 4.5V, Pin = 5dBm to 11dBm, Temperature = -20 to +70°C, Power less than or equal to 30 dBm
- ⁹ Output VSWR = 10:1 all phase angles, Vdd = 2.8V to 4.5V, Pin = 5dBm to 11dBm, Temperature = -20 to +70°C, Power less than or equal to 30dBm.
- ¹⁰ Vdiff var = 20*LOG(Delta(T)/Delta(25°C)), where T is -20 to 65°C, Delta(T)= [Vdiff(T) with RF] [Vdiff(T) without RF], and Delta(25°C)= [Vdiff(25°C) with RF] - [Vdiff(25°C) without RF]

Characteristic	Symbol	Min	Тур	Max	Unit
GSM850 BAND ($P_{in} = 5.0$ to 11 dBm, Vdd = 3.2 Vdc, V_{AP} = High, $T_C = 25^{\circ}C \pm 5^{\circ}C$, unless otherwise noted.)	_C = 0.1 to 2.2 V pulsed, 2	25% duty cyc	le, LOWB_	HIGH = Lov	w, EUB_US
Operating Frequency Transmit Receive	f	824 869	-	849 894	MHz
Power Out	P _{o(max)}	33	-	-	dBm
Power Out Low Voltage (V _{CC_PA} = 2.8 V)	P _{o(min)}	32	-	-	dBm
Power Added Efficiency (Saturated Pout)	PAE	38	43	-	%
Power Control Range ¹	P _{o(range)}	35	-	-	dB
Power Control Slope ²	$\Delta V_{det} / \Delta V_{APC}$	-	-	3.0	V/V
Power Control Frequency 3.0 dB BW	PC3dB	1.0	-	-	MHz
Power Control Response Time ³	t _{PC}	-	-	1.5	μs
Forward Isolation ⁴	ISO1 ISO2	-	-	-41 -22	dBm

Table 7. GSM850 Band Specifications



Characteristic	Symbol	Min	Тур	Max	Unit
Power Detector Voltage ¹ Low Power High Power	VDET(low) VDET(high)	40 1.0	-	150 2.05	mV V
Harmonics ⁶ (P _{out} Max = 33 dBm)	2f _{0 -} 15f ₀	-	-	-33	dBm
GSM850 RX Band Noise ^{5,6} (P _{out} = 33 dBm)	n _{RX}	-	-	-84	dBm/ 100 kHz
P_{out} over Temp (Vdd = 2.8 V, T_A = -20 to 70°C)	Po_ot(min)	31.5	-	-	dBm
Insertion Loss from Antenna to RX_GSM850 ⁶	IL_RX	-	-	1.4	dB
Tx - Rx Isolation ⁶ ANTENNA to RX_CEL ANTENNA to RX_GSM ANTENNA to RX_DCS ANTENNA to RX_PCS	ISO_ANT_RX_CEL ISO_ANT_RX_GSM ISO_ANT_RX_DCS ISO_ANT_RX_PCS	27 27 27 27 27	- - -	- - -	dB
Input VSWR	VSWR	-	-	2:1	
Power Out Change due to Coupling Variations (VSWR =3:1 @ ANT Port) ⁷	CPLV	-1.5	-	1.5	dB
Load Mismatch Stress (Ruggedness) ⁸	Rugg	No performance degradation and no module damage			
Stability - Spurious Output ⁹	Stab	-	-	-36	dBm
Closed Loop Power Variation over Temperature ¹⁰	P_{out} Temp Ambient 1 $P_{out} = 6 \text{ dBm}$ P_{out} Temp Ambient 2 $P_{out} = 20 \text{ dBm}$ $P_{out} = 20 \text{ dBm}$	-1.5 dB 1.0 dB -0.5 dB	-	1.5dB 1.0 dB 0.5 dB	dB dB dB
	P _{out} _Temp Ambient 3 P _{out} = 33 dBm	-0.5 08	-	0.5 08	uБ

Power output must be monotonic with power detector voltage. (Vdd = 2.8V to 4.5V and Temperature = -20 to +70°C) 1

 $^2~$ Vdd = 2.8V to 4.5V and Temperature = -20 to +70°C

- 3 Measurement made from 50% of VAPC to 10%/90% of VDET_OUT final value, with pulsed VAPC with a peak voltage in the range of 0.1 to 2.2 V and both rise and fall edges.

⁴ Measured at antenna port: ISO1: P_{in} = -10 dBm, EN_ANT_TX = 0 V, V_{APC} = 0.1 V; ISO2: P_{in} = 11 dBm, EN_ANT_TX = 2.723 V, V_{APC} = 0.1 V. (Vdd = 2.8V to 4.5V and Temperature = -20 to +70°C)

- ⁵ GSM850 RX band = 869 to 894 MHz. Vdd = 4.2 V, Saturated P_{out} .
- ⁶ Vdd = 2.8V to 4.5V and Temperature = -20 to +70°C

⁷ Maintaining constant Vdet-Vref at all mismatch phase angles. Measured at antenna port: P_{out} = 33 dBm

- ⁸ VSWR = 20:1 all phase angles, Vdd = 2.8V to 4.5V, Pin = 5dBm to 11dBm, Temperature = -20 to +70°C, Power less than or equal to 33dBm
- 9 Output VSWR = 10:1 all phase angles, Vdd = 2.8V to 4.5V. Pin = 5dBm to 11dBm, Temperature = -20 to +70°C, Power less than or equal to 33dBm.
- ¹⁰ Vdiff_var = 20*LOG(Delta(T)/Delta(25°C)), where T is -20 to 65°C, Delta(T)= [Vdiff(T) with RF] [Vdiff(T) without RF], and Delta(25°C)= [Vdiff(25°C) with RF] - [Vdiff(25°C) without RF]



Table 8. PCS Band Specifications

Characteristic	Symbol	Min	Тур	Max	Unit
PCS BAND (P _{in} = 5.0 to 11 dBm, Vdd = 3.2 Vdc, V_{APC} = 0.1 to 2.2 V High, T _C = 25°C ±5°C, unless otherwise noted.)	pulsed, 25% duty cycle	e, LOWI	B_HIG	GH = Hię	gh, EUB_US =
Operating Frequency Transmit Receive	f	1850 1930	-	1910 1990	MHz
Power Out	P _{o(max)}	30	-	-	dBm
Power Out Low Voltage (Vdd = 2.8 V)	P _{o(min)}	29	-	-	dBm
Power Added Efficiency (Saturated Pout)	PAE	28	33	-	%
Power Control Range ¹	P _{o(range)}	35	-	-	dB
Power Control Slope ²	$\Delta V_{det} / \Delta V_{APC}$	-	-	3.5	V/V
Power Control Frequency 3.0 dB BW	PC3dB	1.0	-	-	MHz
Power Control Response Time ³	t _{PC}	-	-	1.5	μs
Forward Isolation ⁴	ISO1 ISO2	-	-	-53 -25	dBm
Power Detector Voltage ¹ Low Power High Power	VDET(low) VDET(high)	40 1.0	-	150 2.05	mV V
Harmonics ⁶ (P _{out} Max = 30 dBm)	2f _{0 -} 15f ₀	-	-	-33	dBm
RX Band Noise ^{5,6} (P _{out} = 30 dBm)	n _{RX}	-	-	-77	dBm/100 kHz
P_{out} over Temp (Vdd = 2.8 V, T_A = -20 to 70°C)	P _{o_ot(min)}	28.5	-	-	dBm
Insertion Loss from Antenna to RX_PCS ⁶	IL_RX	-	-	1.7	dB
Tx - Rx Isolation ⁶ ANT to RX_CEL ANT to RX_GSM ANT to RX_DCS ANT to RX_PCS	ISO_ANT_RX_CEL ISO_ANT_RX_GSM ISO_ANT_RX_DCS ISO_ANT_RX_PCS	27 27 27 27 27	- - -	- - -	dB
Input VSWR	VSWR	-	-	2:1	
Power Out Change due to Coupling Variations (VSWR = $3:1$ @ ANT Port) ⁷	CPLV	-1.5	-	1.5	dB
Load Mismatch Stress (Ruggedness) ⁸	Rugg	No performance degradation o module damage			

Table 8. PCS Band Specifications (continued)

Characteristic	Symbol	Min	Тур	Max	Unit
Stability - Spurious Output ⁹	Stab	-	-	-36	dBm
Closed Loop Power Variation over Temperature ¹⁰	P _{out} _Temp Ambient 1 P _{out} = 3 dBm	-1.5 dB	-	1.5dB	dB
	P _{out} _Temp Ambient 2 P _{out} = 15 dBm	1.0	-	1.0 dB	dB
	P _{out} _Temp Ambient 3 P _{out} = 30 dBm	dB	-	0.5	dB
		-0.5 dB		dB	

1 Power output must be monotonic with power detector voltage. (Vdd = 2.8V to 4.5V and Temperature = -20 to +70 °C)

- 2 Vdd = 2.8V to 4.5V and Temperature = -20 to +70 °C
- Measurement made from 50% of V_{APC} to 10%/90% of V_{DET OUT} final value, with pulsed V_{APC} with a peak voltage in the range 3 of 0.1 to 2.2 V and both rise and fall edges.
- 4
- Measured at antenna port, ISO1: P_{in} = -10 dBm, EN_ANT_TX = 0 V, V_{APC} = 0.1 V; ISO2: P_{in} = 11 dBm, EN_ANT_TX = 2.723 V, V_{APC} = 0.1. (Vdd = 2.8V to 4.5V and Temperature = -20 to +70 °C)
- 5 PCS RX band = 1930 to 1990 MHz. Vdd = 4.2 V, Saturated Pout.
- ⁶ Vdd = 2.8V to 4.5V and Temperature = -20 to +70 °C

 7 Maintaining constant Vdet-Vref at all mismatch phase angles. Measured at antenna port: P_{out} = 30 dBm

- ⁸ VSWR = 20:1 all phase angles, Vdd = 2.8V to 4.5V, Pin = 5dBm to 11dBm, Temperature = -20 to +70°C, Power less than or equal to 30 dBm
- 9 Output VSWR = 10:1 all phase angles, Vdd = 2.8V to 4.5V, Pin = 5dBm to 11dBm, Temperature = -20 to +70°C, Power less than or equal to 30dBm.
- ¹⁰ Vdiff_var = 20*LOG(Delta(T)/Delta(25°C)), where T is -20 to 65°C, Delta(T)= [Vdiff(T) with RF] [Vdiff(T) without RF], and Delta(25°C)= [Vdiff(25°C) with RF] - [Vdiff(25°C) without RF]



5 Signal Descriptions



Figure 2. Pin Out

Table 9. Contact Connections

Section	Signal	Description	Pin(s)
RF	TXIN_LB	TX Input (Low Bands), DC Blocked	6
	TXIN_HB	TX Input (High Bands), DC Blocked	4
	ANT	Antenna	21
	RX_GSM	RX Output (GSM Band)	23
	RX_DCS	RX Output (DCS Band)	27
	RX_CEL	RX Output (CEL Band)	25
	RX_PCS	RX Output (PCS Band)	29

NP

Signal Descriptions

Section	Signal	Description	Pin(s)
Supply	VDDB_LB	DC Supply (Battery) for Bias Control (Low Bands)	10
	VDD1_LB	DC Supply (Battery) for Pre-Driver (Low Bands)	
	VDD2_LB	DC Supply (Battery) for Driver Stage (Low Bands)	11
	VDD3_LB	DC Supply (Battery) for Final Stage (Low Bands)	12
	VDDB_HB	DC Supply (Battery) for Bias Control (High Bands)	34
	VDD1_HB	DC Supply (Battery) for Pre-Driver (High Bands)	
	VDD2_HB	DC Supply (Battery) for Driver Stage (High Bands)	33
	VDD3_HB	DC Supply (Battery) for Final Stage (High Bands)	32
	VREG28	DC Reference Supply (Regulated)	13
Control	VAPC	Analog Power Control	2
	LOWB_HIGH	Band Select Low (CEL, GSM)/High (DCS,PCS)	17
	EN_ANT_TX	Enable TX Antenna Switch Path	18
	EUB_US	Mode Select EU (GSM, DCS)/US (CEL, PCS)	19
	EN_DET_PA	Enable Detector and Power Amplifier ¹	16
Power Detection	VDET	Detected Output of Detector	15
	VREF	Reference Output of Detector	14
Ground	Gnd	Ground	3, 5, 7, 20, 22, 24, 26, 28, 31, 1, 9, 30

Table 9. Contact Connections (continued)

¹ The MMM6025 pinout is compatible with that of MMM6022, *except* pin 8.

MMM6025 signal EN_DET_PA (pin 16) is equivalent to a combination of both MMM6022 signals EN_TX and EN_DET.

EUB_US	LOWB_HIGH	EN_AN_TX	Mode	Band(s)
High	Low	High	ТХ	GSM850
Low	Low	High	ТХ	EGSM
Low	High	High	ТХ	DCS
High	High	High	ТХ	PCS
High	Low	Low	RX	GSM850
Low	Low	Low	RX	EGSM
Low	High	Low	RX	DCS
High	High	Low	RX	PCS

Table 10. Logic States





Figure 3 shows the top view of the MMM6025 demoboard application schematic.

Figure 3. Demoboard Application Schematic



Package Information

6 Package Information

Figure 4 shows the MMM6025 $9.85 \times 9.0 \times 1.4$ mm HDI package case outline. Figure 5 on page 15 shows the bottom view.









NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETERS
- 2. INTERPRET DIMENSIONS AND TOLERANCES PER ASME Y14.5M-994.
- 3. \oplus 0.1 \bigcirc A B C APPLIES TO ALL PAD LOCATIONS.

Figure 5. Package Outline—Bottom View



Product Documentation 7

This data sheet is labeled as a particular type: Product Preview, Advance Information, or Technical Data. Definitions of these types are available at: http://www.freescale.com on the documentation page.

Table 11 summarizes revisions to this document since the previous release (Rev. 5).

Table 11. Revision History

Location	Revision
Mulitple locations	Changed Case Temp to Ambient Temp. Added Tape and Reel and Lead Free information.

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Europe, Middle East, and Africa: Freescale Halbleiter Deutschland GmbH Technical Information Center Schatzbogen 7 81829 Muenchen, Germany +44 1296 380 456 (English) +46 8 52200080 (English) +49 89 92103 559 (German) +33 1 69 35 48 48 (French) support@freescale.com

Japan:

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