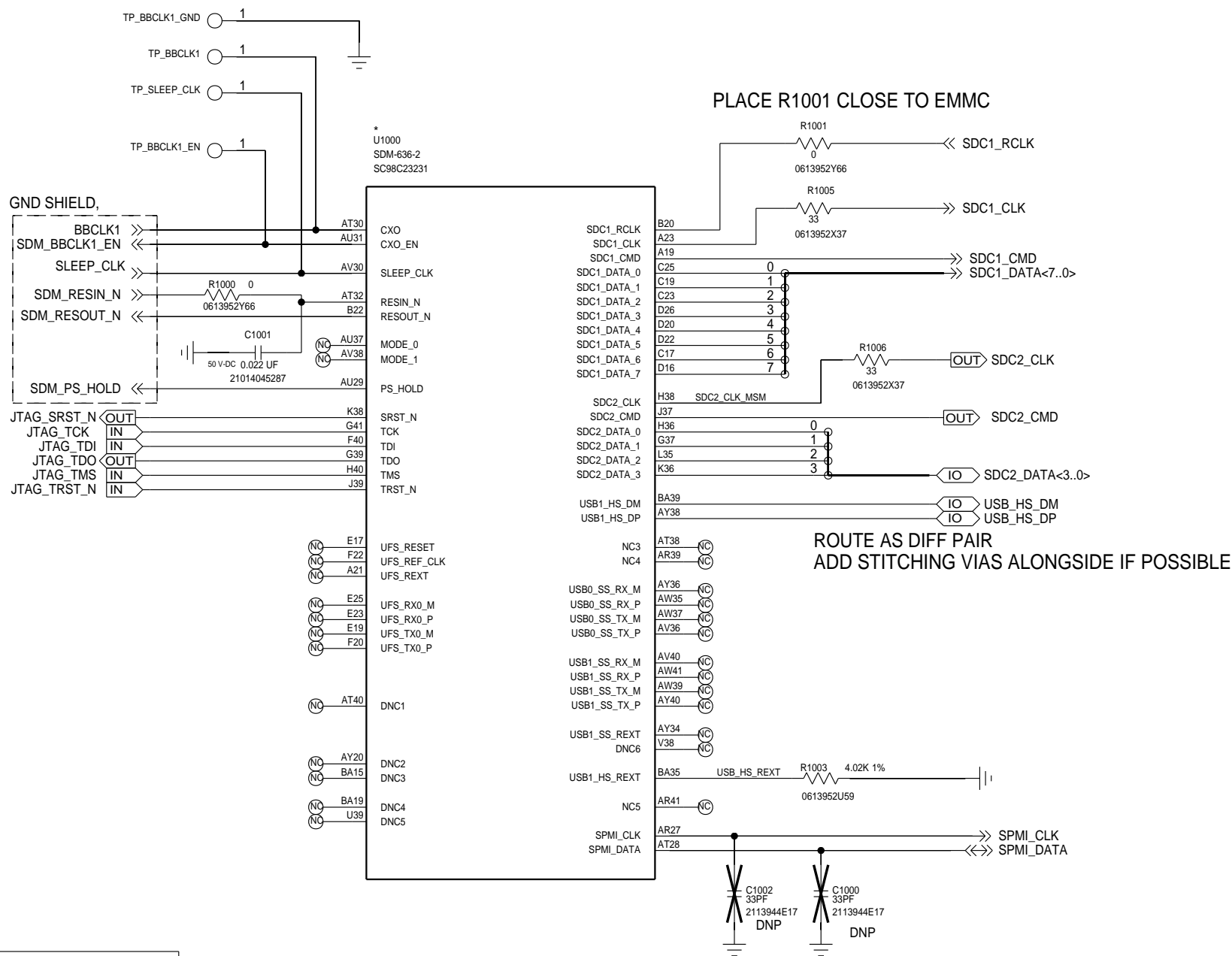


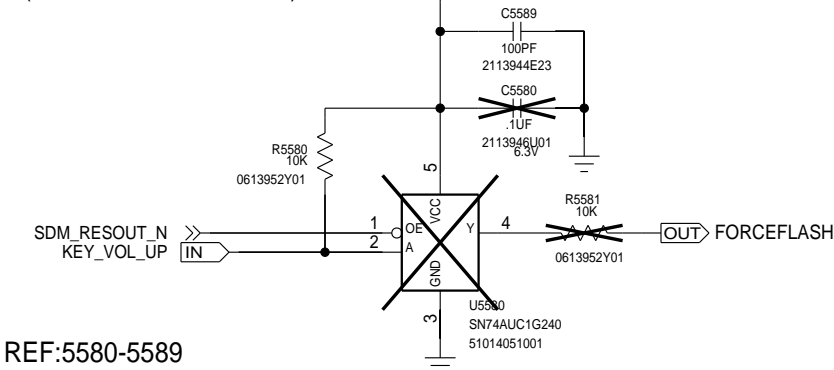
SDM: CTRL

REF 1000-1099



FORCE FLASH SUPPORT WITH VOL UP KEY

(DNI FOR PRODUCTION)



SDM: MIPI/DSI/CSI/EBI0

REF 1200-1299

D

D

C

C

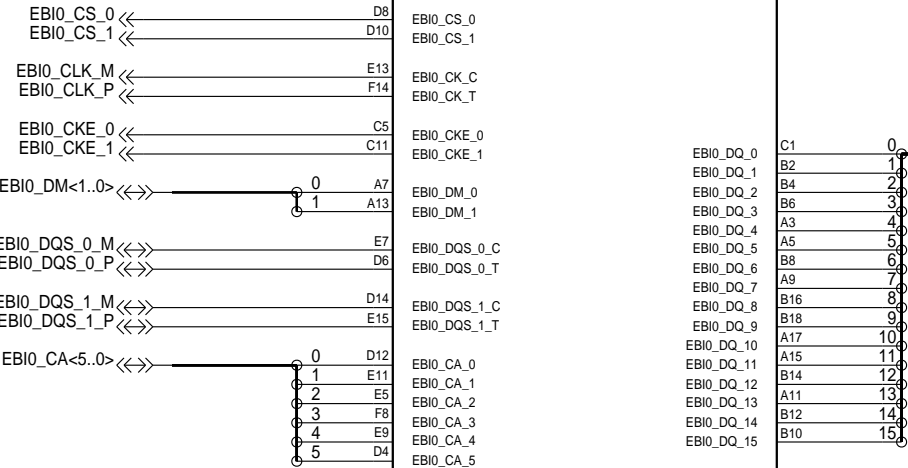
B

B

A

A

*
U1000
SDM-636-2
SC98C23231



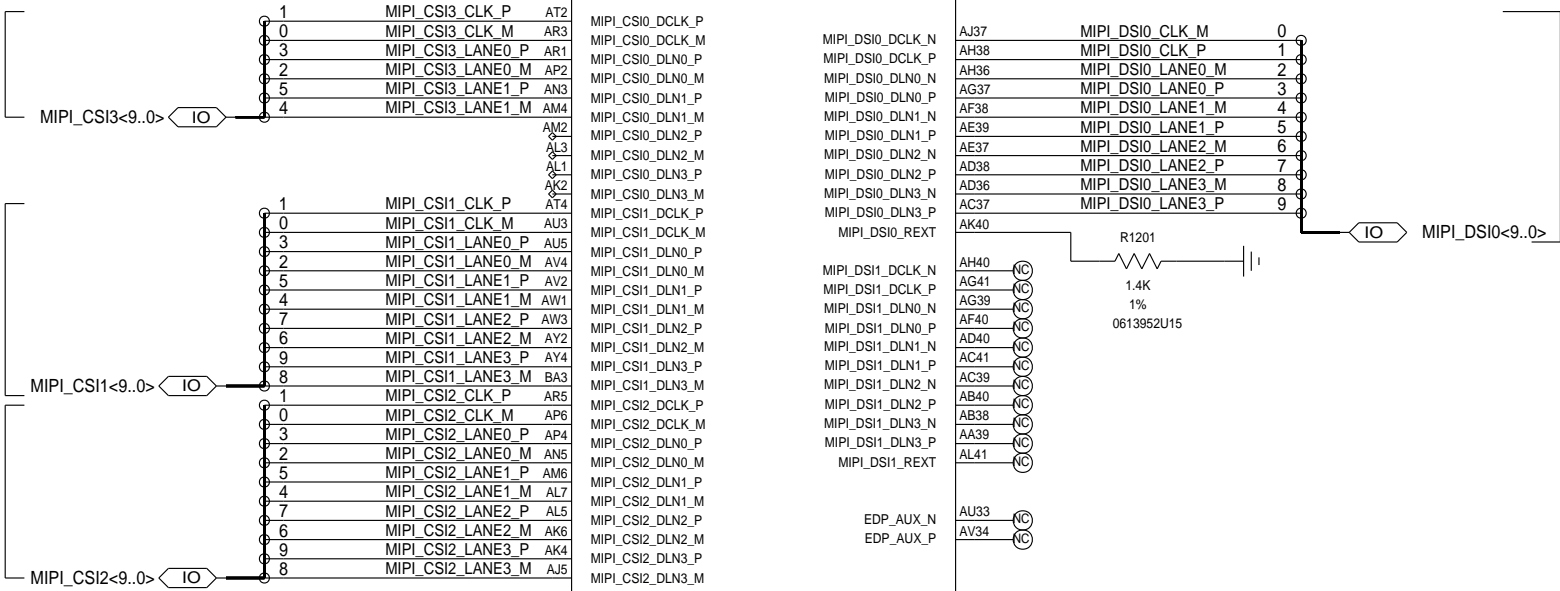
GND SHIELD FOR CSI3 AS A GROUP IF POSSIBLE

REAR AUX CAMERA

REAR CAMERA

FRONT CAMERA

*
U1000
SDM-636-2
SC98C23231



DISPLAY

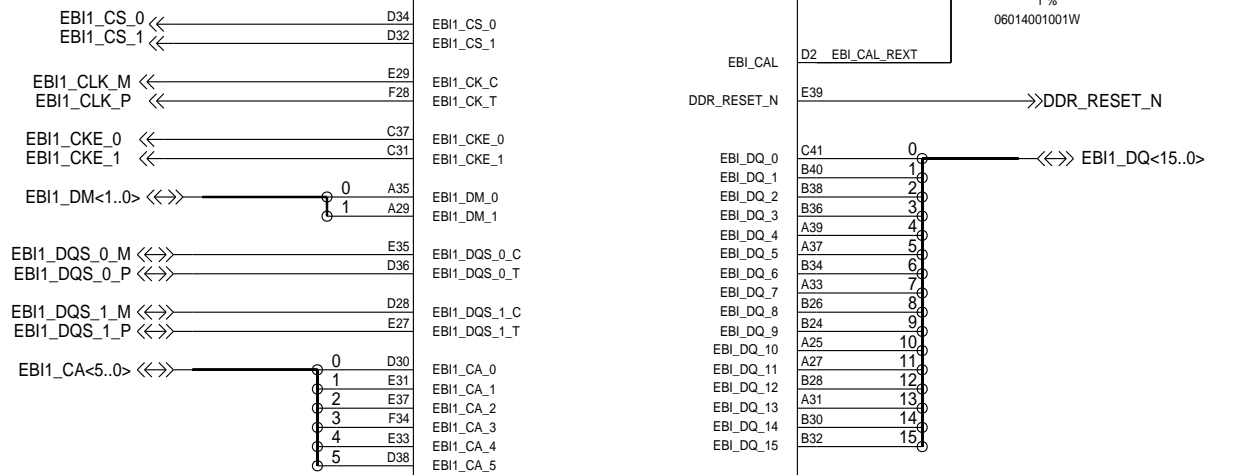
GND SHIELD FOR EBI0 AS A GROUP IF POSSIBLE

GND SHIELD FOR CSI2 AS A GROUP IF POSSIBLE
GND SHIELD FOR CSI1 AS A GROUP IF POSSIBLE

GND SHIELD FOR DSI0 AS A GROUP IF POSSIBLE

ADD GND STITCHING VIAS FOR EACH SIGNAL GROUP (EBI,CSI,DSI) WHEN POSSIBLE

*
U1000
SDM-636-2
SC98C23231



GND SHIELD FOR EBI1 AS A GROUP IF POSSIBLE

SDM: GPIO (0-141) REF 1200-1299

D

C

B

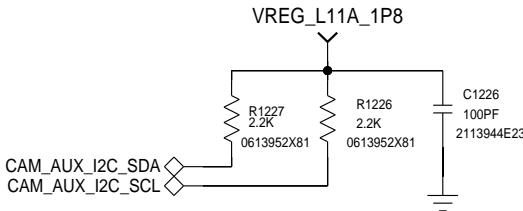
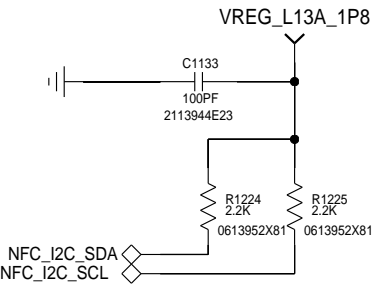
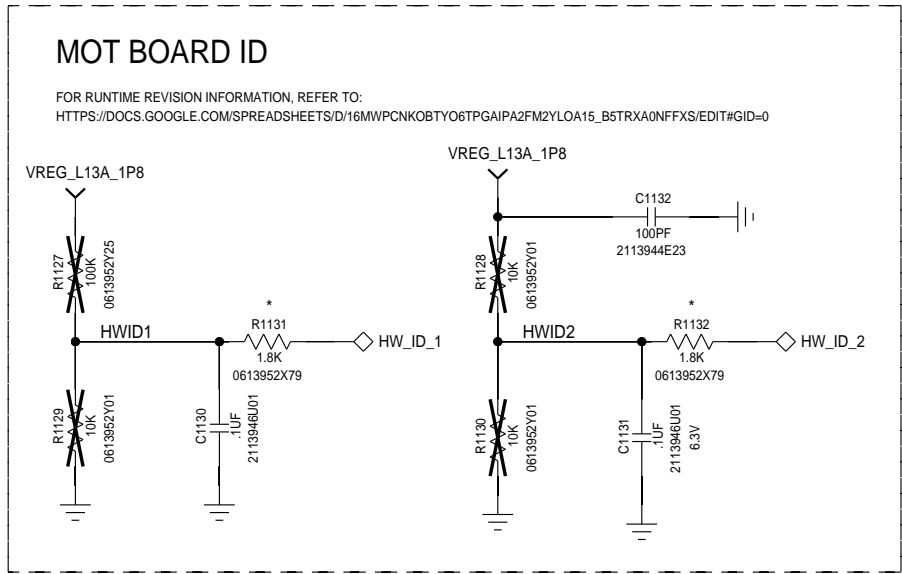
A

D

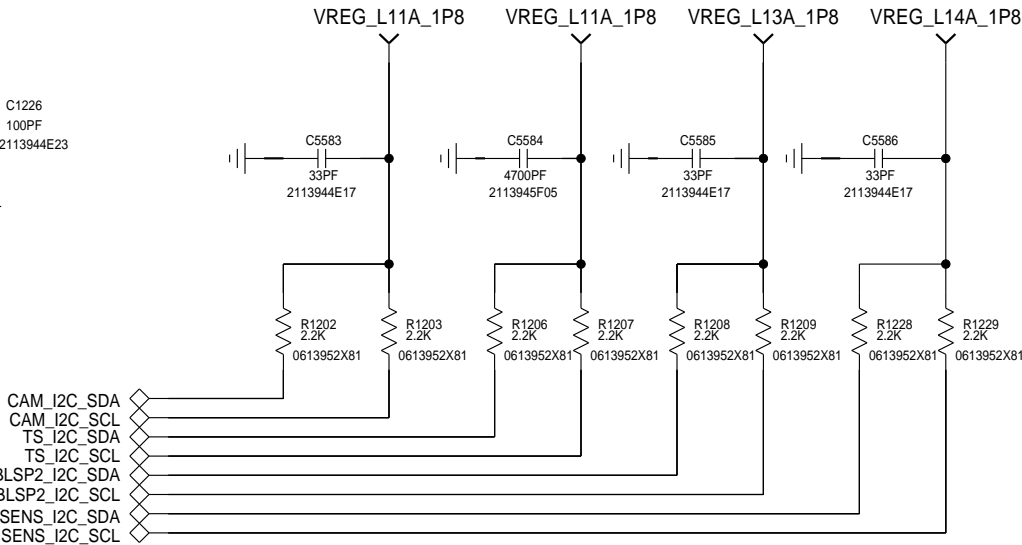
C

B

A

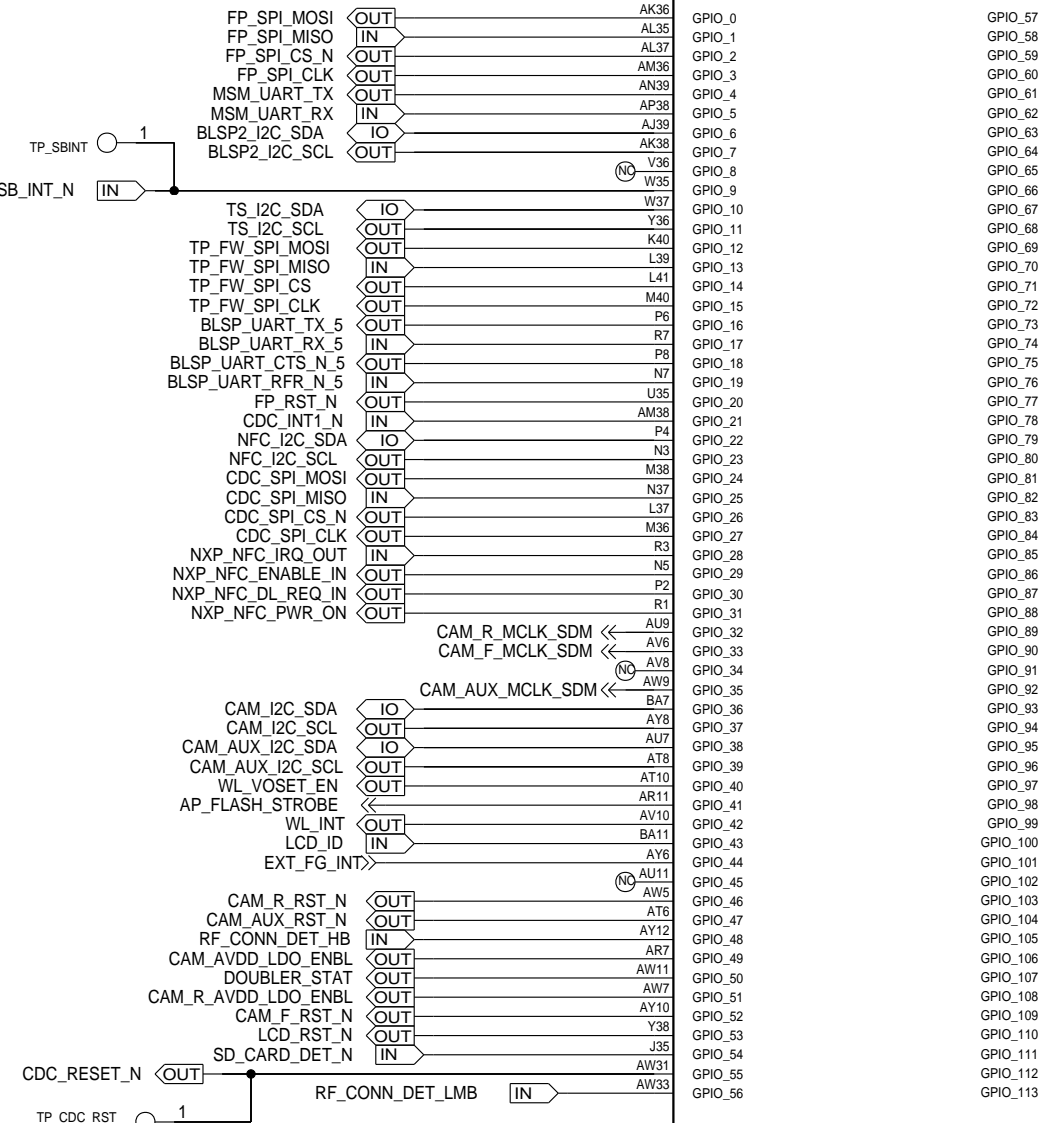


I2C2 ADDRESS	
DRV2624	0xBA,0XB5
AW8690	0XB6,0XB7
SAR9331	0X50,0X51
SAR9325	0X50,0X51
SMART PA TOP	0X82,0X83
SMART PA	0X80,0X81
BQ25970	0X65,0X67
RT5738	0XAC,0XAD
TPS61280	0XEA,0XEB
PM3003A	0XC0,0XC1
BQ27426	0XAA,0XAB



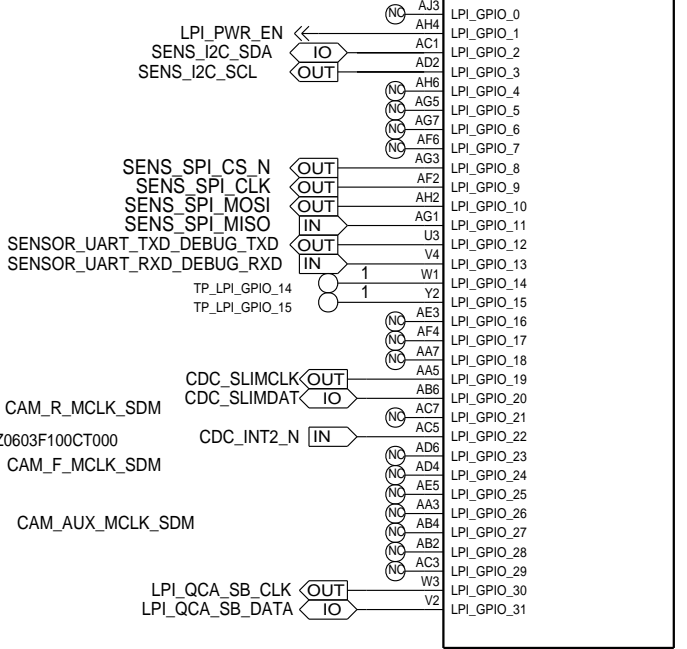
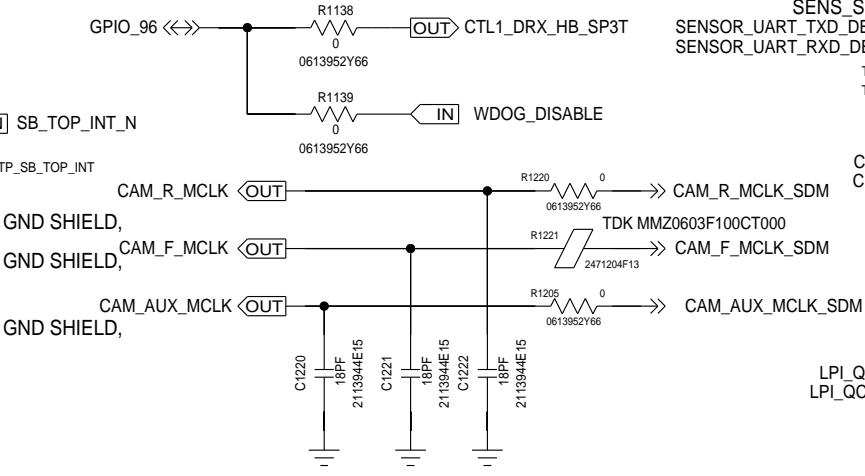
* U1000
SDM-636-2
SC98C23231

* U1000
SDM-636-2
SC98C23231



GPIO92,
GPIO93,
GPIO64,
GPIO63,
GPIO113,
GPIO53,
FOR SECURE BOOT, MAKE SURE NO PULLUP.

GPIO96,
GPIO97,
GPIO98,
GPIO60,
GPIO111,
FAST BOOT DEFAULT 0X00, MAKE SURE NO PULLUP.

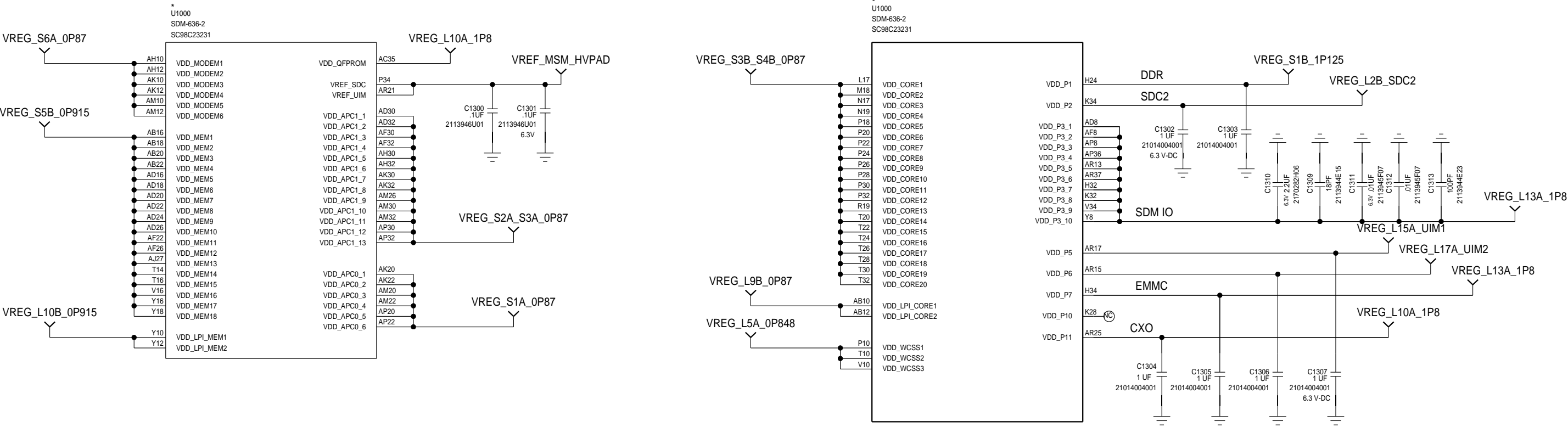


ENSURE THAT THERE ARE NO EXTERNAL PULLS ON THE GPIOs (92, 93, 64, 63,113, AND 53) IF SECURE BOOT IS NOT REQUIRED

SDM: PWR1

REF 1300-1399

CAD NOTE: PLACE ONE DECAP NEAR PINS V5, V6 AND
THE OTHER DECAP NEAR PIN R5

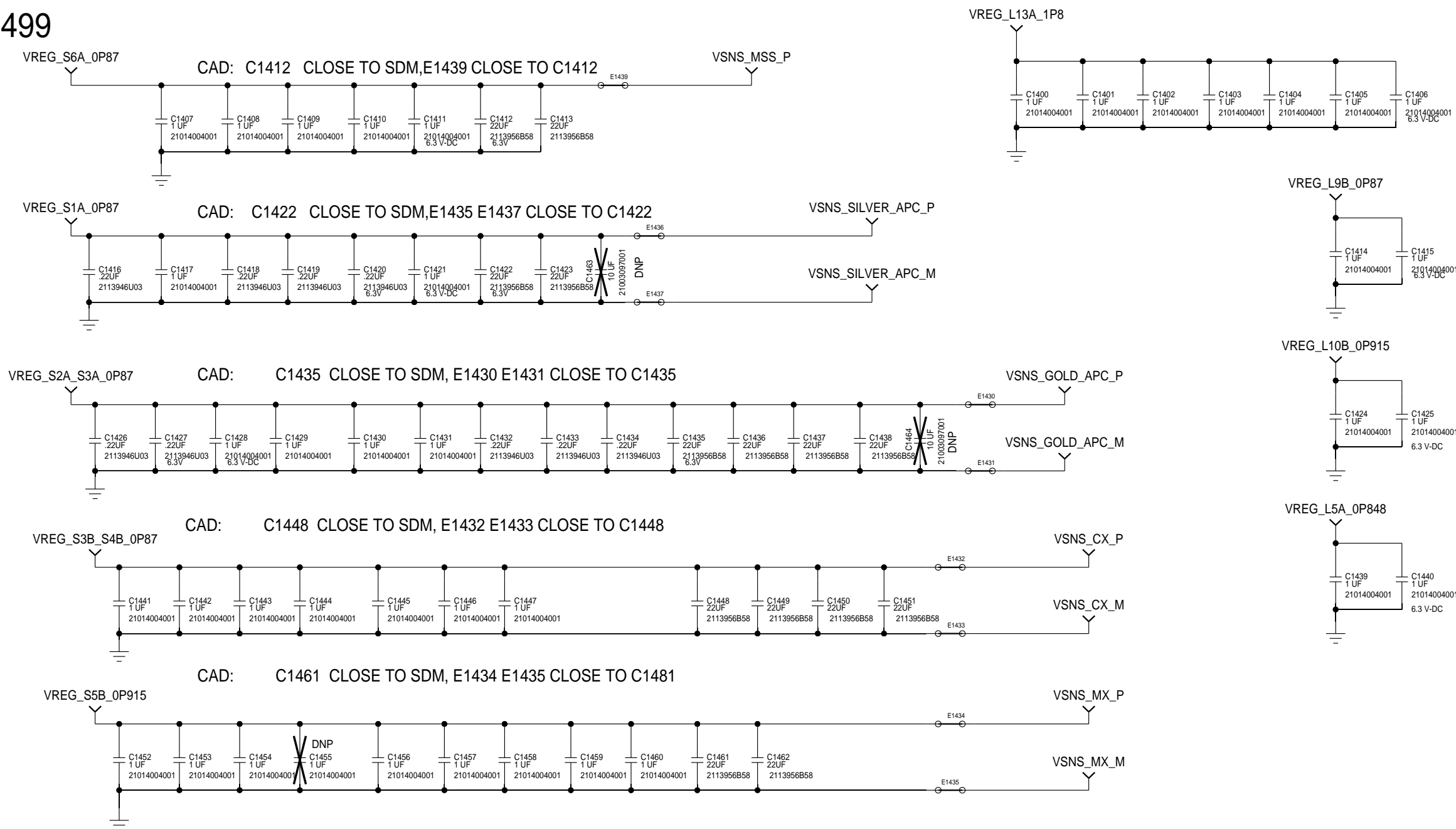


SDM: PWR2

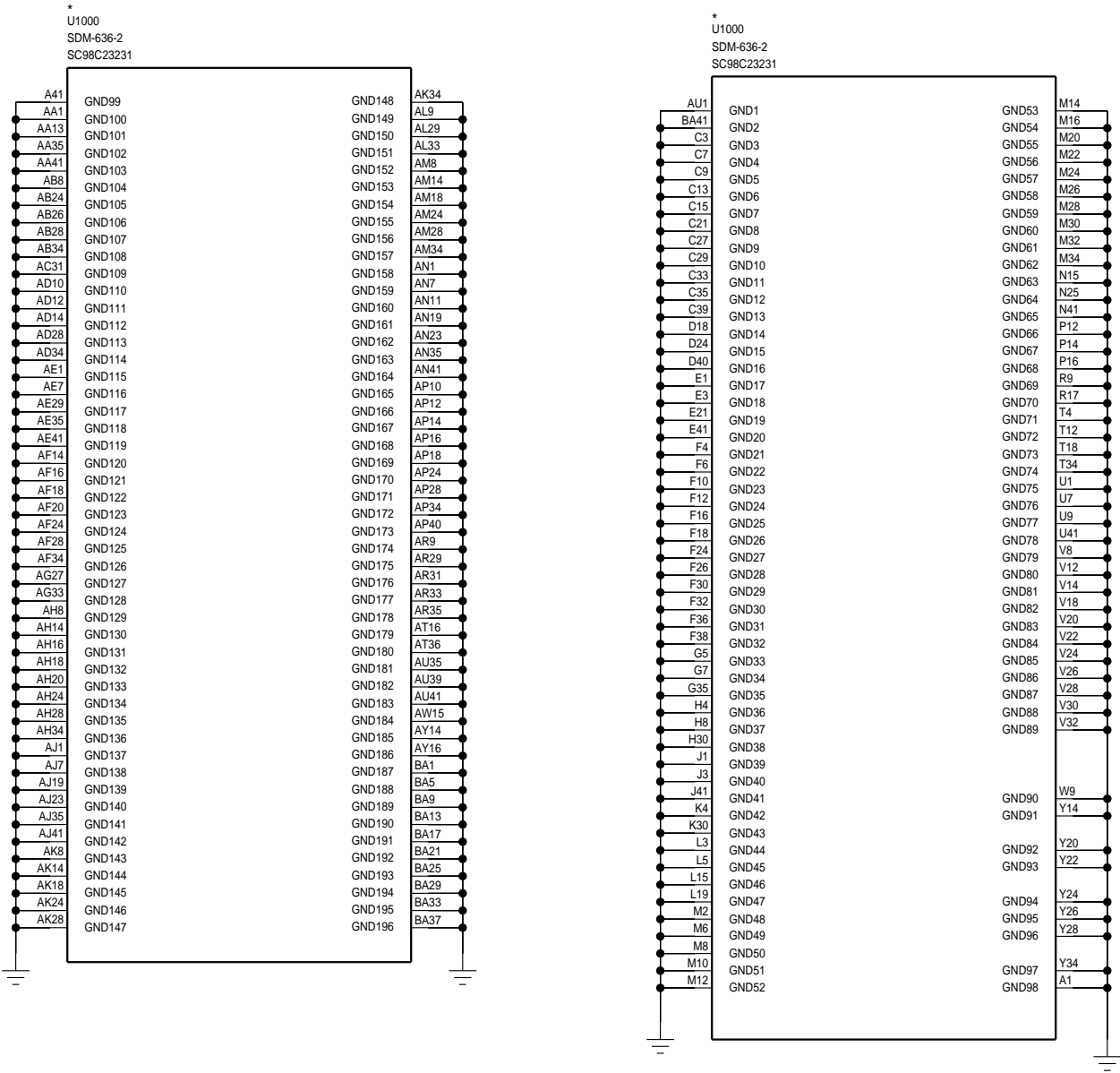
REF 1300-1399

SDM: BYPASS CAPS

REF 1400-1499

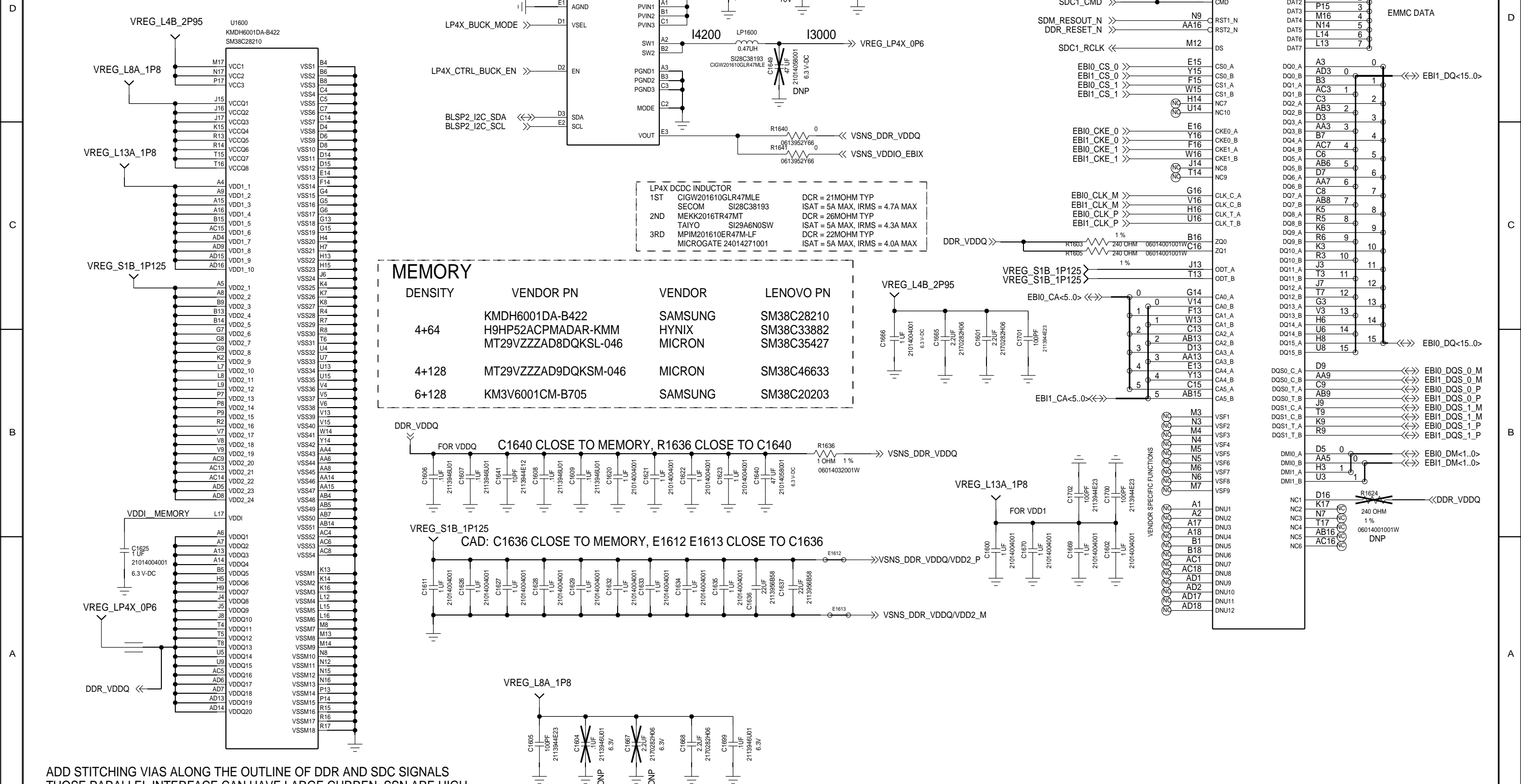


SDM: GROUND/DNC



MEMORY: EMCP

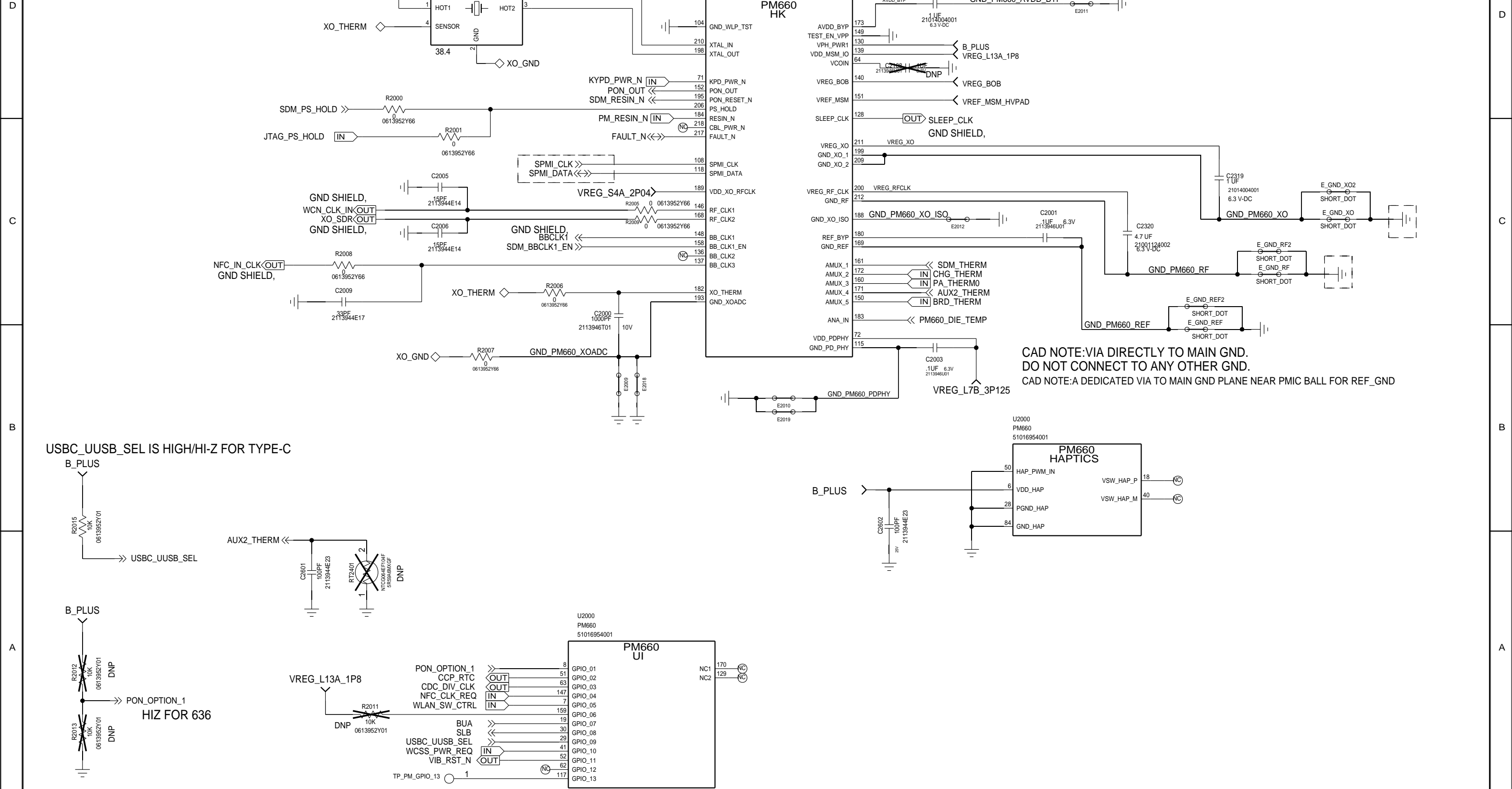
REF: 1600-1699



ADD STITCHING VIAS ALONG THE OUTLINE OF DDR AND SDC SIGNALS
THOSE PARALLEL INTERFACE CAN HAVE LARGE CURRENT, SSN ARE HIGH
MAY CAUSE RF DESNE IF RETURN CURRENT ARE NOT WELL LIMITED
AVOID ROUTING SENSITIVE SIGNAL CORSS THIS SECTION
SENSITIVE SIGNALS CAN BE CLOCKS, (CK, MCLK), ANALOG SIGNALS AND SO ON

PM660: HK/GPIO
REF: 2000-2099

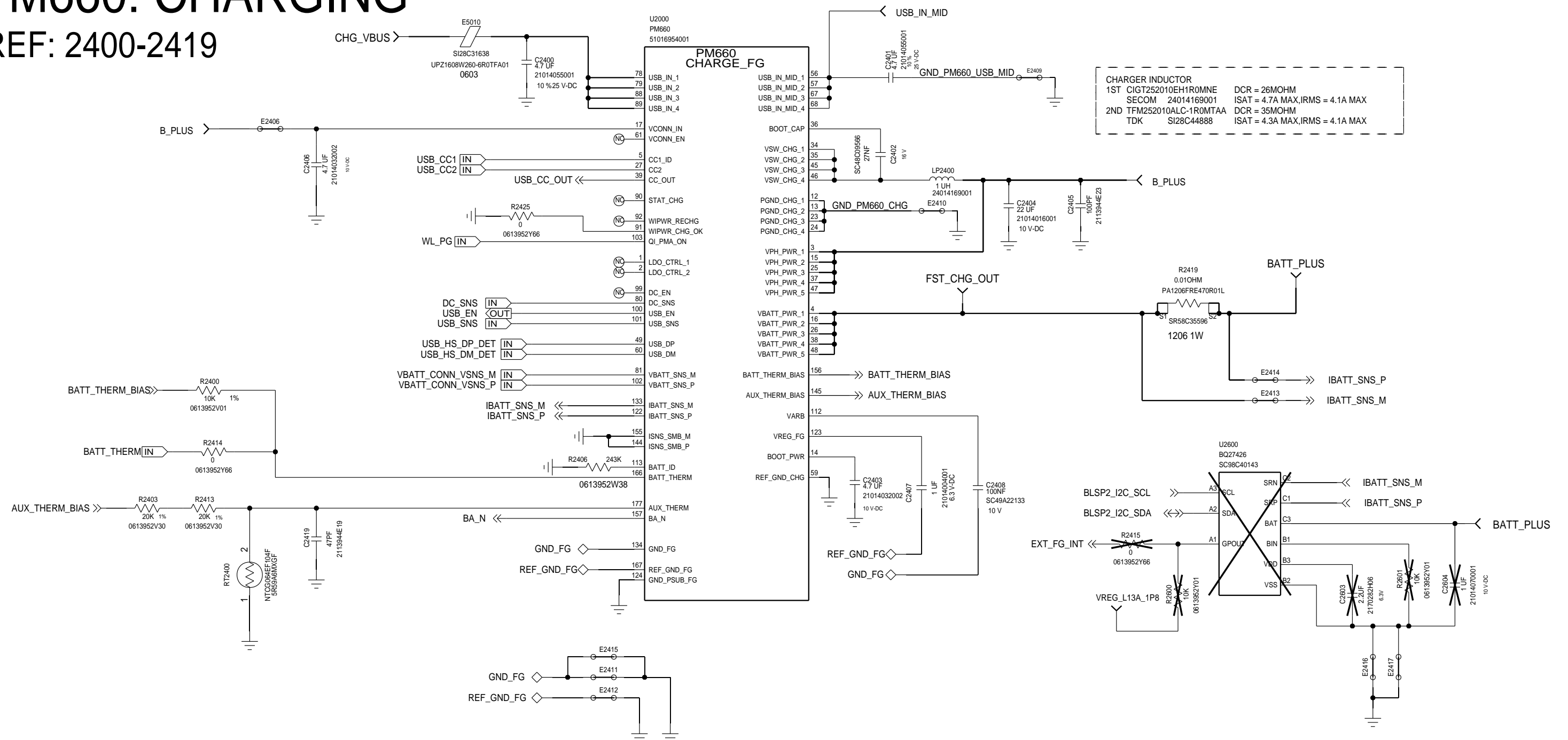
XTAL_IN, AND XTAL_OUT ROUTED ON 1ST LAYER, NOT AS DIFFERENT PAIR. REFER TO 80-P7747-3 REV. B FOR CRYSTAL LAYOUT GUIDE



D

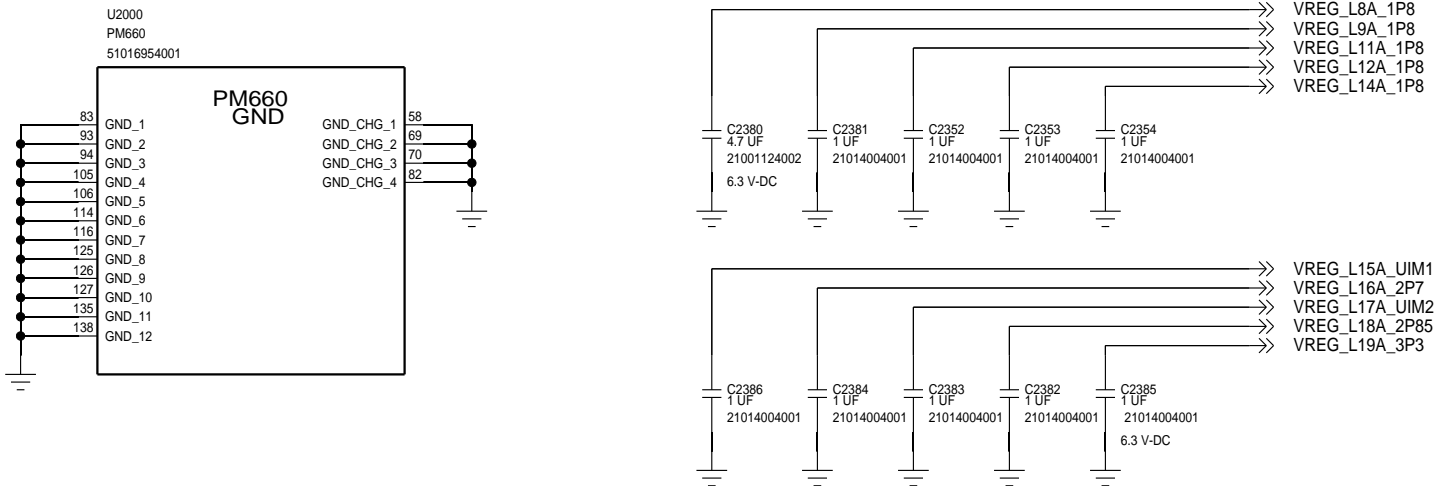
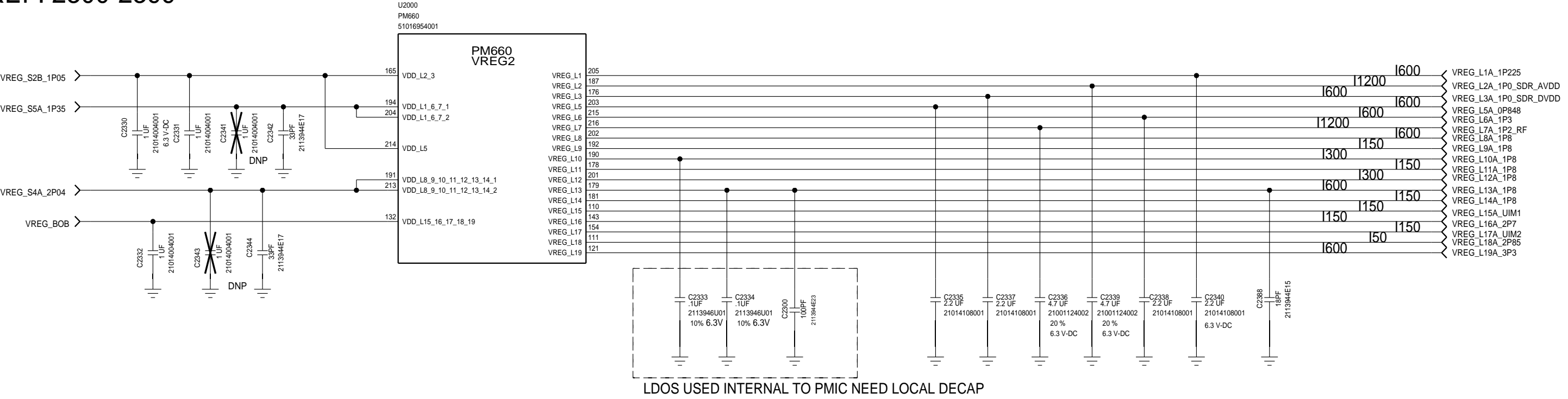


REF: 2400-2419



PM660: VREGS

REF: 2300-2399

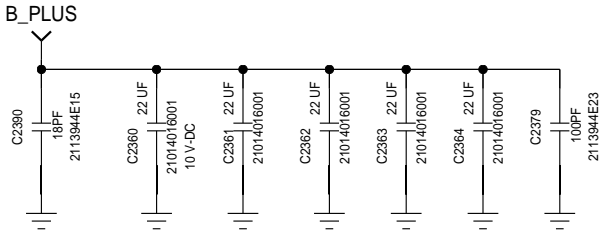
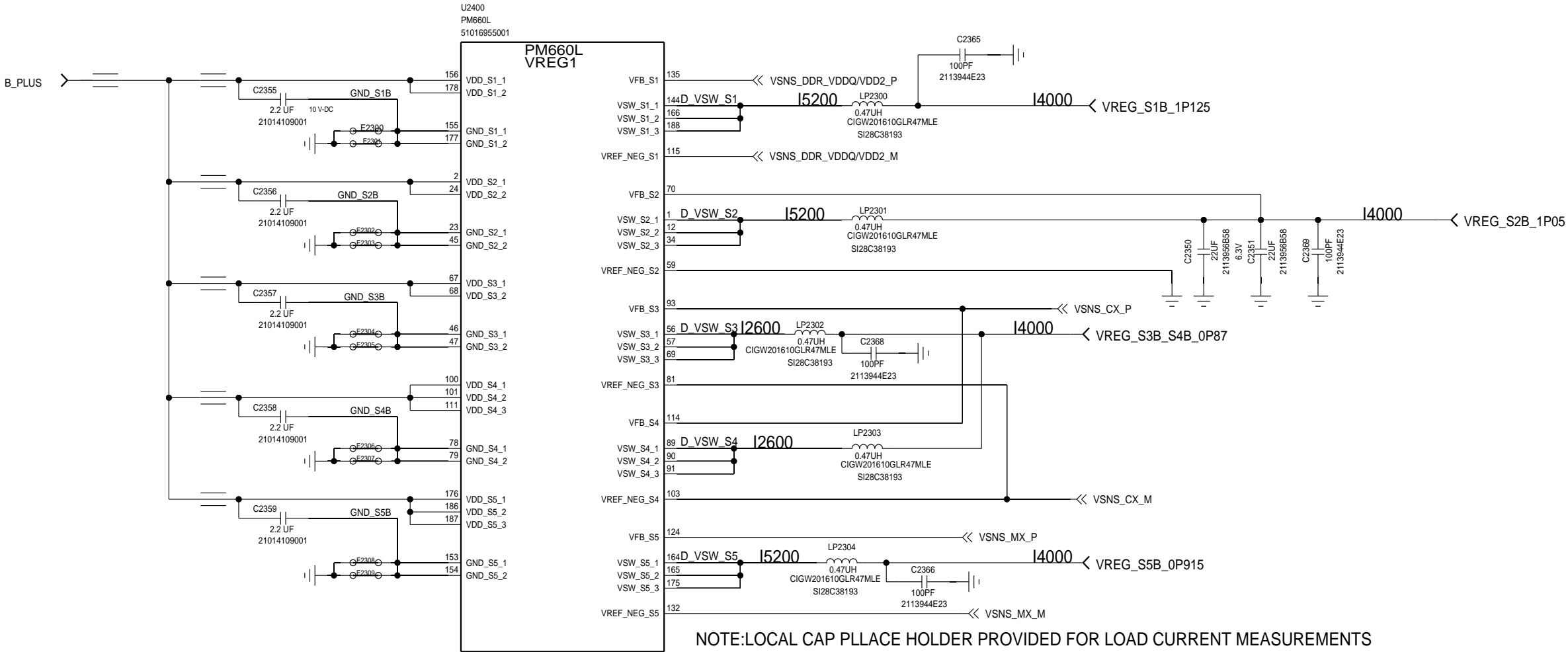


PSEUDO CAPLESS LODS
FOR CAPLESS LODS:IF DECAPS ON THE LOAD SIDE DO NOT ADD UP TO LDO SPEC, THEN INSTALL THE CAP CLOSE TO THE PMIC

PM660L: SMPS

REF: 2300-2399

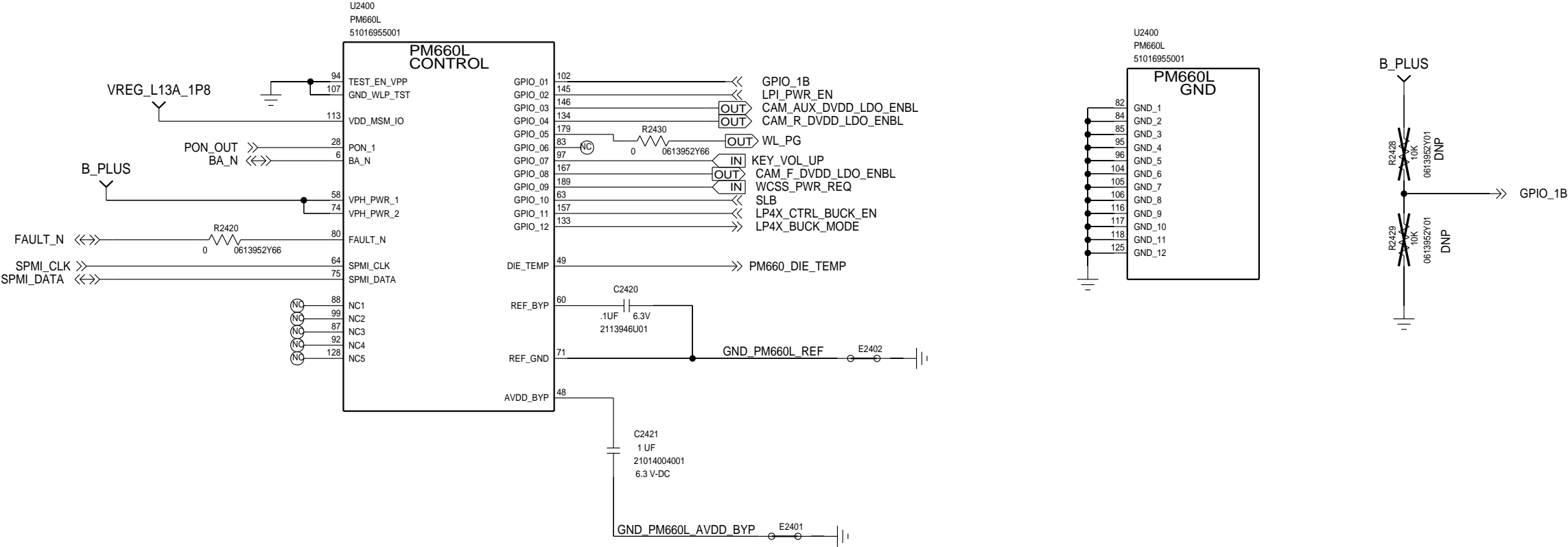
S1B-S5B INDUCTOR		
1ST	CIGW201610GLR47MLE	DCR = 21MOHM TYP
	SECOM SI28C38193	ISAT = 5A MAX, IRMS = 4.7A MAX
2ND	MEKK2016TR47MT	DCR = 26MOHM TYP
	TAIYO SI29A6N0SW	ISAT = 5A MAX, IRMS = 4.3A MAX
3RD	MPIM201610ER47M-LF	DCR = 22MOHM TYP
	MICROGATE 24014271001	ISAT = 5A MAX, IRMS = 4.0A MAX



BULK CAPS. DISTRIBUTE THESE ALL OVER U2400

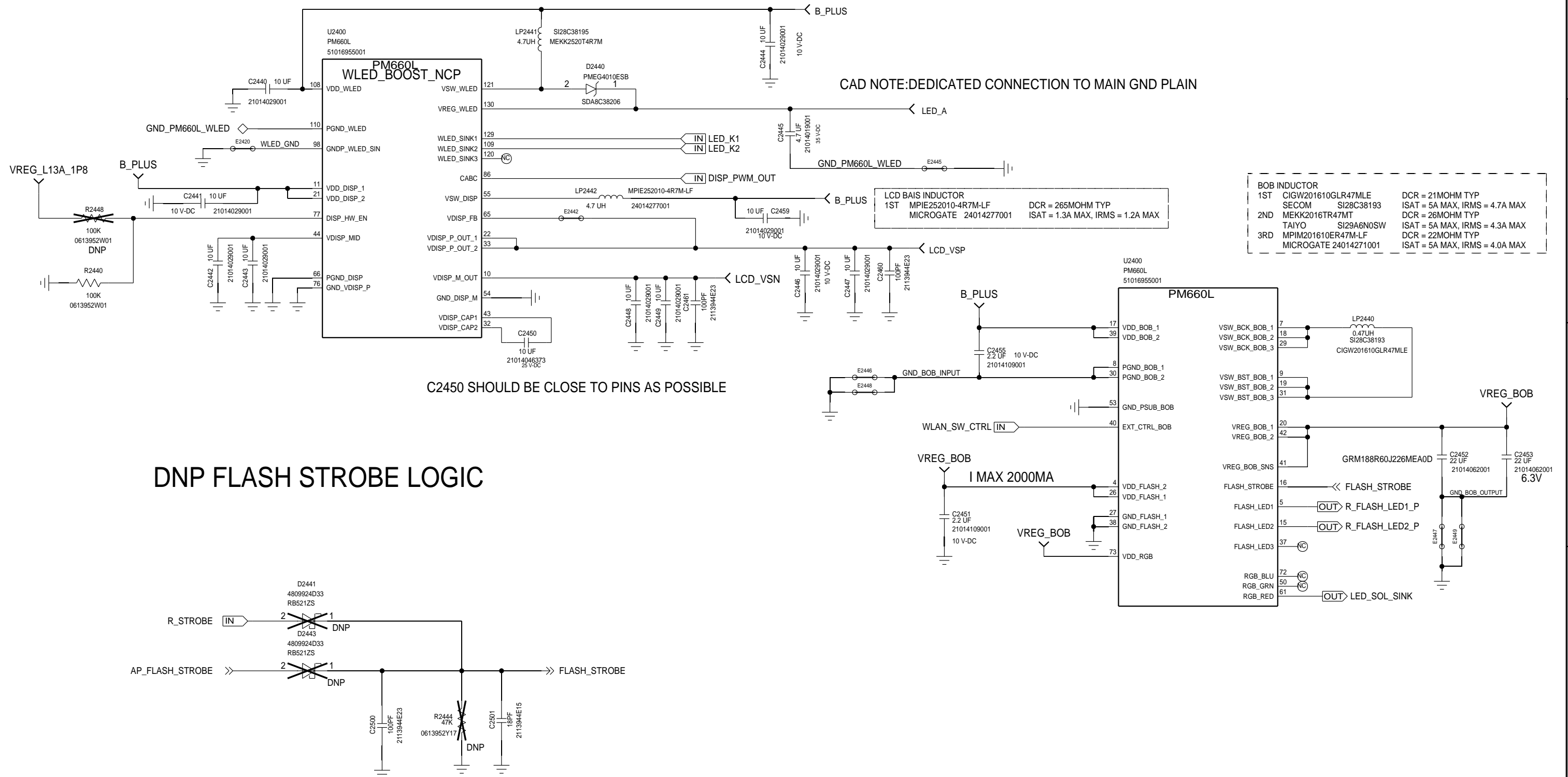
PM660L: CONTROL

REF: 2420-2439



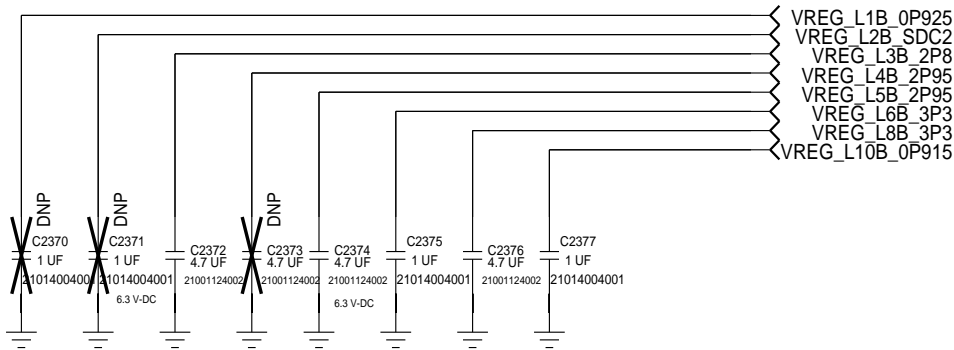
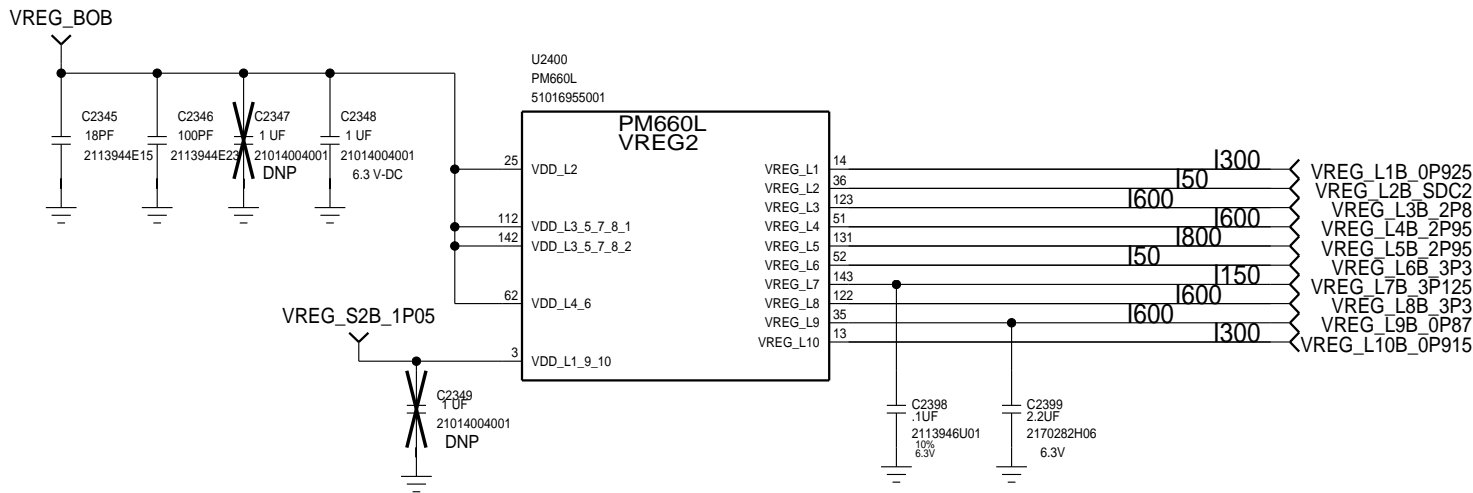
GND SHIELDED IF POSSIBLE, ROUTE DIFFERENTIALLY AND SPACING WITH 2X LW

REF: 2440-2459



PM660L: VREGS

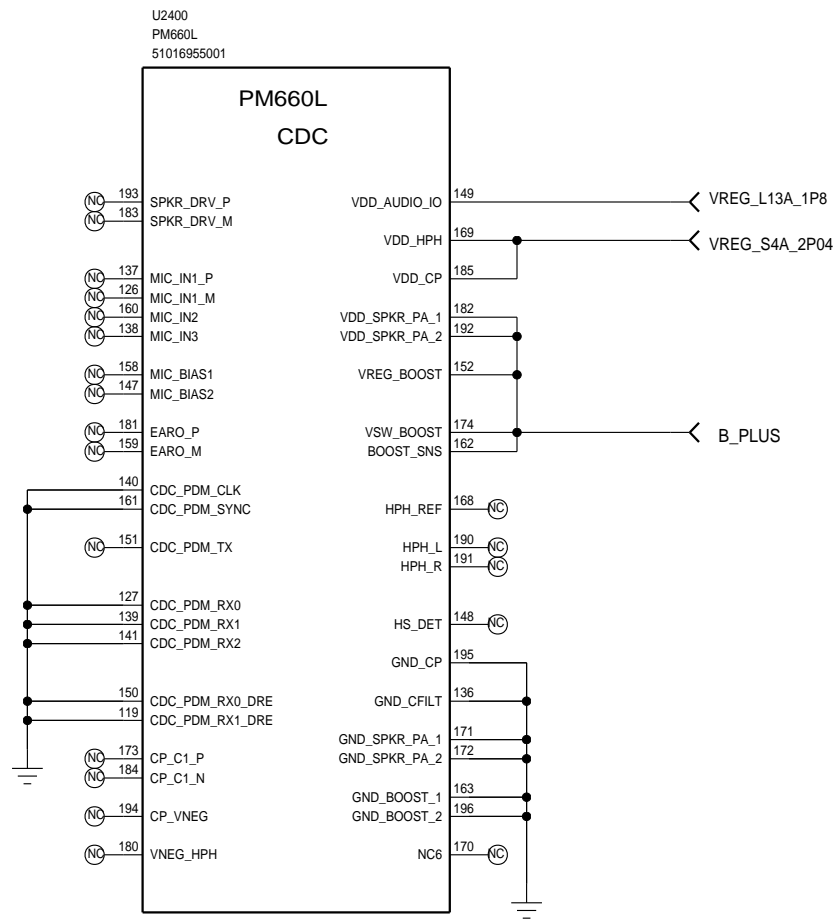
REF: 2300-2399



PSEUDO CAPLESS LODS
FOR CAPLESS LODS:IF DECAPS ON THE LOAD SIDE DO NOT ADD UP TO LDO SPEC, THEN INSTALL THE CAP CLOSE TO THE PMIC

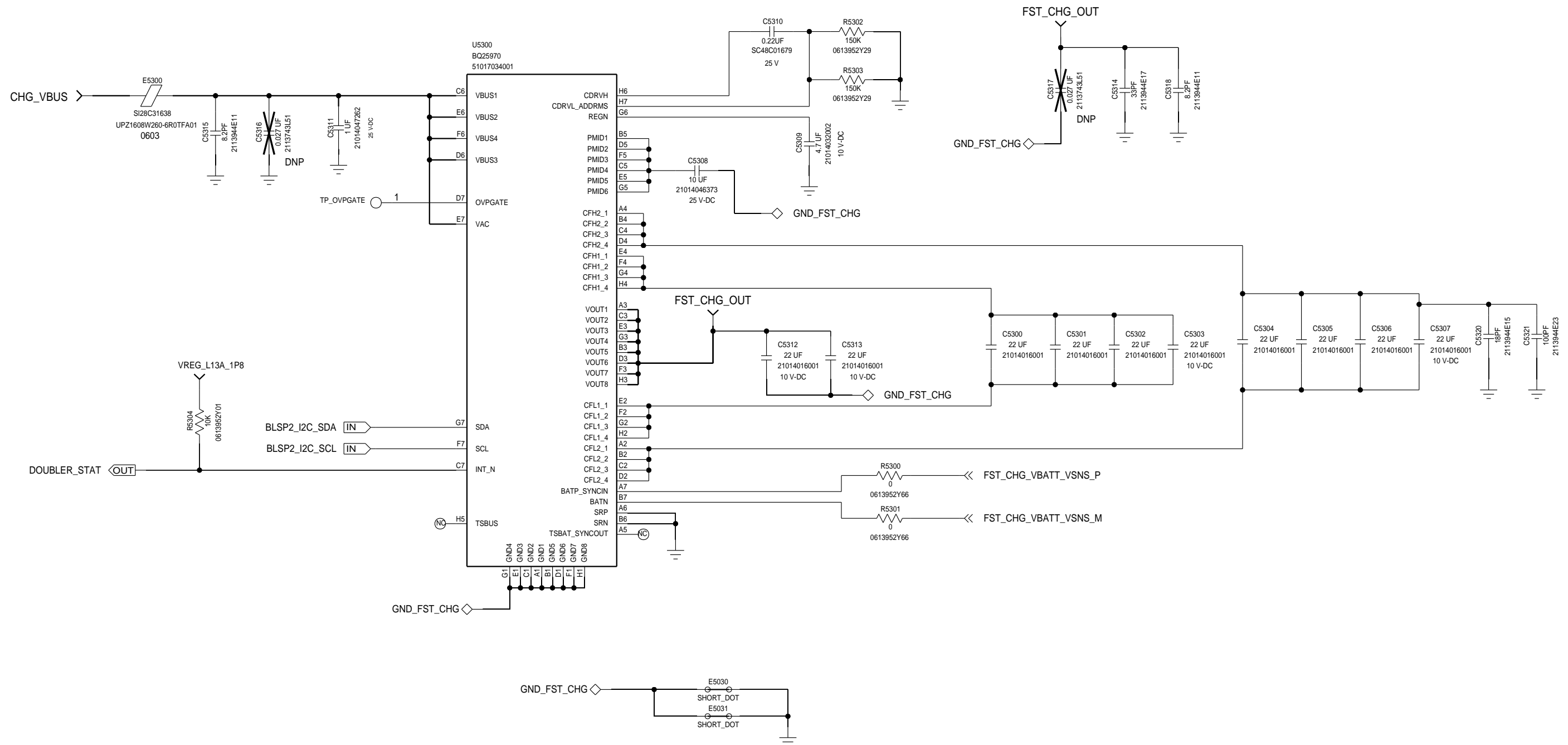
PM660L: CODEC

REF 2500-2579



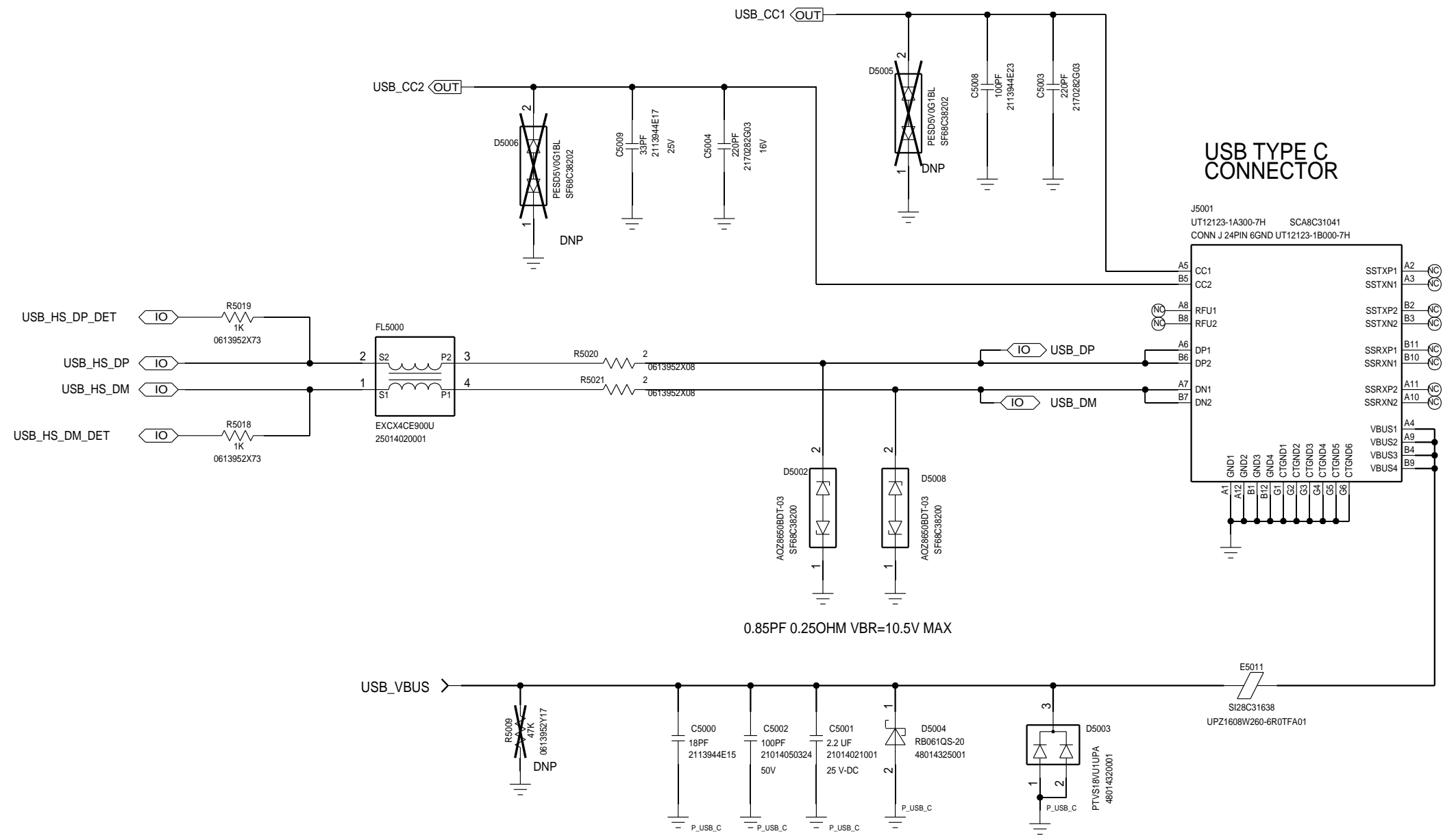
PWR: FAST CHARGER

REF:5300 - 5399



PWR: USB-C

REF:5300 - 5399



PWR: BATT

REF:510 - 550

BATT_THERMISTOR_10KOHM
BETA < 3900

BATT_THERM

OUT

TP_BATT_THERM

IN

E512

SHORT_DOT

D511
PESD5V0G1BL
SF68C38202

11PF 0.2OHM VBR=7.8V MAX

DNP

C510

33PF

2113944E17

CLOSE TO CONN

RTH+

BATTERY BLOCK

USE PMI INTERNAL SENSE

BATT_PLUS

C512

8.2PF

2113944E11

D510

ESD56201D04-2_TR
SF68C38204

C513

100PF

2113944E23

C514

10 UF

21014029001

10 V-DC

G3

P510

28014040001

NO

4

P510

28014040001

2

P510

28014040001

G2

P510

28014040001

P510

G4

28014040001

3

P510

28014040001

1

P510

28014040001

G1

P510

28014040001

CLOSE TO CONN

C515

33PF

2113944E17

C516

33PF

2113944E17

D512

VS3V3BB1EST15R
48014232001

10PF 0.1OHM VBR=6.7V MAX

OUT

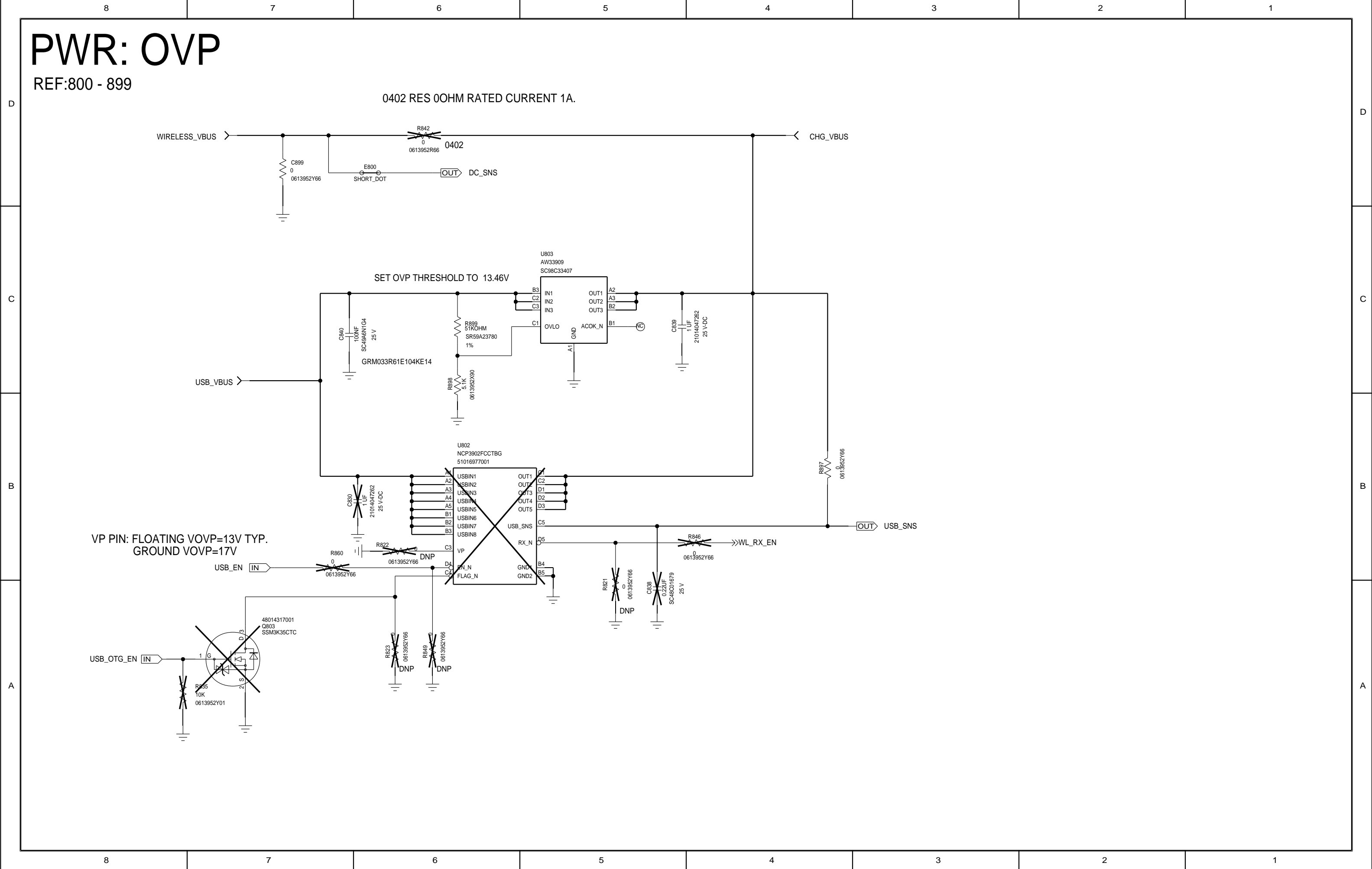
VBATT_CONN_VSNS_P

OUT

VBATT_CONN_VSNS_M

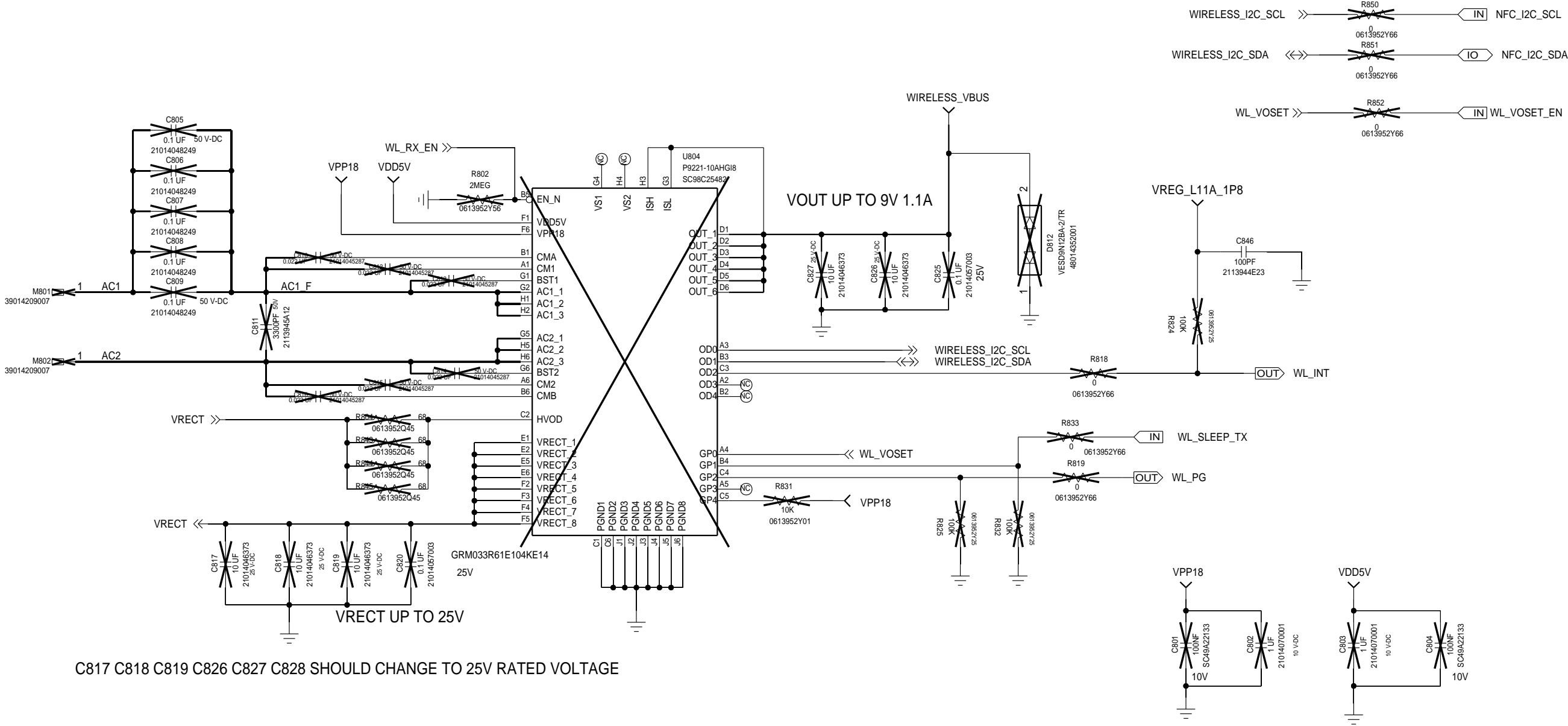
1

FST_CHG_VBATT_VSNS_P

[illegible][illegible]

WIRELESS CHARGER

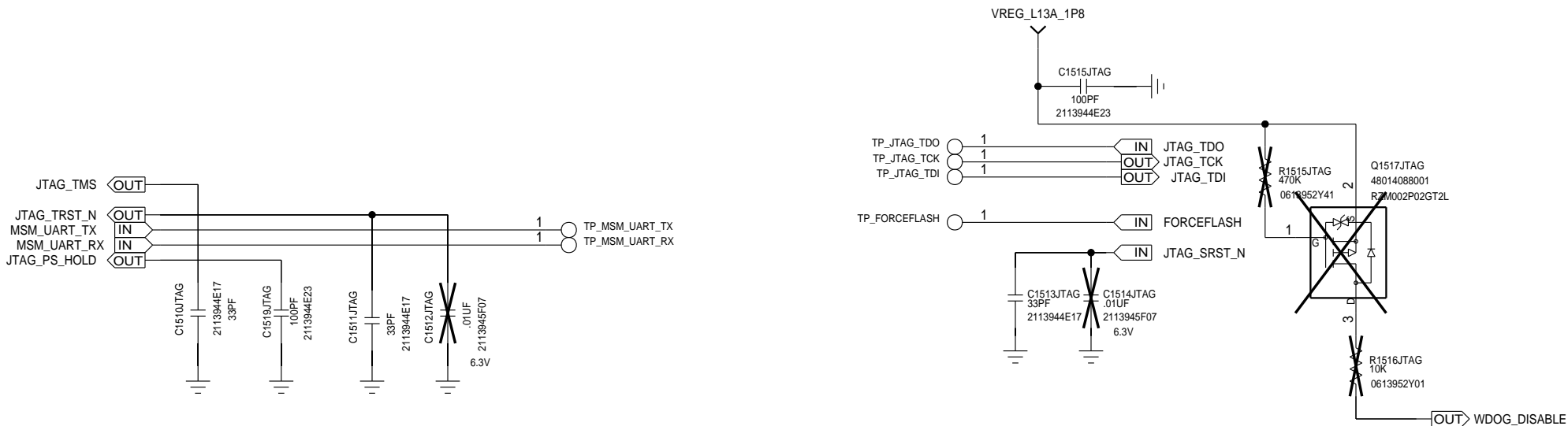
REF:800 - 899



C817 C818 C819 C826 C827 C828 SHOULD CHANGE TO 25V RATED VOLTAGE

DEBUG PORT

REF 1000-1099



D

C

A

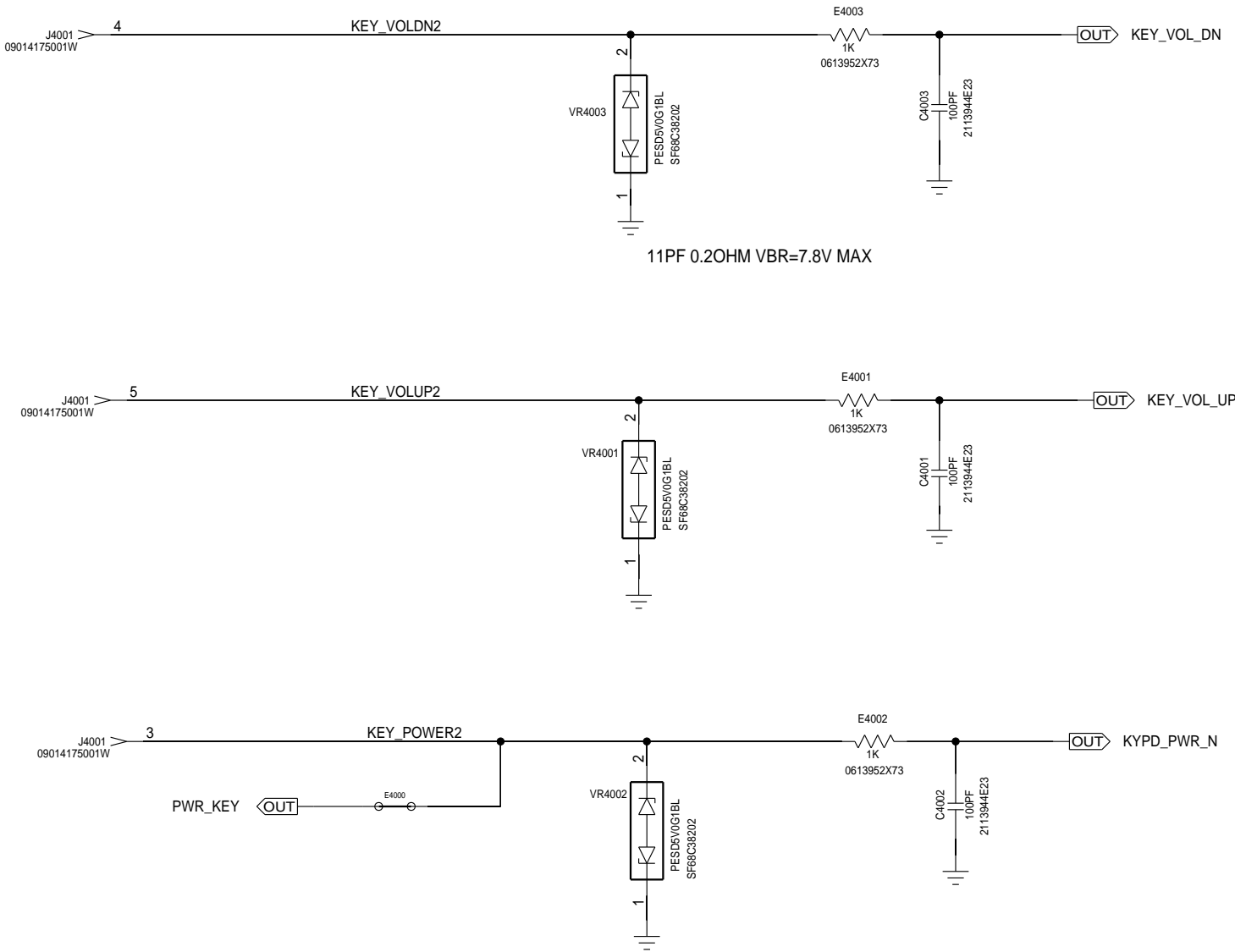
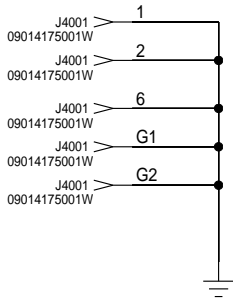


UI: SIDE KEY

REF:4000-4199

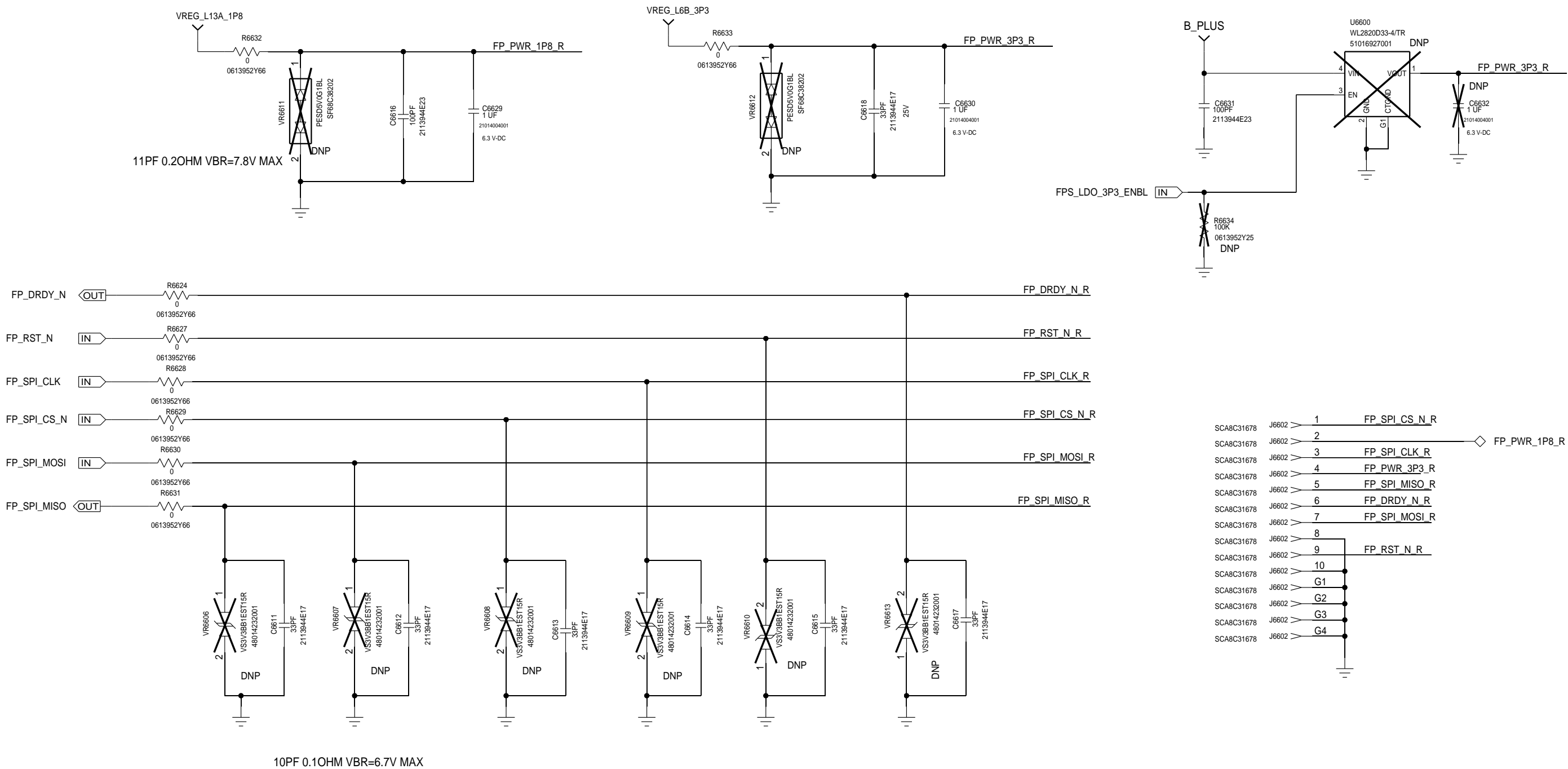
MECHANICAL KEYS

VOL_UP/DOWN/GND TPS NEED P0 BRINGUP ONLY



UI: FPS

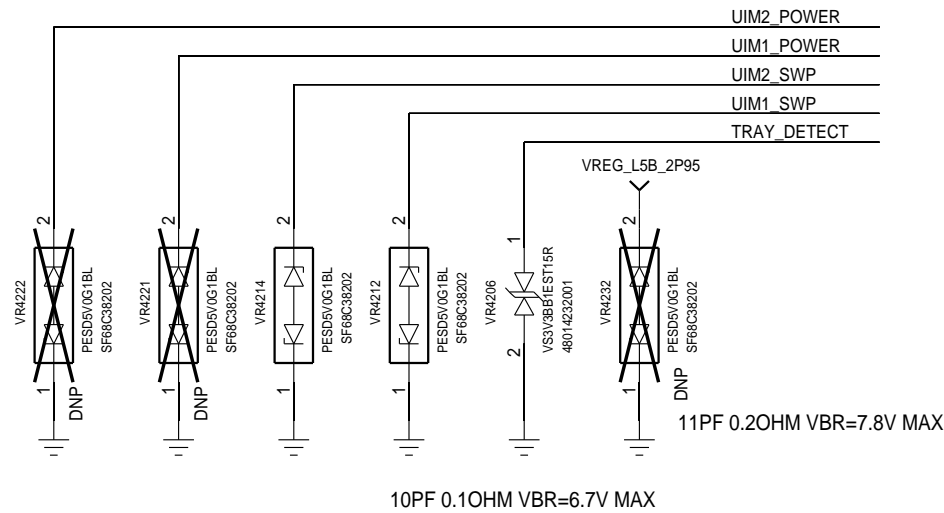
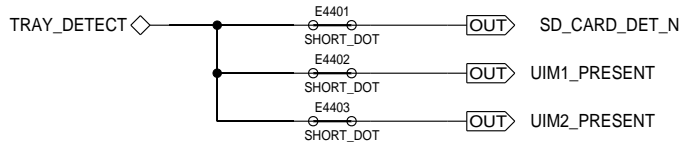
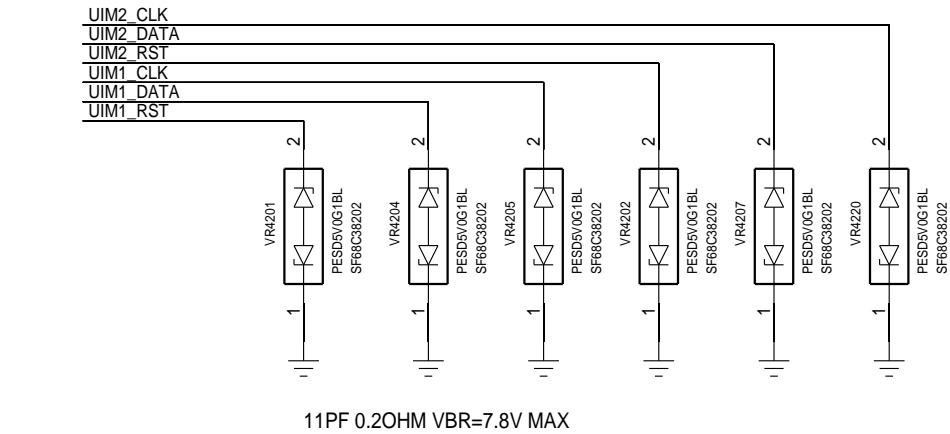
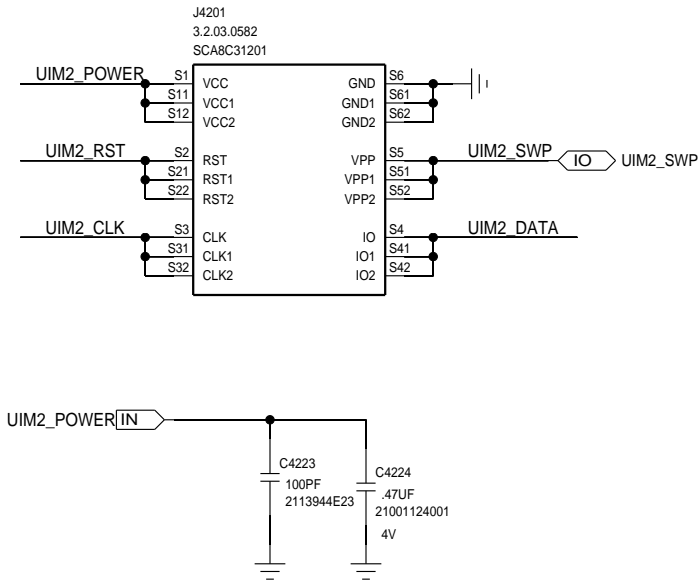
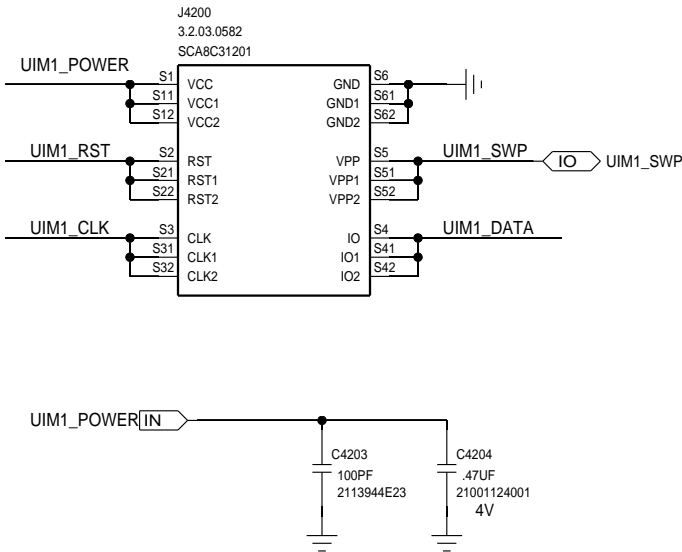
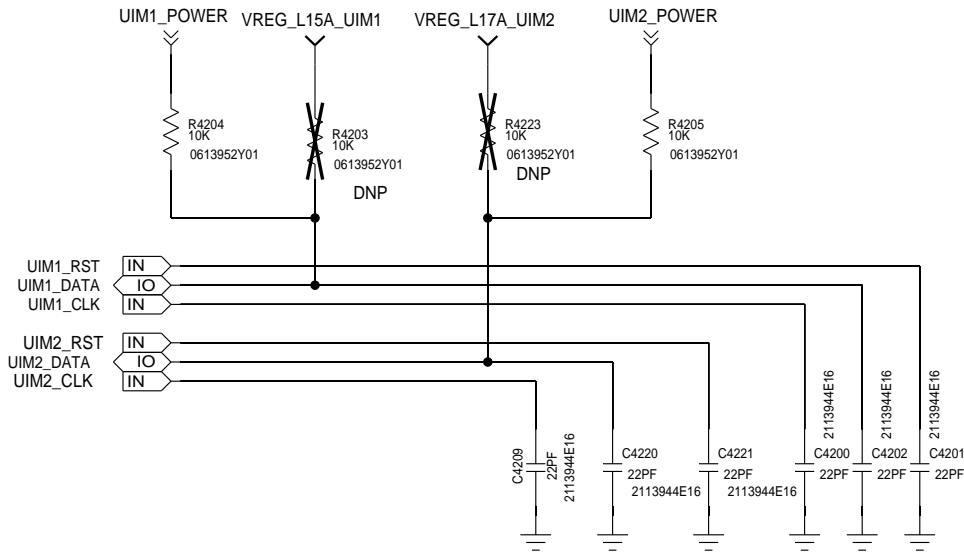
REF: 6600 - 6699



SCA8C31678	J6602 > 1	FP_SPI_CS_N_R	
SCA8C31678	J6602 > 2	FP_PWR_1P8_R	
SCA8C31678	J6602 > 3	FP_SPI_CLK_R	
SCA8C31678	J6602 > 4	FP_PWR_3P3_R	
SCA8C31678	J6602 > 5	FP_SPI_MISO_R	
SCA8C31678	J6602 > 6	FP_DRDY_N_R	
SCA8C31678	J6602 > 7	FP_SPI_MOSI_R	
SCA8C31678	J6602 > 8		
SCA8C31678	J6602 > 9	FP_RST_N_R	
SCA8C31678	J6602 > 10		
SCA8C31678	J6602 > G1		
SCA8C31678	J6602 > G2		
SCA8C31678	J6602 > G3		
SCA8C31678	J6602 > G4		

UI: UIM

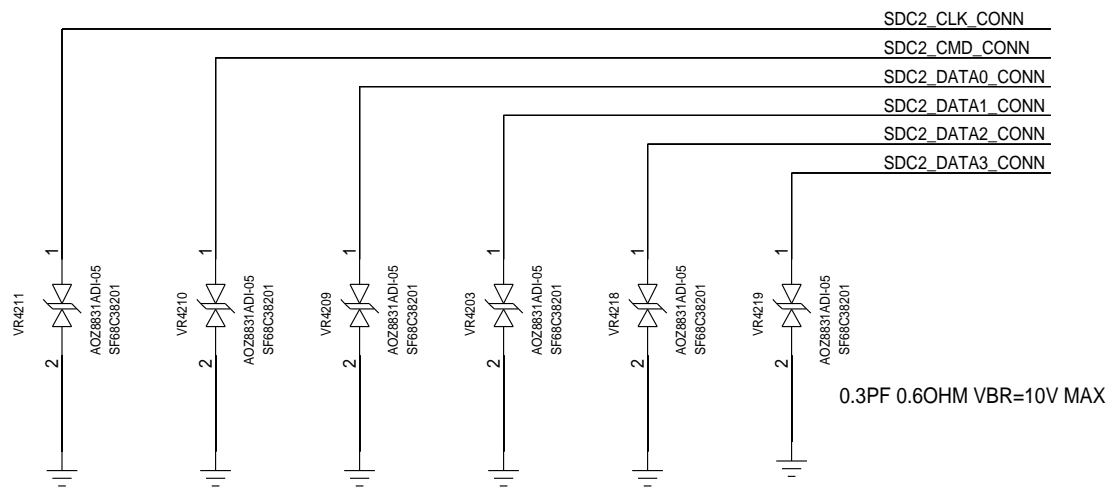
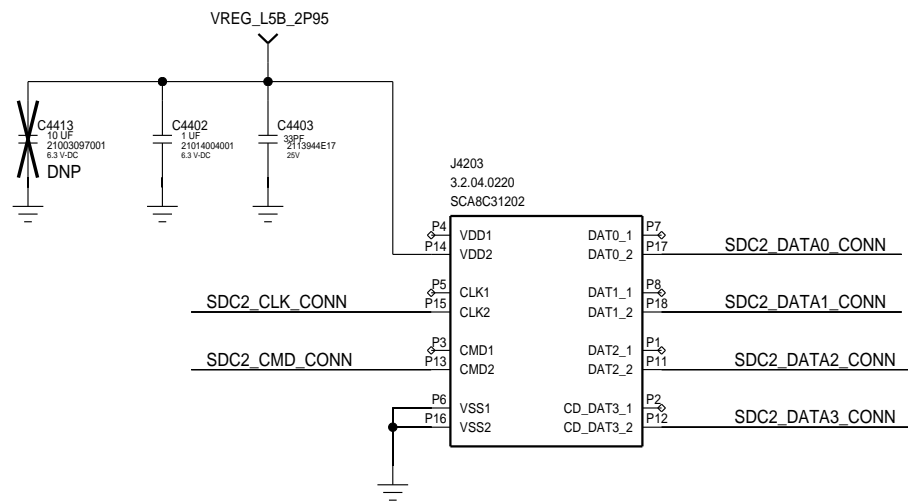
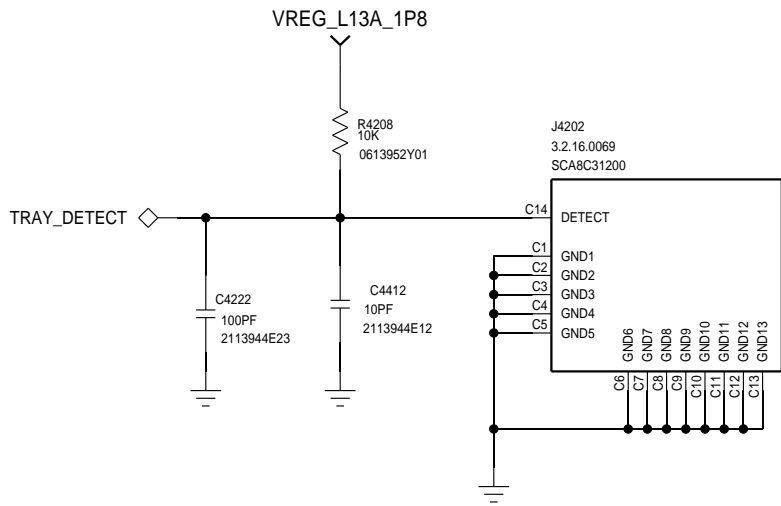
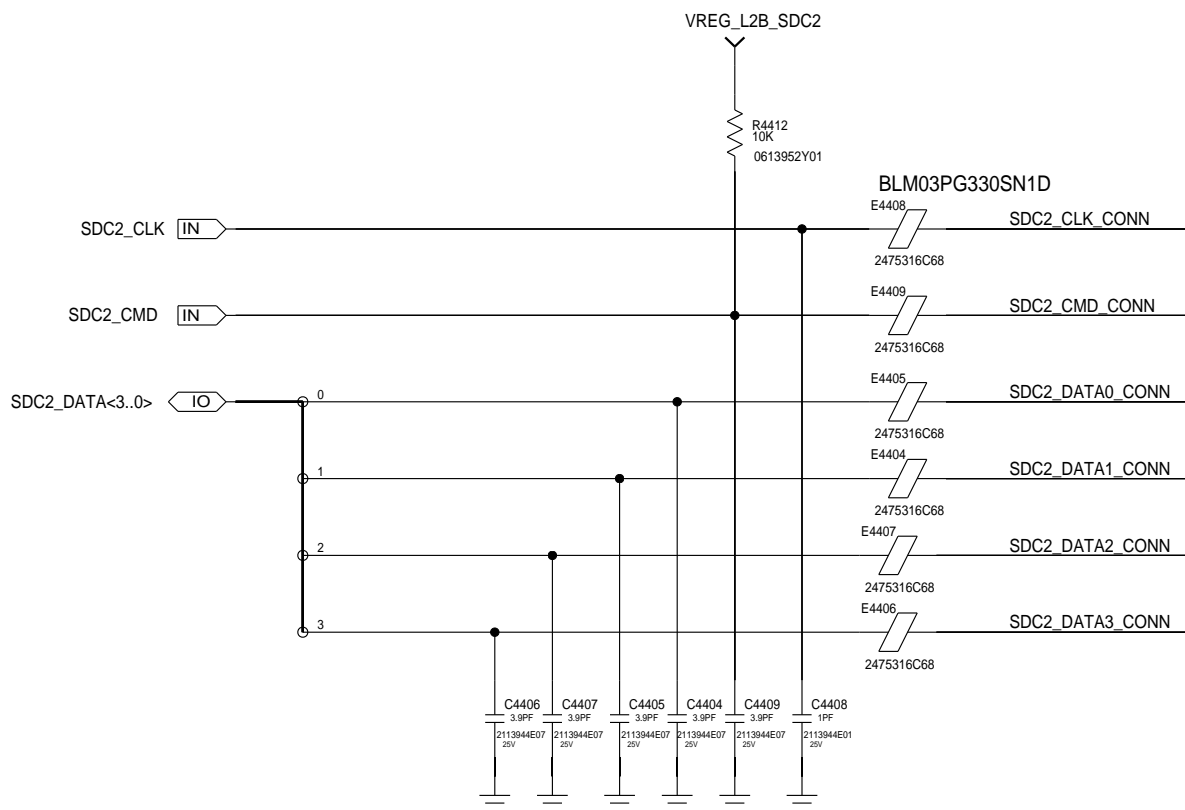
REF:4200-4499



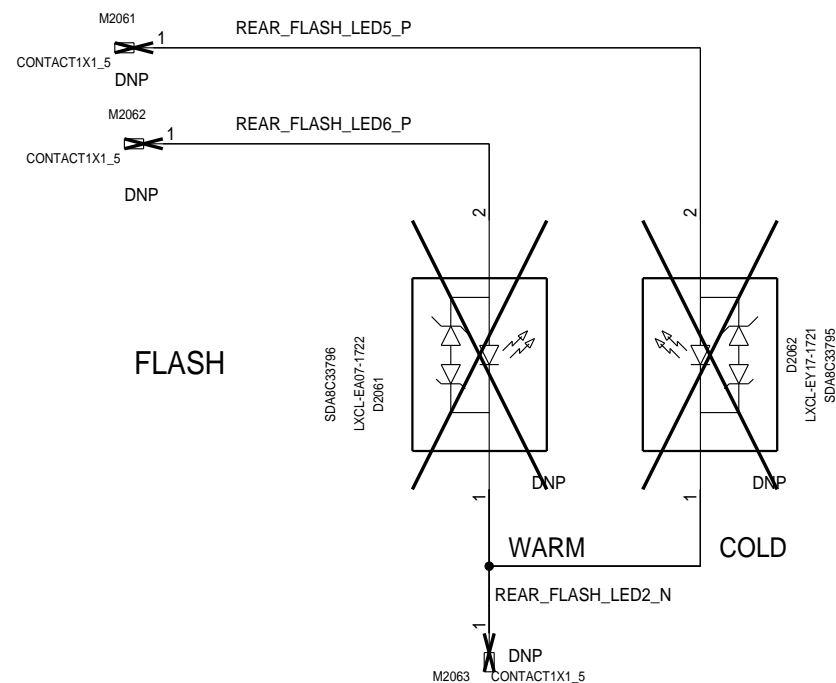
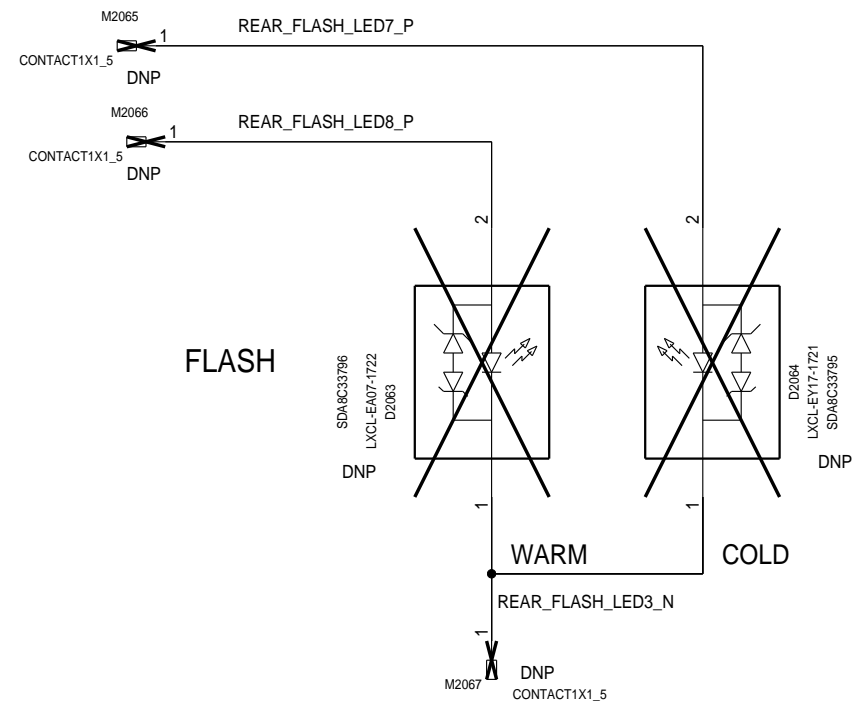
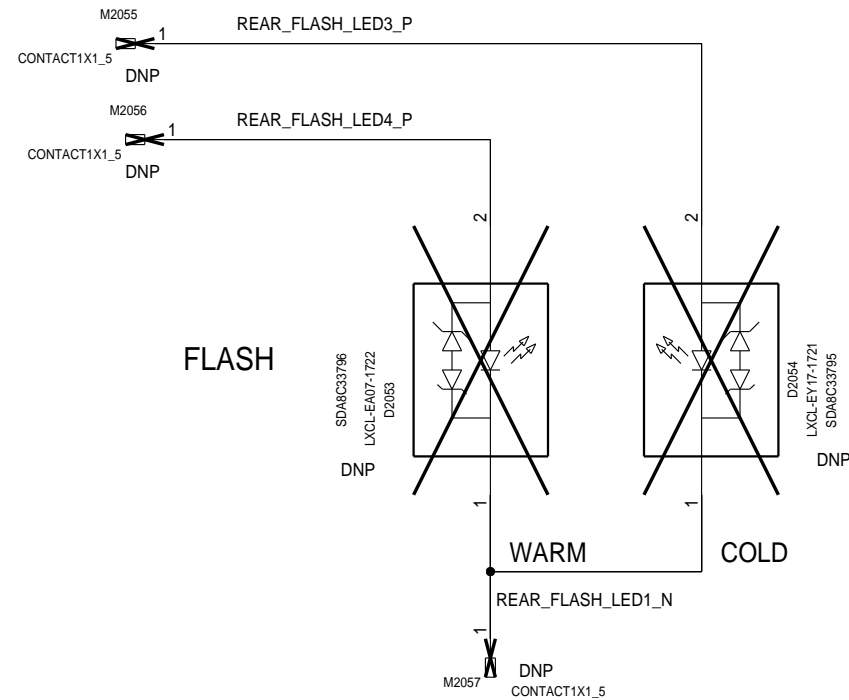
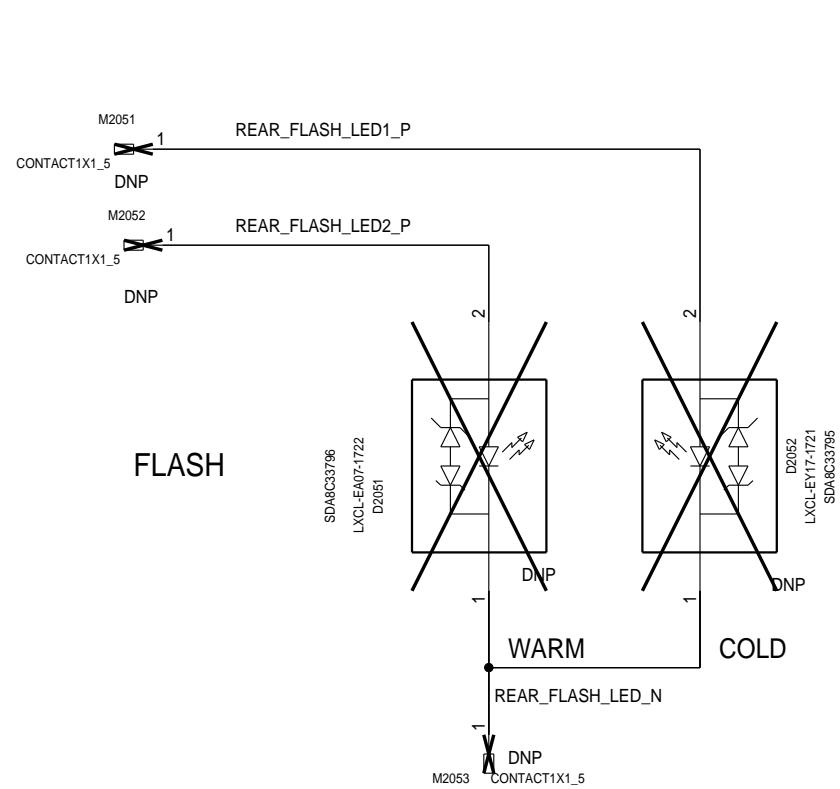
NOTE 1: USE 24.9K PULL-UP TO SUPPORT HOT SWAP WITH FREEZE-IO FEATURE.
NOTE 2: UIM_PWR BYPASS OF 0.33UF RECOMMENDED BY NFC IC SUPPLIER, PLACE NEAR CONNECTOR.

UI: SDCARD

REF:4200-4499



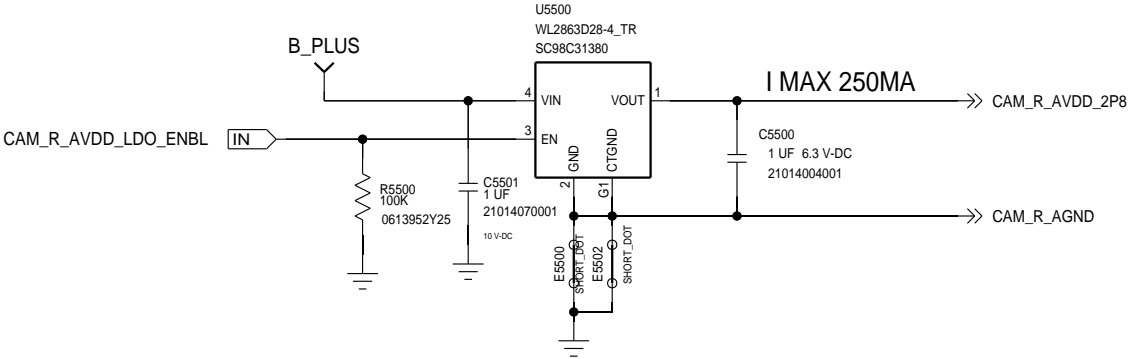
CAM: REAR FLASH



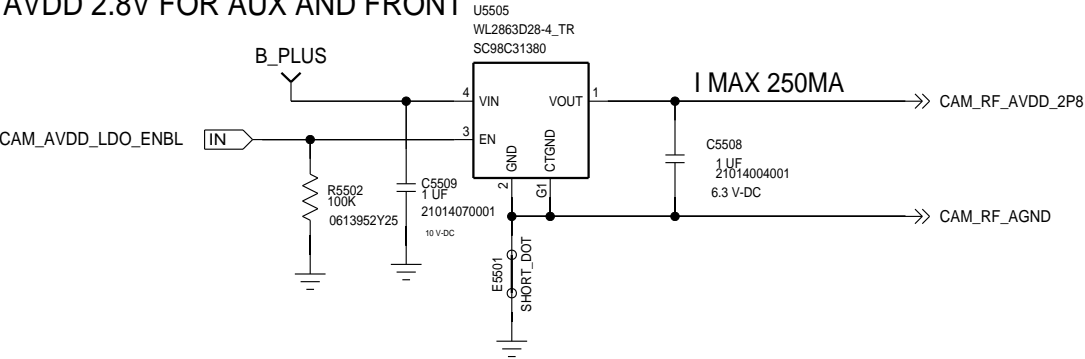
CAM: POWER

REF:5500-5529

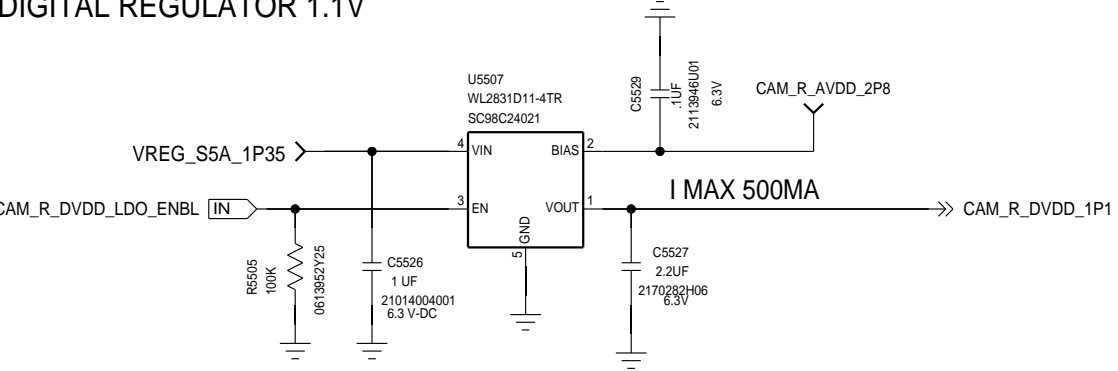
AVDD 2.8V FOR REAR MAIN



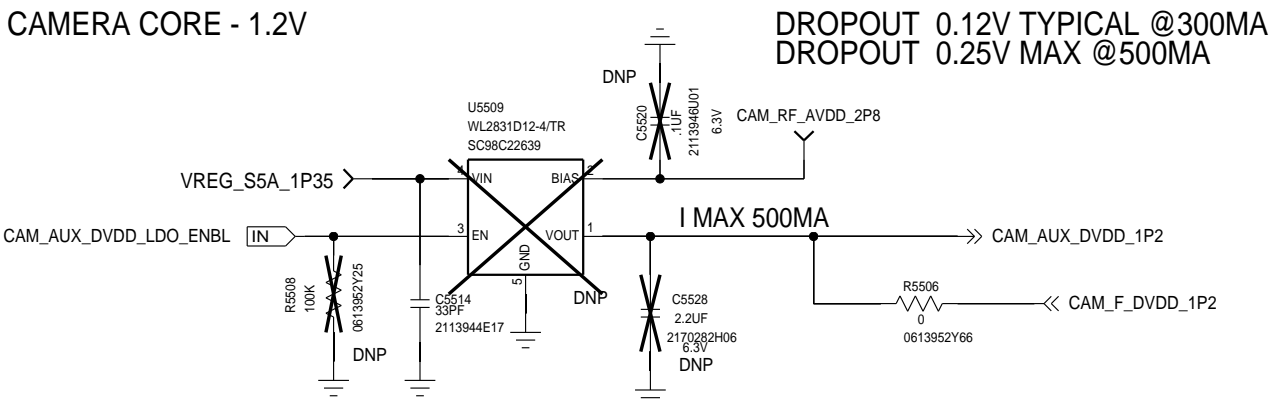
AVDD 2.8V FOR AUX AND FRONT



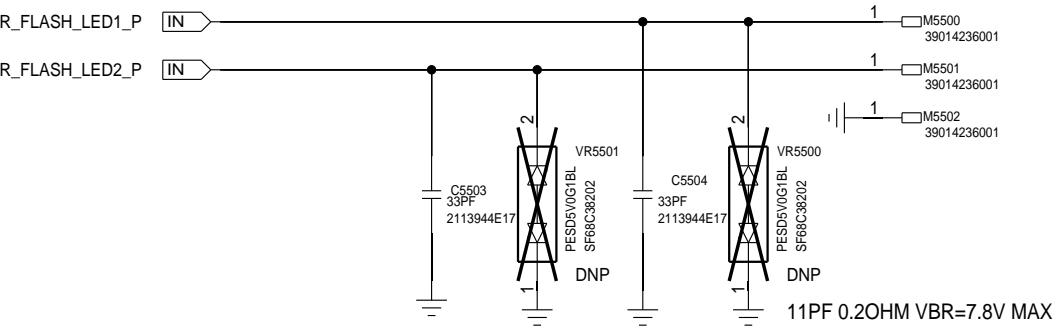
DIGITAL REGULATOR 1.1V



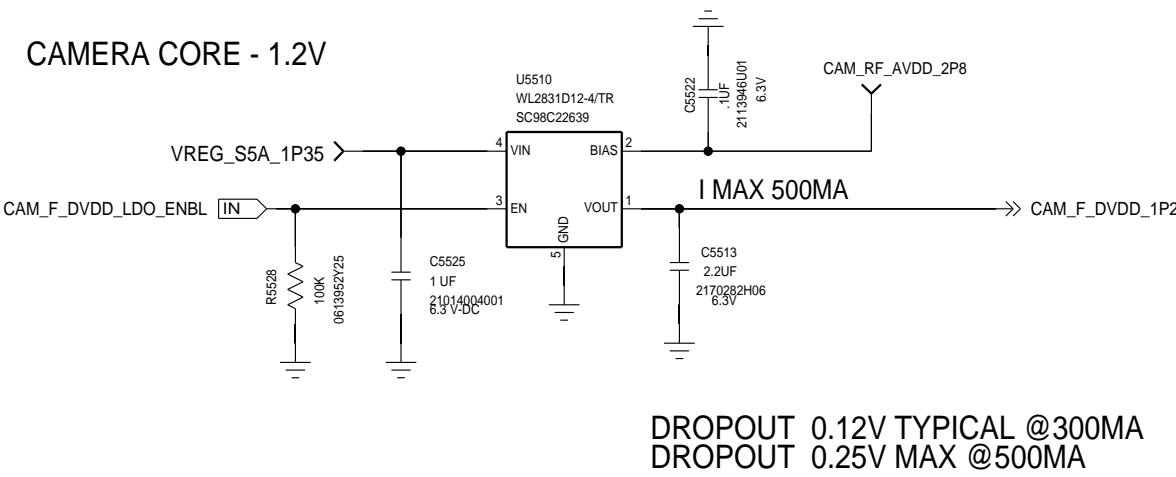
CAMERA CORE - 1.2V



REAR FACING FLASH REAR FLASH I MAX 1500MA FOR EACH



CAMERA CORE - 1.2V



DROPOUT 0.12V TYPICAL @300MA
DROPOUT 0.25V MAX @500MA

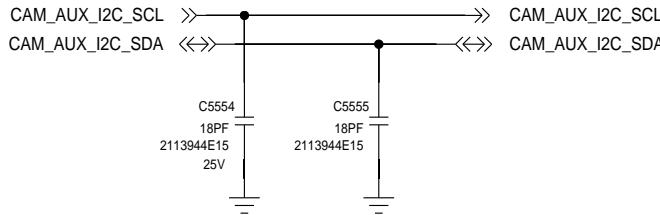
CAM: FRONT CONNECTOR

REF 5550-5569

FF CAMERA CONNECTOR

I2C

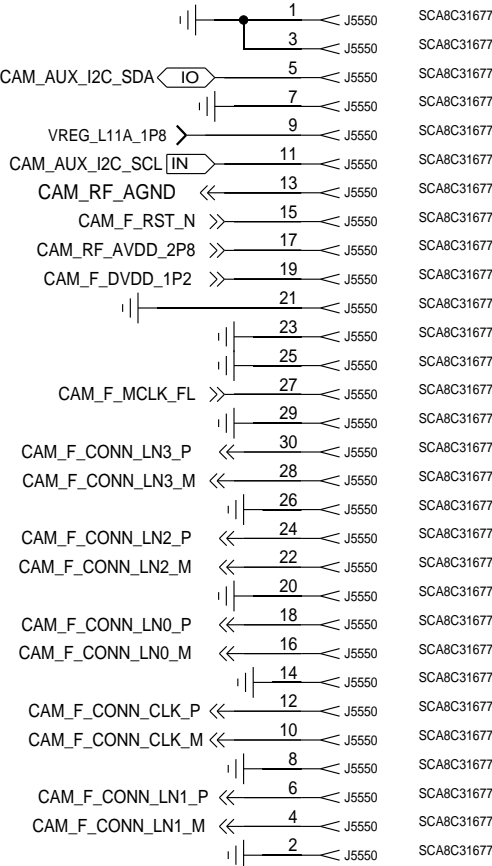
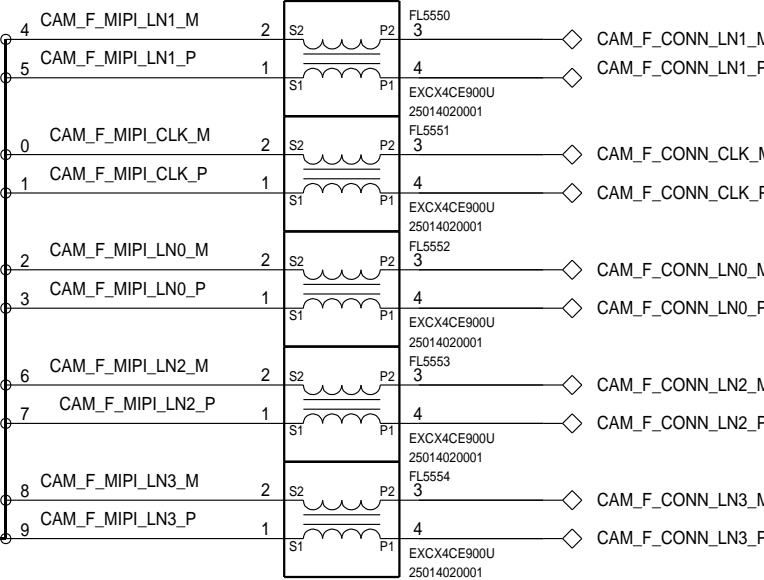
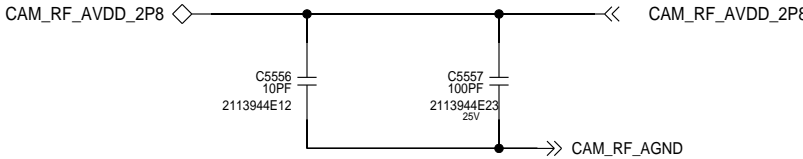
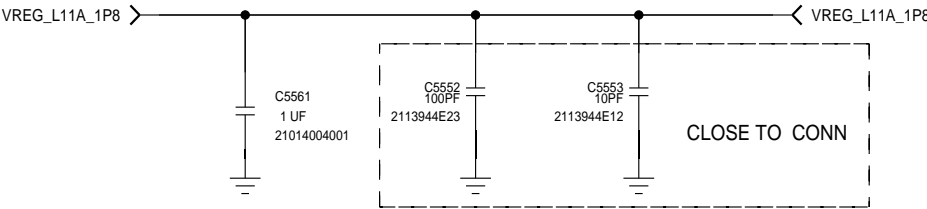
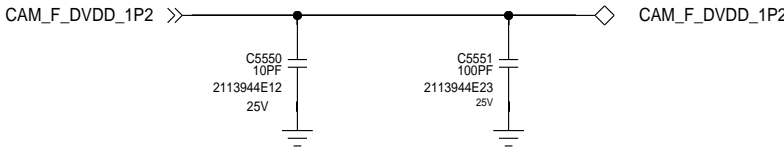
12M CAM I2C ADDR: 0X20(W), 0X21(R)



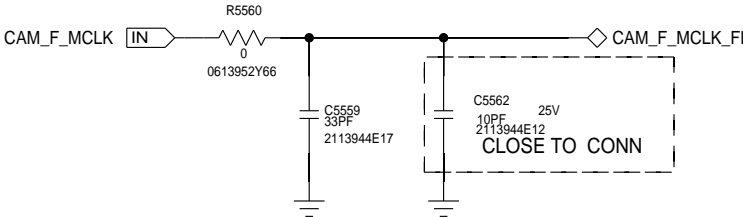
SENSOR: OV12A10
12M, 1.25UM, 1/2.8", 4096X3072

CAM I2C ADDRESS
FRONT OV12A10 CAM_AUX_I2C
SENSOR: 0X20,0X21
EEPROM: 0XA0,0XA1
AUX S5K5E9YX04 CAM_AUX_I2C
SENSOR: 0X20,0X21
REAR MAIN IMX519 CAM_I2C
SENSOR: 0X34,0X35
OIS: 0X48,0X49
EEPROM: 0XA0,0XA1

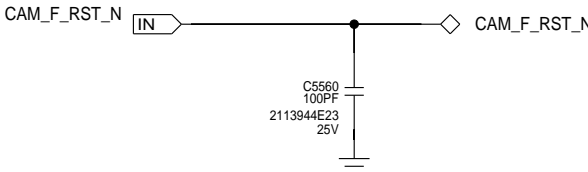
CAPACIORS BEADS ON THIS PAGE NEED BE PLACED CLOSE TO FRONT CAMERA CONNECTOR PIN.



EMI/ESD FILTER



XSHUTDOWN

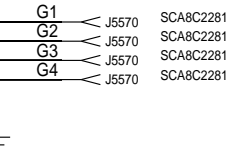
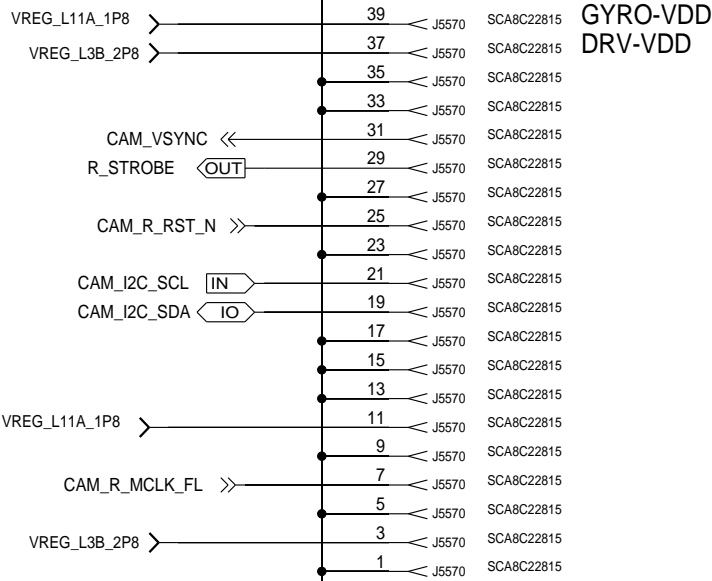
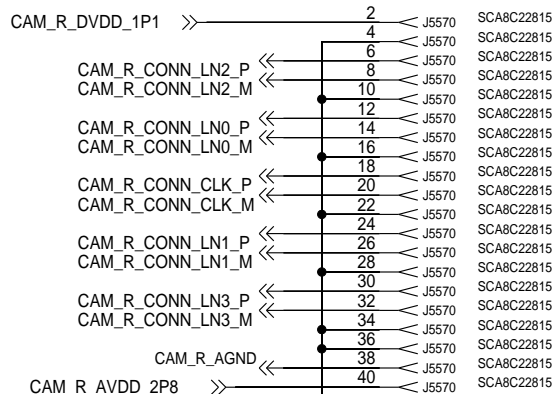
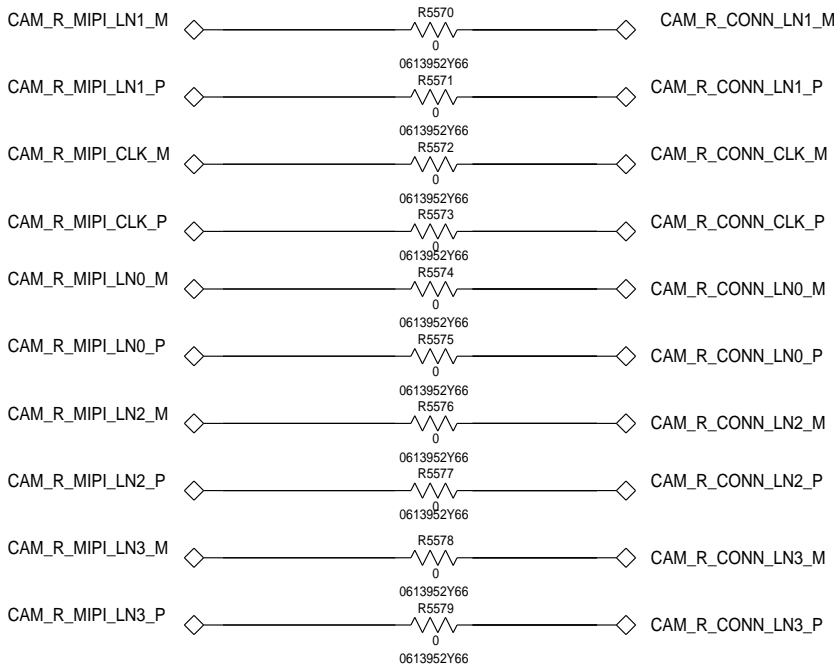
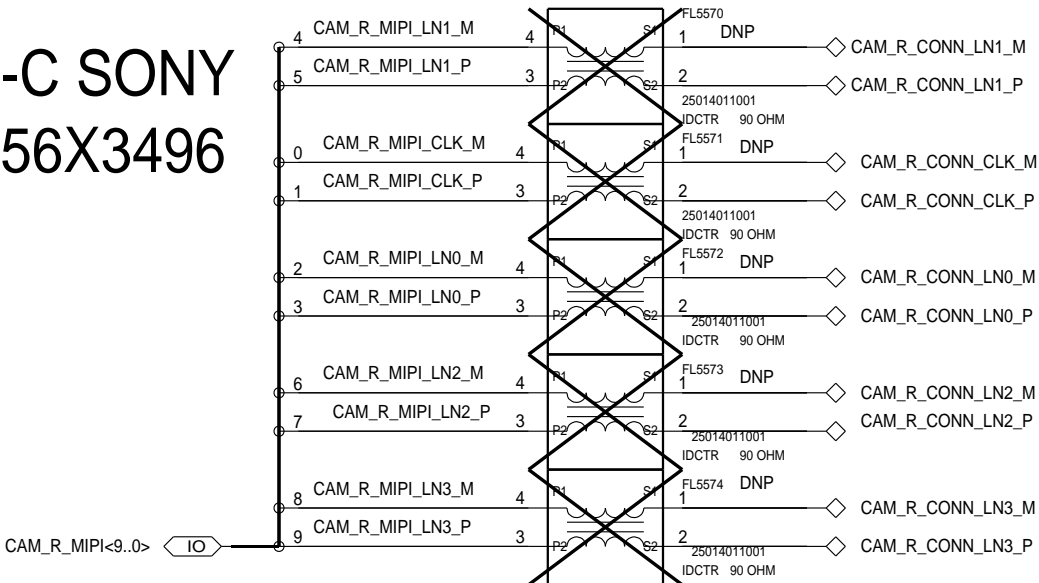
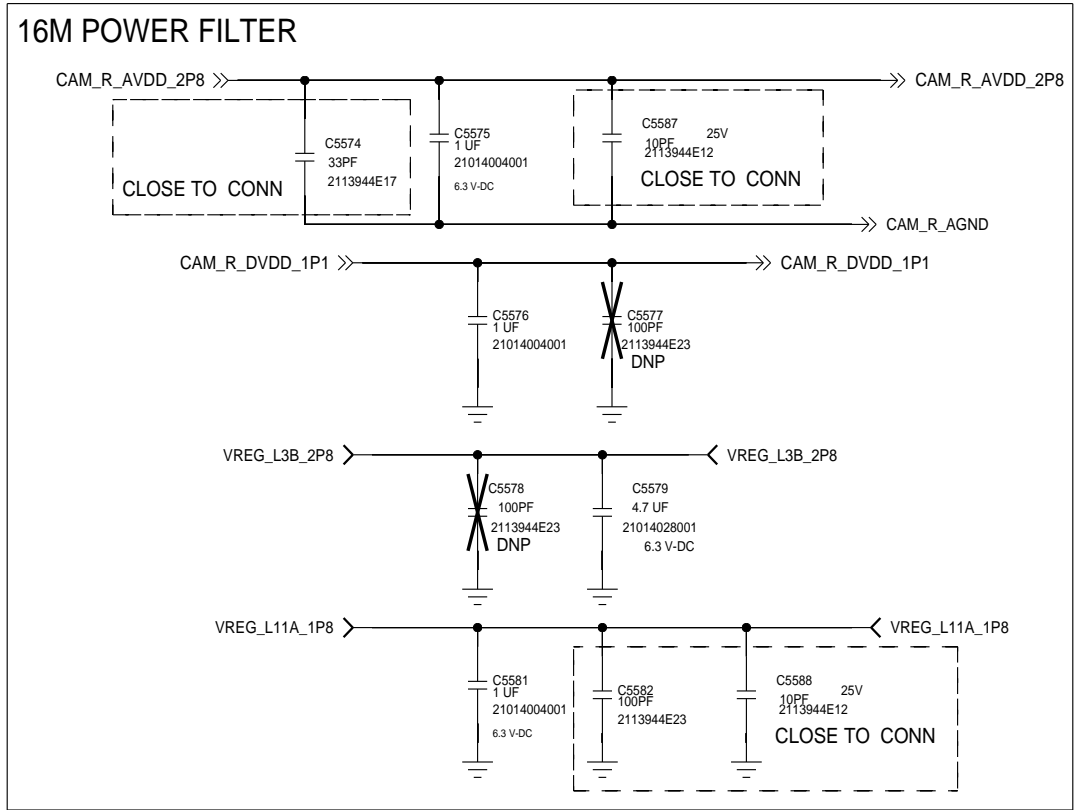
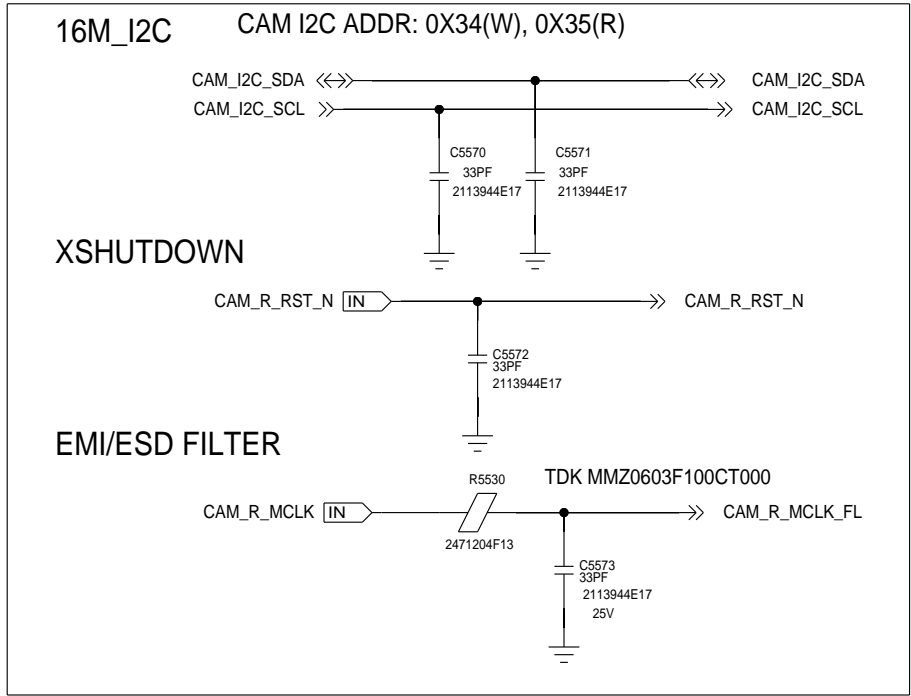


cam: rear main connector

REF:5570-5589

SENSOR: IMX519-AAQH5-C SONY

16M, 1.22UM, 1/2.534", 4656X3496



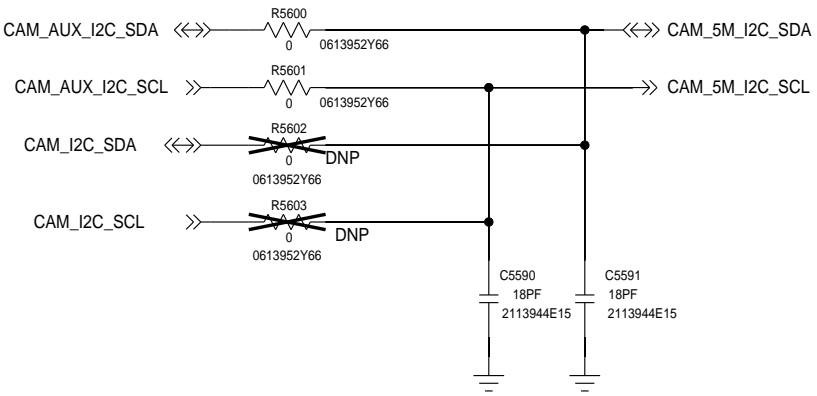
CAPACIORS BEADS ON THIS PAGE NEED BE PLACED CLOSE TO MAIN REAR CAMERA CONNECTOR PIN.

CAM: REAR AUX CONNECTOR

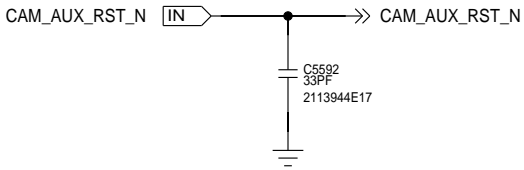
REF:5590-5610 5M CAM I2C ADDR: 0X20(W), 0X21(R)

SENSOR: S5K5E9YX04 SUMSANG
5M, 1.12UM, 1/5", 2592X1944

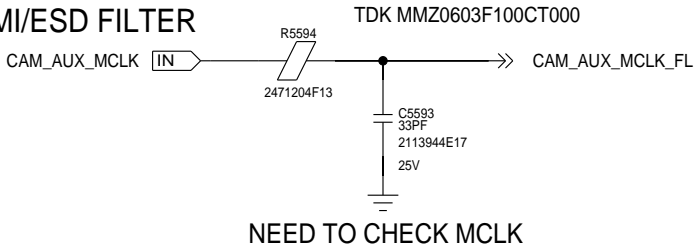
AUX_I2C



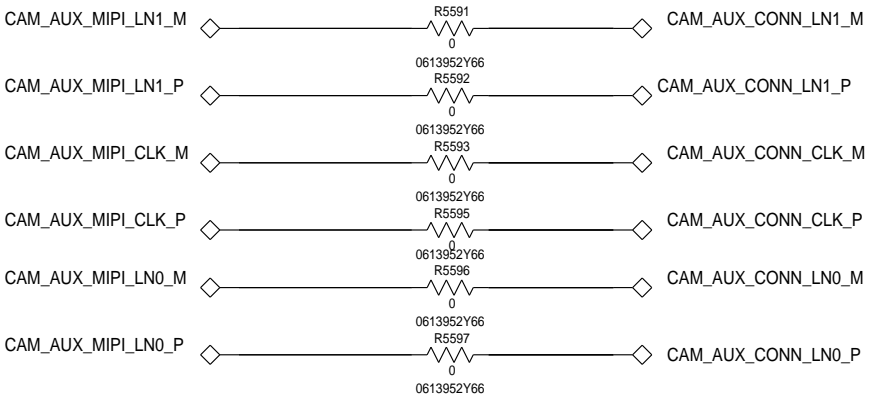
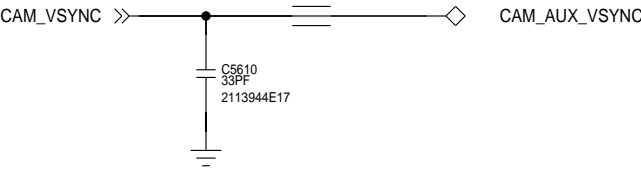
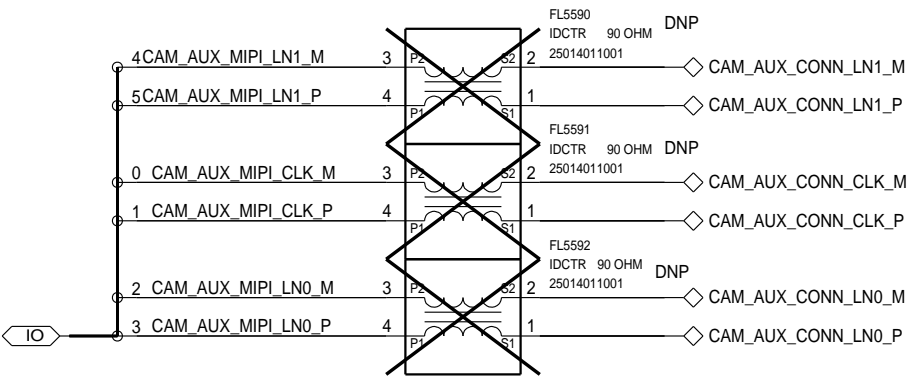
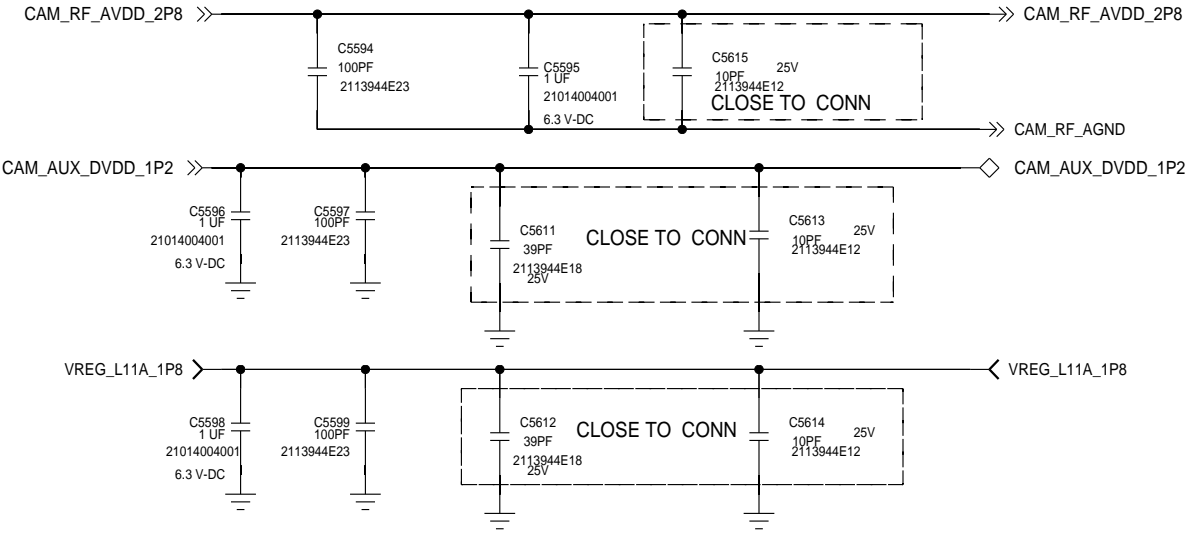
XSHUTDOWN



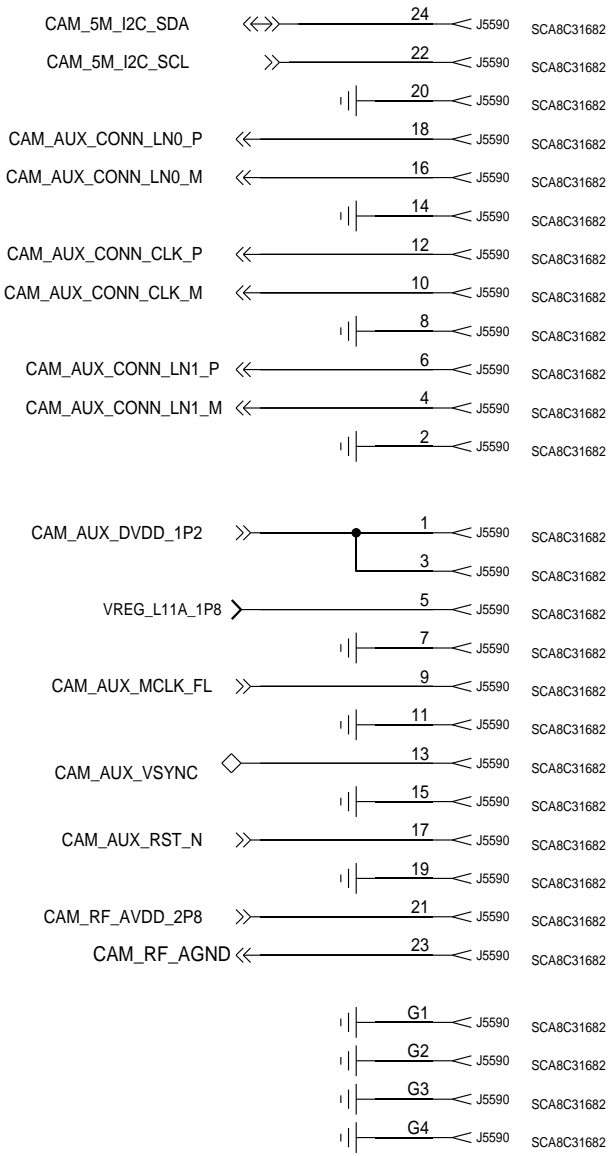
EMI/ESD FILTER



AUX POWER FILTER



CAPACIORS BEADS ON THIS PAGE NEED BE PLACED CLOSE TO AUX REAR CAMERA CONNECTOR PIN.



DISP: CONNECTOR

REF:5620--5650

ATTENTION TO POWER SUPPLY

LCD BACKUP ID

1 NO J5620 SCA8C31679

LCD_ID 1 3 J5620 SCA8C31679

5 J5620 SCA8C31679

DATA_3N 7 J5620 SCA8C31679

DATA_3P 9 J5620 SCA8C31679

11 J5620 SCA8C31679

DATA_0N 13 J5620 SCA8C31679

DATA_0P 15 J5620 SCA8C31679

17 J5620 SCA8C31679

CLK_N 19 J5620 SCA8C31679

CLK_P 21 J5620 SCA8C31679

23 J5620 SCA8C31679

DATA_1N 25 J5620 SCA8C31679

DATA_1P 27 J5620 SCA8C31679

29 J5620 SCA8C31679

DATA_2N 31 J5620 SCA8C31679

DATA_2P 33 J5620 SCA8C31679

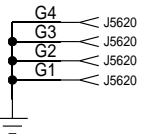
35 J5620 SCA8C31679

LED_PWM 37 J5620 SCA8C31679

LCD_TE_1 39 J5620 SCA8C31679

LCD_RST_N_1 40 J5620 SCA8C31679

IOVCC 38 J5620 SCA8C31679



36 NO J5620 SCA8C31679

LCD_VSP 34 J5620 SCA8C31679

LCD_VSN 32 J5620 SCA8C31679

30 NO J5620 SCA8C31679

BLM03HG102SN1D

28 J5620 SCA8C31679

LED_A 26 J5620 SCA8C31679

LED_K1 24 J5620 SCA8C31679

LED_K2 22 J5620 SCA8C31679

20 NO J5620 SCA8C31679

TS_INT_T 18 J5620 SCA8C31679

TS_I2C_SCL 16 J5620 SCA8C31679

TS_I2C_SDA 14 J5620 SCA8C31679

TS_RST_N_CONN 12 J5620 SCA8C31679

TP_FW_SPI_SS IN 10 J5620 SCA8C31679

TP_FW_SPI_MISO OUT 8 J5620 SCA8C31679

TP_FW_SPI_MOSI IN 6 J5620 SCA8C31679

TP_FW_SPI_CLK IN 4 J5620 SCA8C31679

2 J5620 SCA8C31679

VREG_L11A_1P8

E5620
SHORT_DOT

C5628

100PF

211394E01

6.3V

C5637

100PF

2113944E23

CLOSE TO CONN

LCD_VSP

LCD_VSN

LED_A

C5627

2.2UF

21014021001

25 V-DC

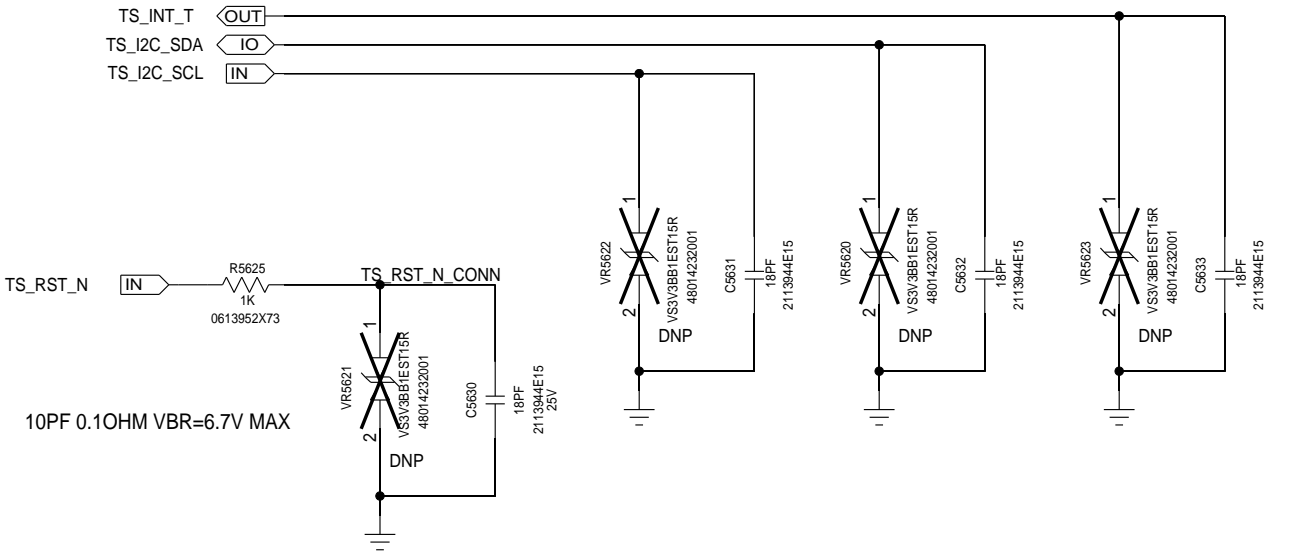
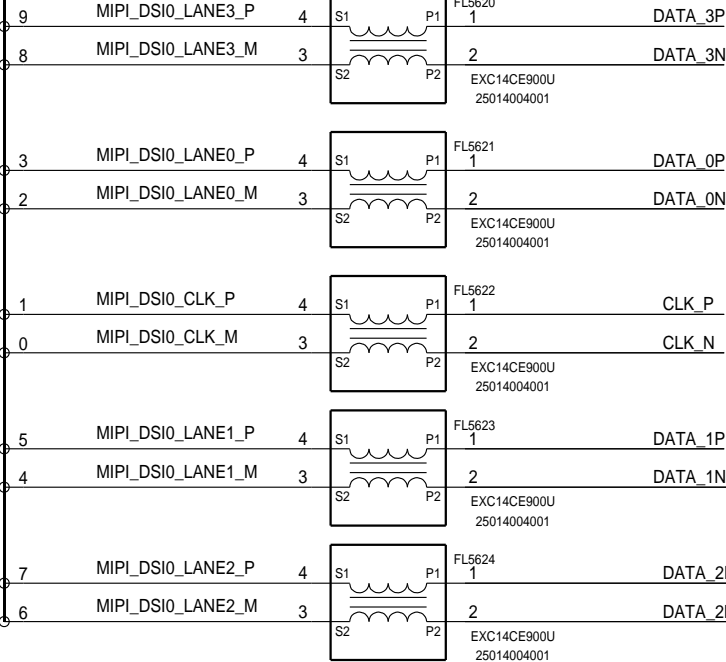
DNP

C5625

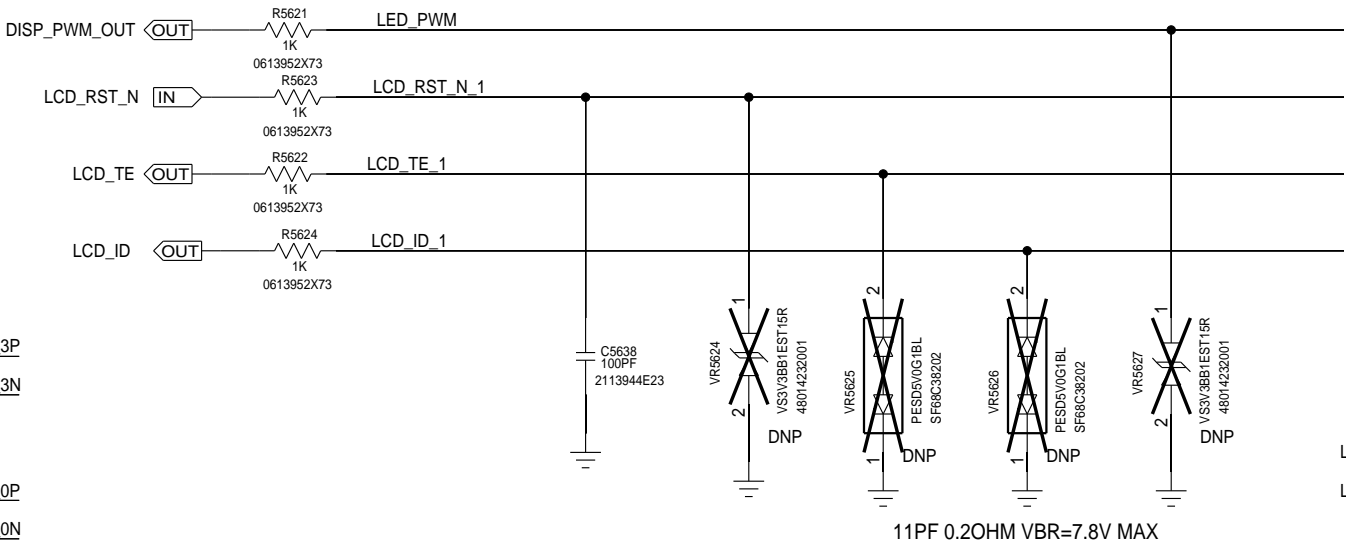
100PF

2113944E23

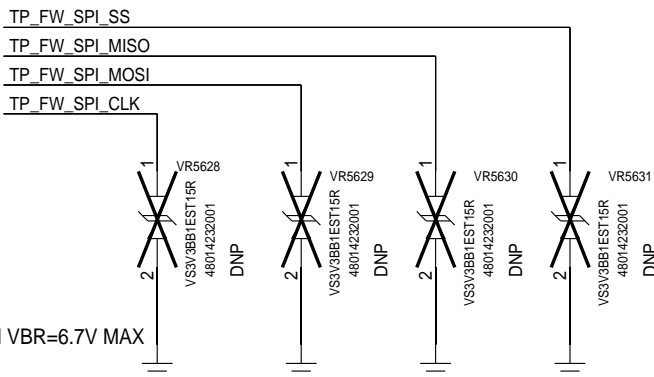
MIPI_DSI0<9..0>



10PF 0.1OHM VBR=6.7V MAX



11PF 0.2OHM VBR=7.8V MAX



10PF 0.1OHM VBR=6.7V MAX

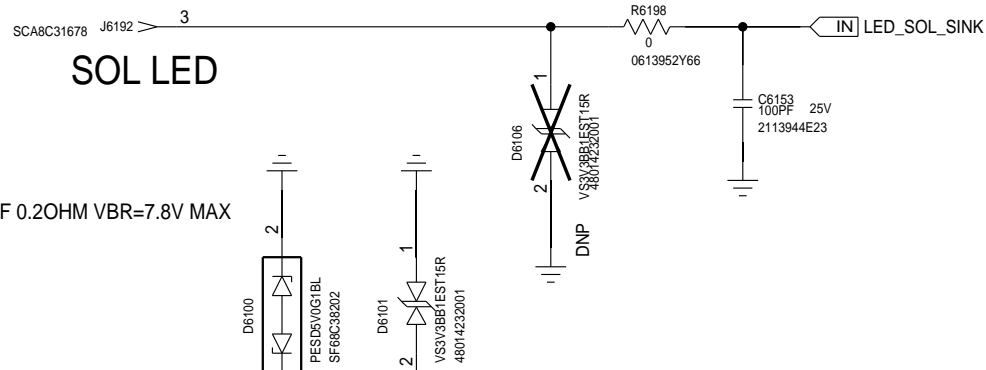
GND SHIELDED FOR MIPI DSI SIGNALS AS A GROUP
ADD GND STITCHING VIAS FOR EACH SIGNAL GROUP (EBI,CSI,DSI) WHEN POSSIBLE TO MINIMIZE RETURN CURRENT LOOP

XMCS: SENSORS

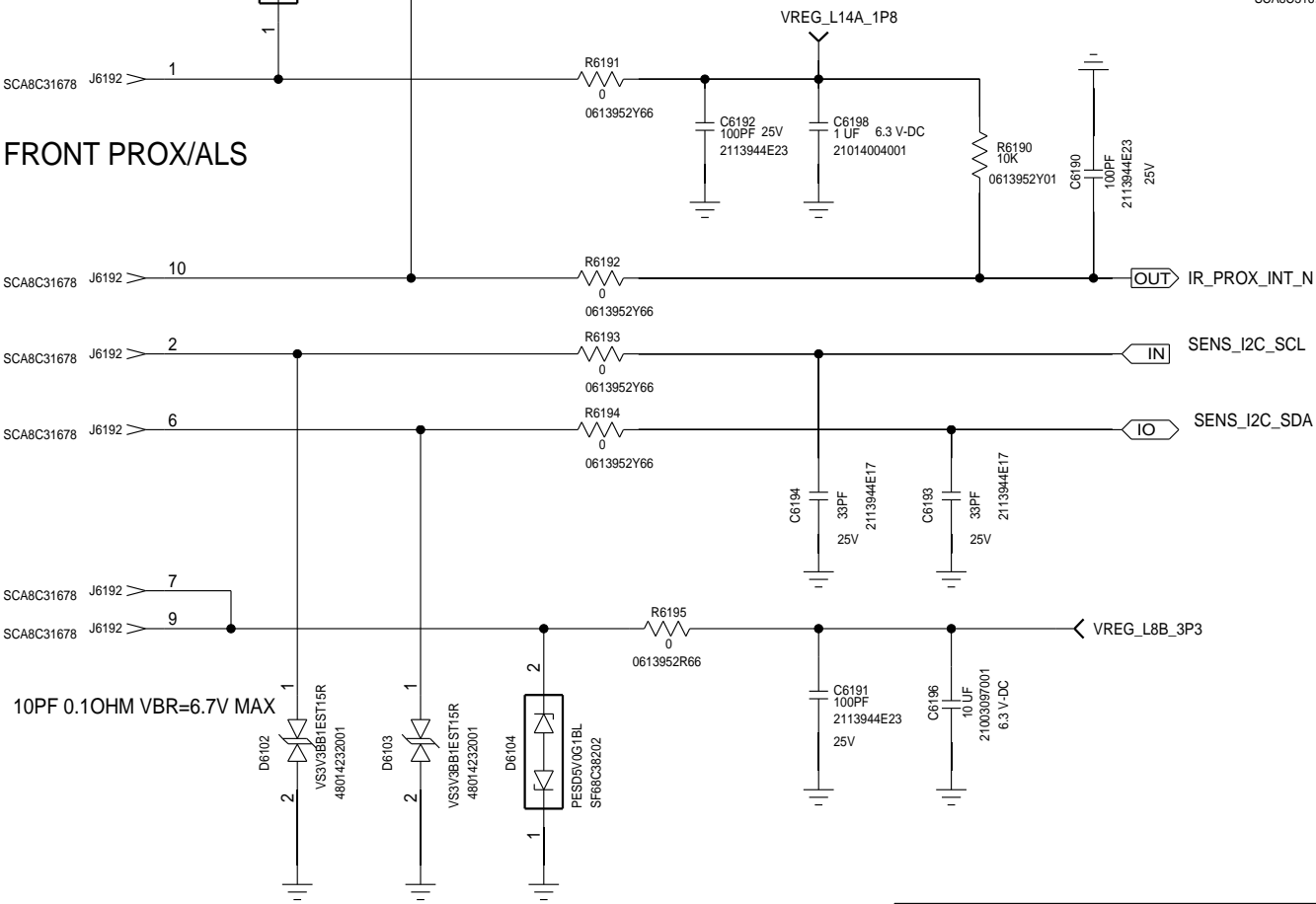
REF: 6100-6199

SOL LED

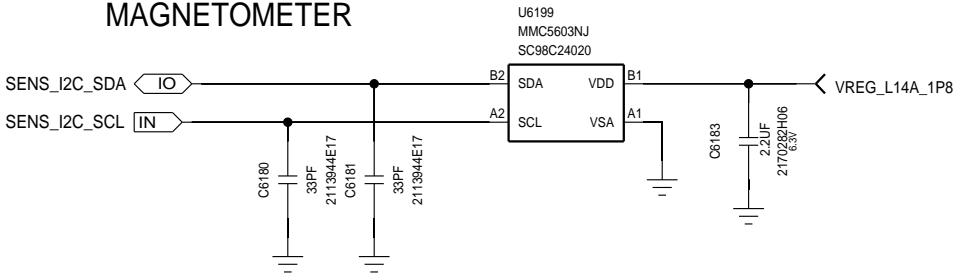
11PF 0.2OHM VBR=7.8V MAX



FRONT PROX/ALS



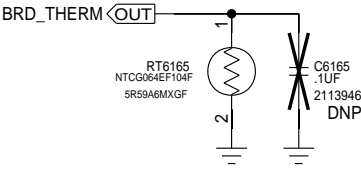
MAGNETOMETER



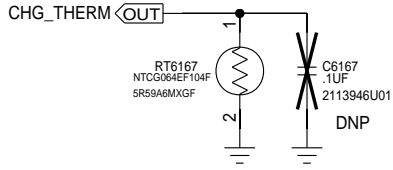
I2C ADDRESS
MMC5603NJ 0X60,0X61
CS47L35 0X34,0X35
TMD3702V 0X49,0X4A

AWAY FORM NOISY SIGNALS AND HIGH POWER SOURCES

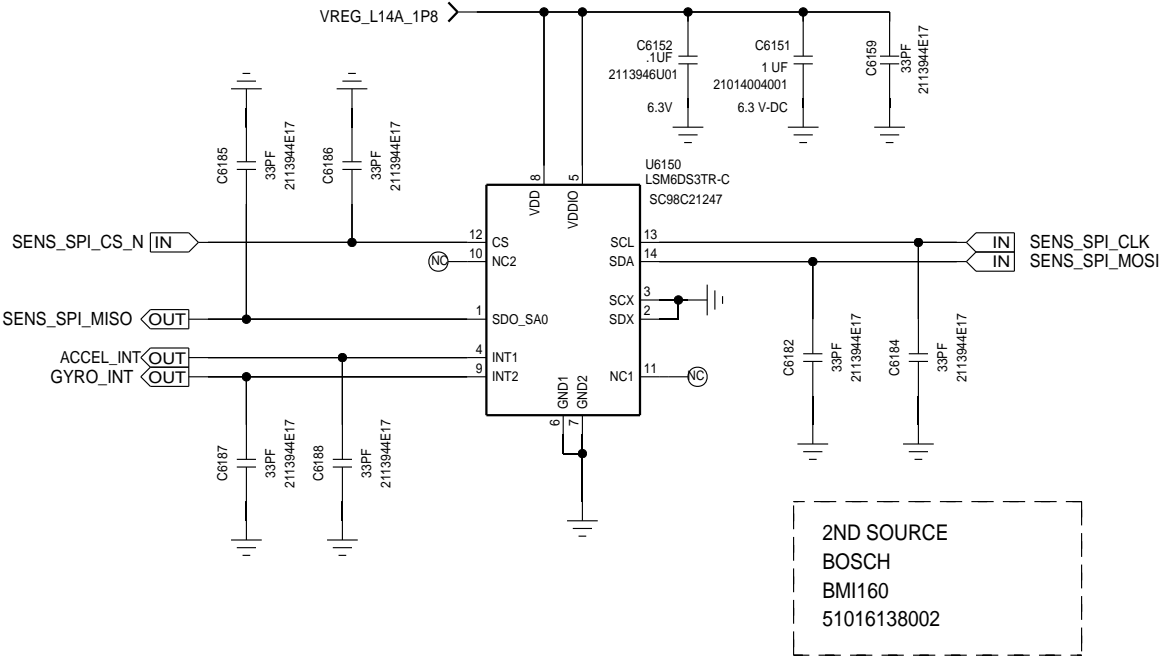
BOARD TEMP SENSOR



CHRG TEMP SENSOR



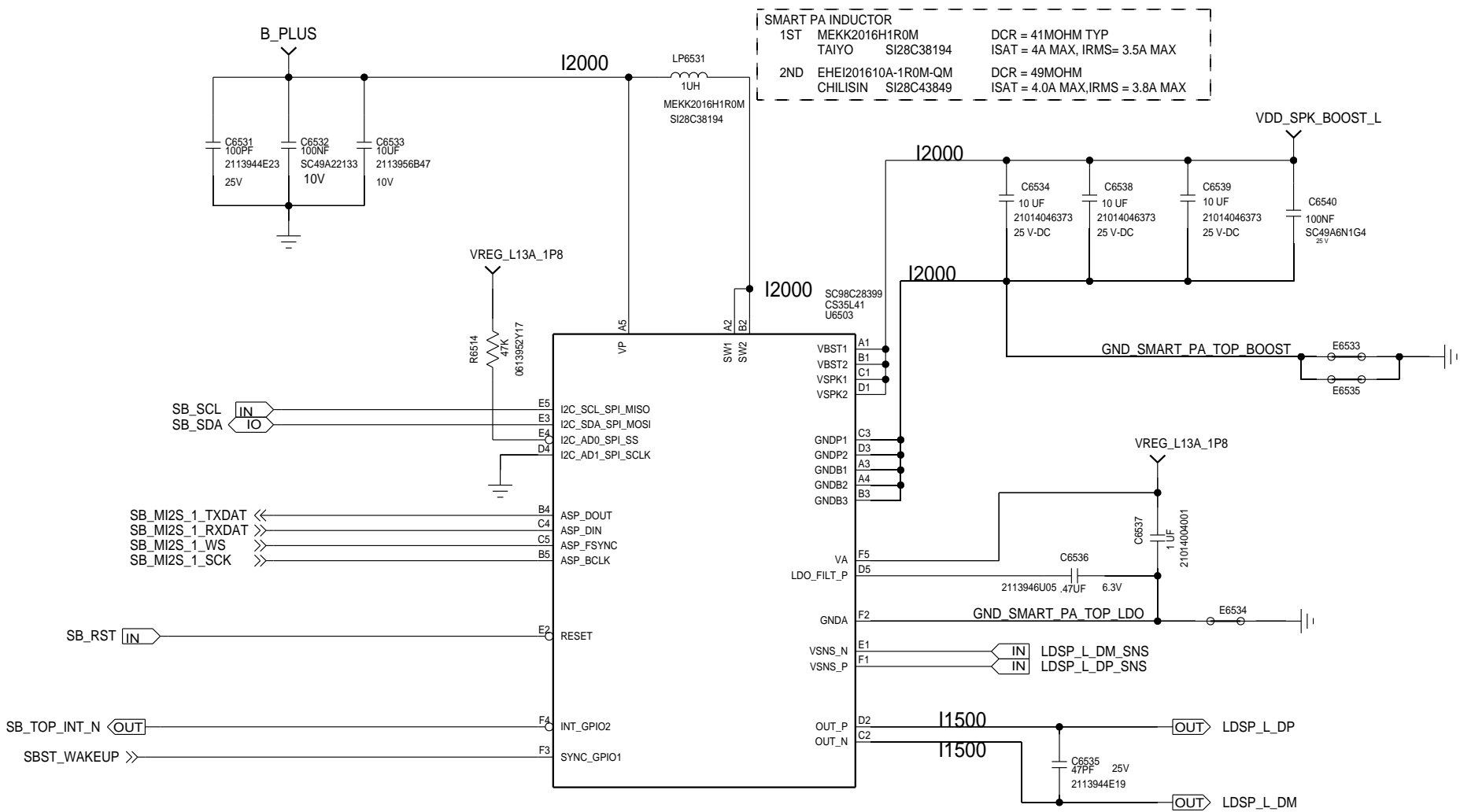
ACCELEROMETER+GYRO



XMCS: SMART BOOST TOP

REF 6500-6599

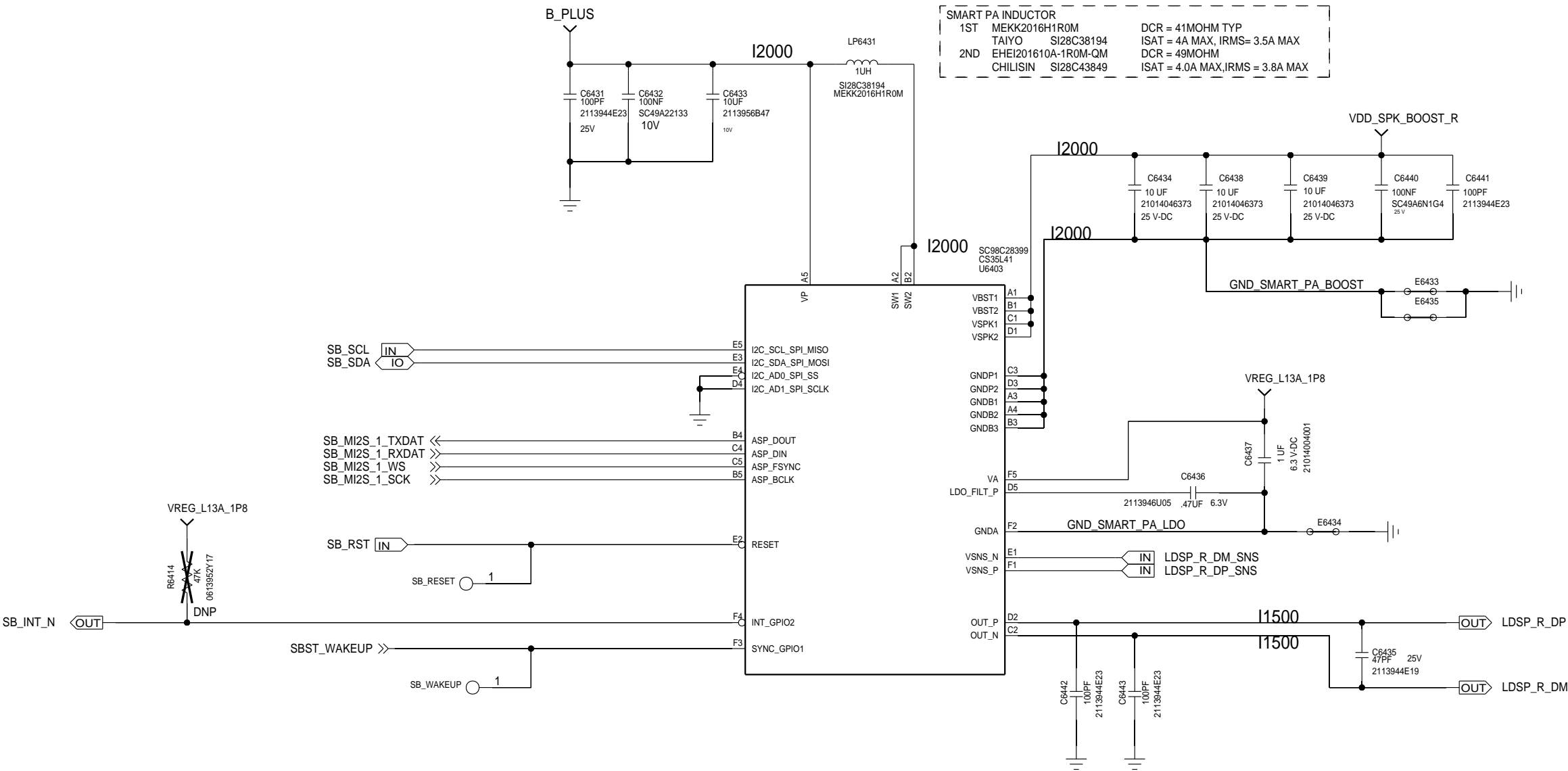
*** BOOST IC I2C ADD. 0X??



XMCS: SMART BOOST

REF 6400-6499

*** BOOST IC I2C ADD. 0X??



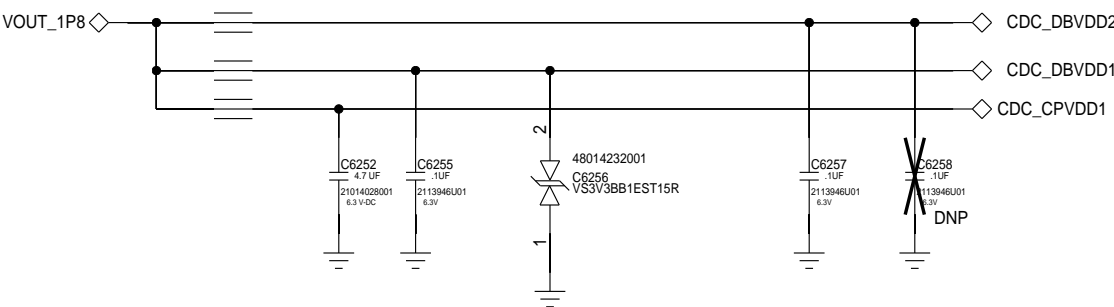
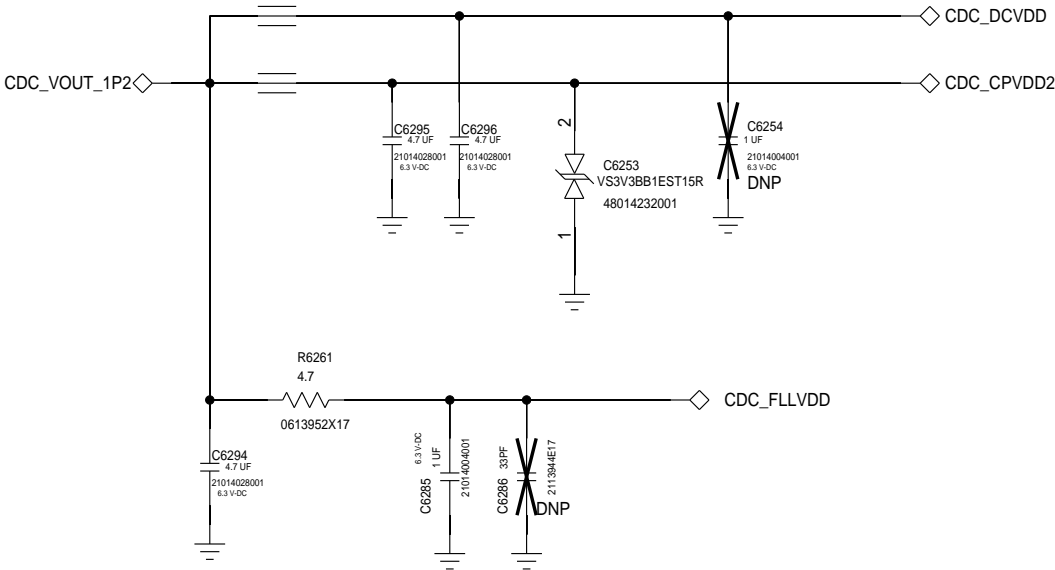
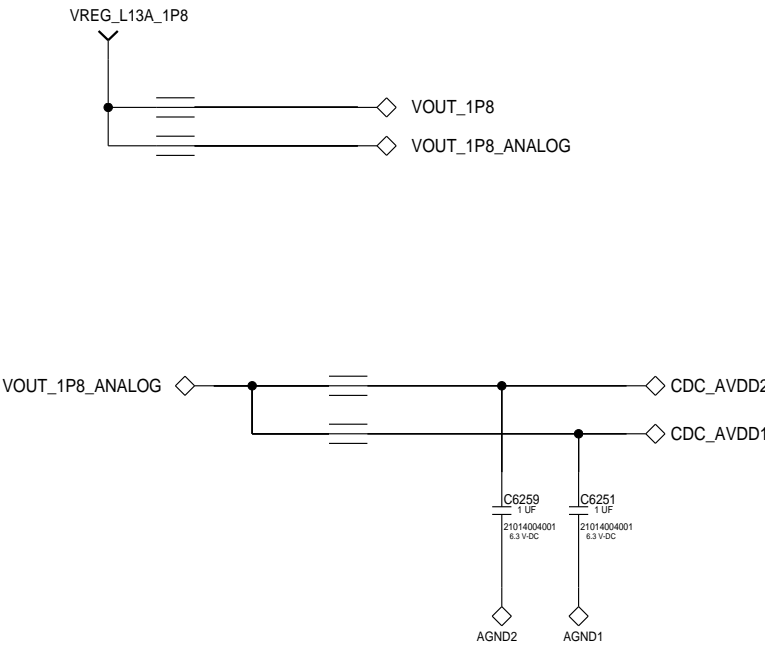
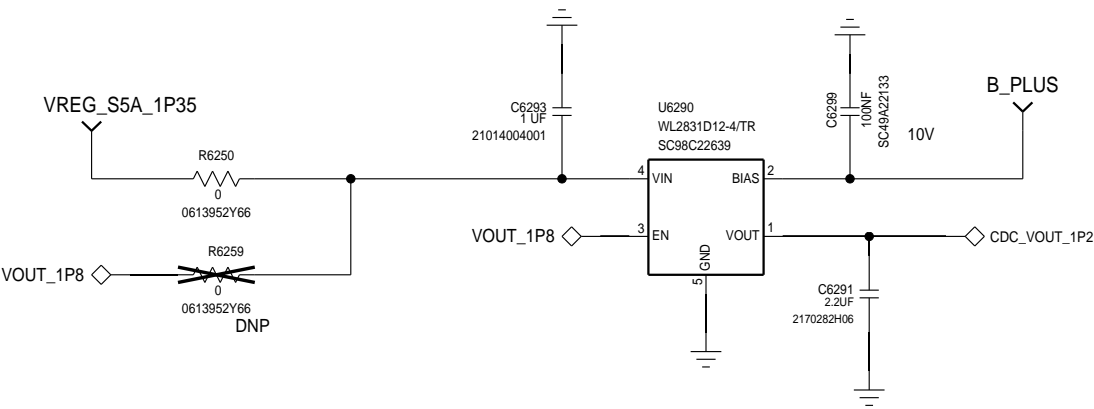
XMCS: CIRRUS CODEC
REF 6250-6299

XMCS: CODEC POWER

REF 6250-6299

1.2V POWER SUPPLY

DROPOUT 0.12V TYPICAL @300MA
DROPOUT 0.25V MAX @500MA



AFE: MICS

REF: 5700-5789

PRIMARY MIC

REF: 5700-5709

GND SHIELD, AWAY FROM NOISY SIGNAL

PRI_MIC_BIAS ☐

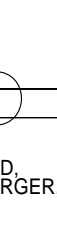
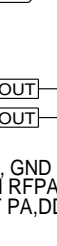
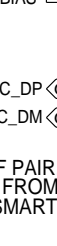
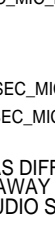
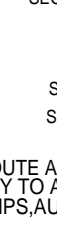
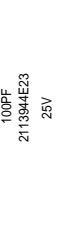
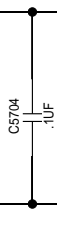
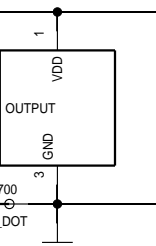
PRI_MIC_DP ☐

PRI_MIC_DM ☐

ROUTE AS DIFF PAIR, GND SHIELD.
TRY TO AWAY FROM RFPA, CHARGER,
SMPS,AUDIO SMART PA,DDR



AAC MIC
BOTTOM MIC



E5700 LOCATED CLOSED TO MIC OUTPUT

SECONDARY MIC

REF:5750-5759

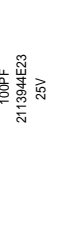
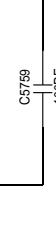
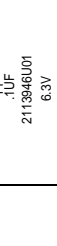
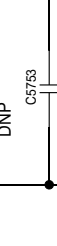
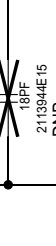
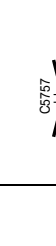
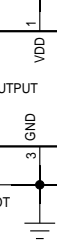
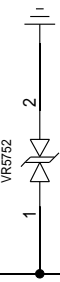
GND SHIELD, AWAY FROM NOISY SIGNAL

SEC_MIC_BIAS ☐

SEC_MIC_DP ☐

SEC_MIC_DM ☐

ROUTE AS DIFF PAIR, GND SHIELD.
TRY TO AWAY FROM RFPA, CHARGER,
SMPS,AUDIO SMART PA,DDR



E5750 LOCATED CLOSED TO MIC OUTPUT

10PF 0.1OHM VBR=6.7V MAX

TERTIARY MIC

REF: 5725-5730

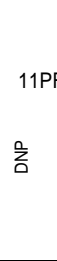
GND SHIELD, AWAY FROM NOISY SIGNAL

TER_MIC_BIAS ☐

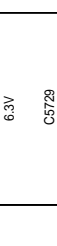
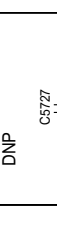
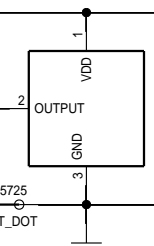
TER_MIC_DP ☐

TER_MIC_DM ☐

ROUTE AS DIFF PAIR, GND SHIELD.
TRY TO AWAY FROM RFPA, CHARGER,
SMPS,AUDIO SMART PA,DDR



AAC MIC
BOTTOM MIC



E5725 LOCATED CLOSED TO MIC OUTPUT

11PF 0.2OHM VBR=7.8V MAX

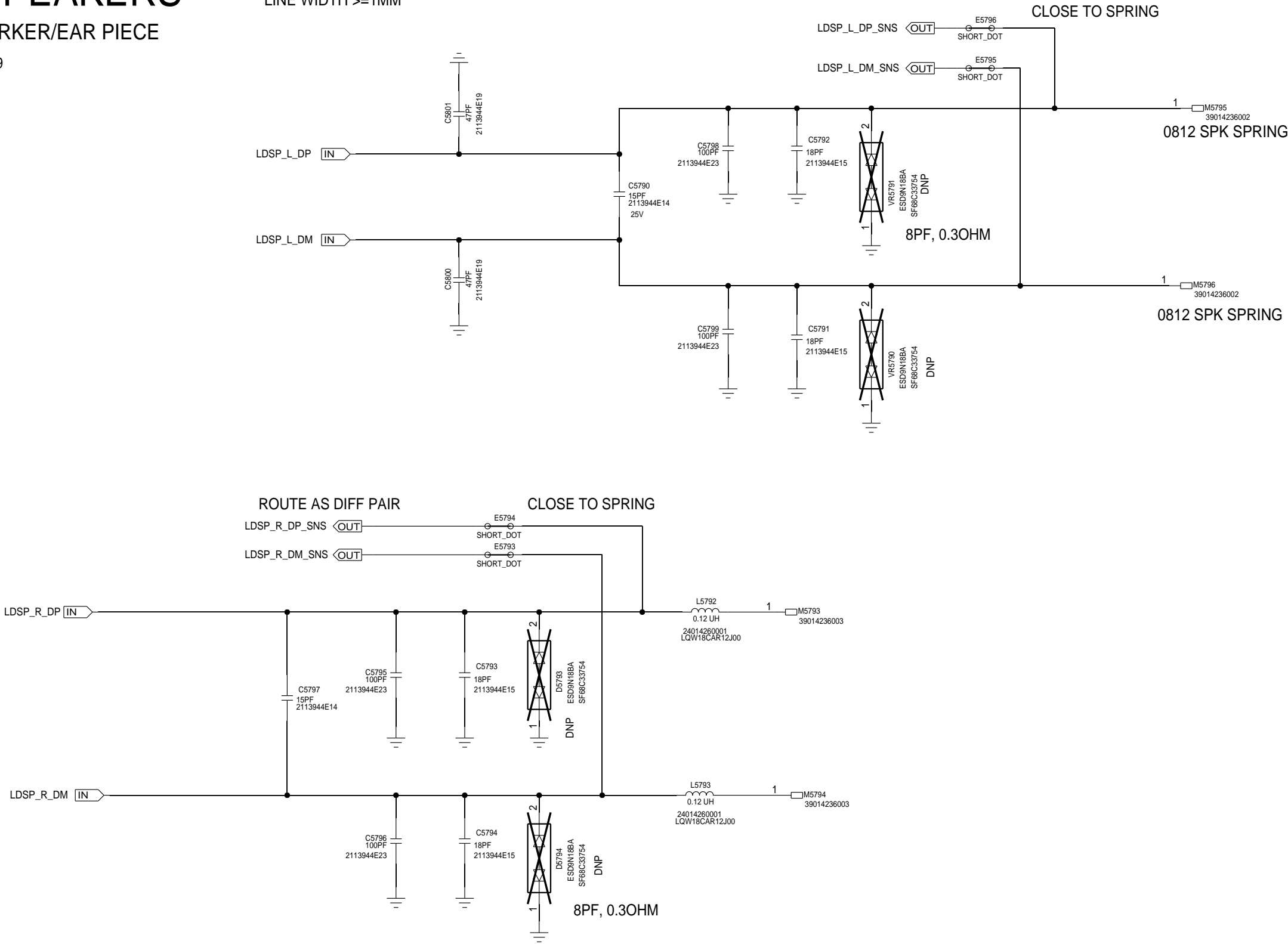
AFE: SPEAKERS

LOUDSPEAKER/EAR PIECE

REF:5790-5799

ROUTE AS DIFF PAIR
LINE WIDTH >=1MM

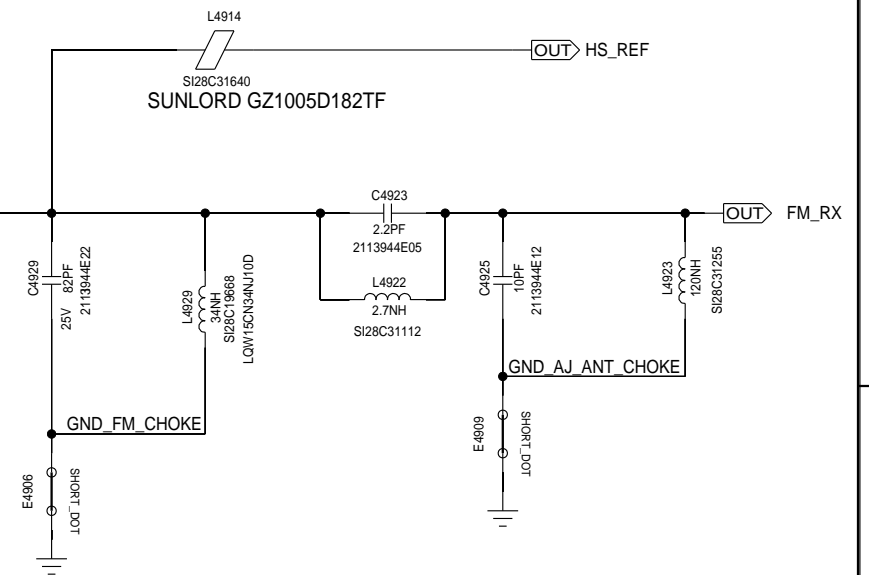
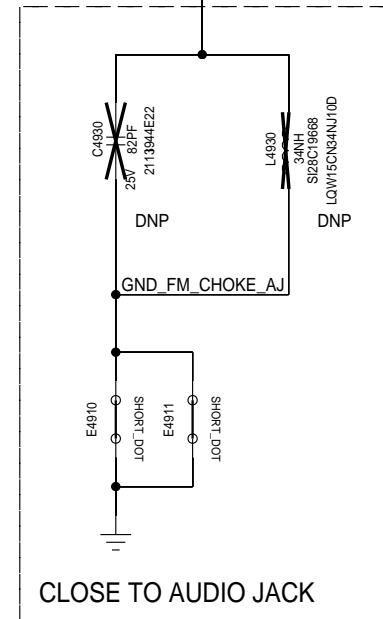
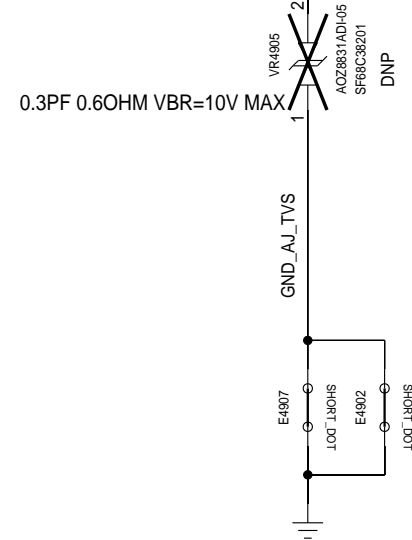
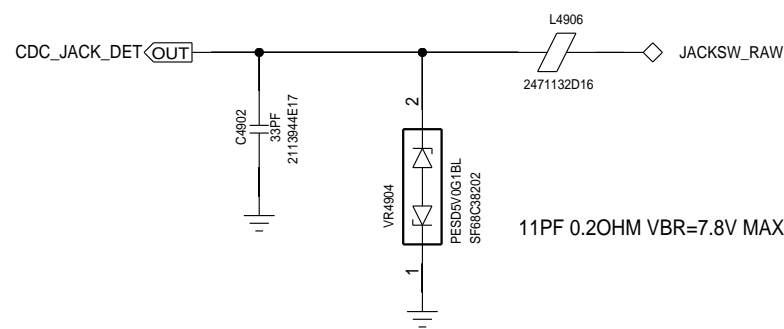
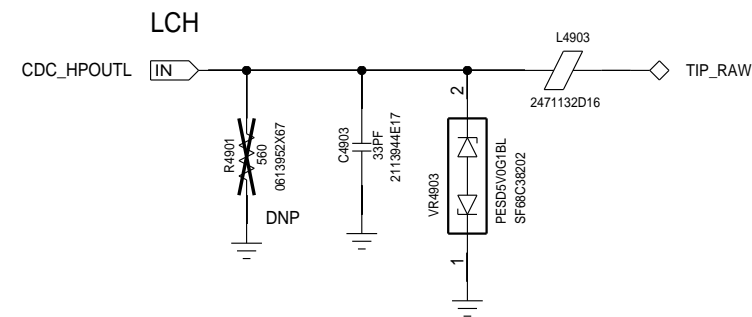
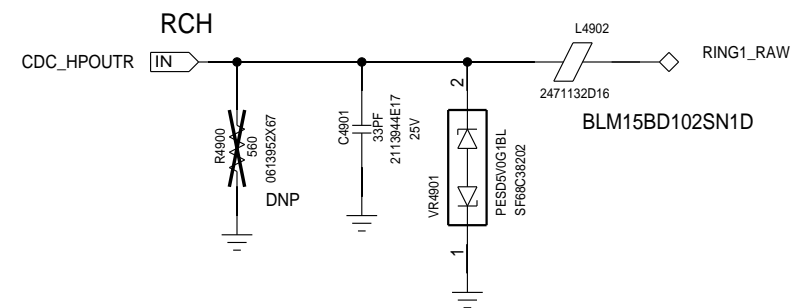
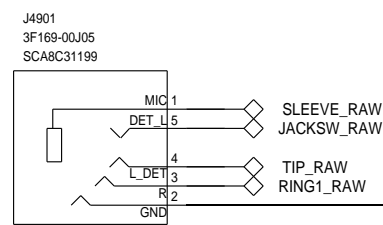
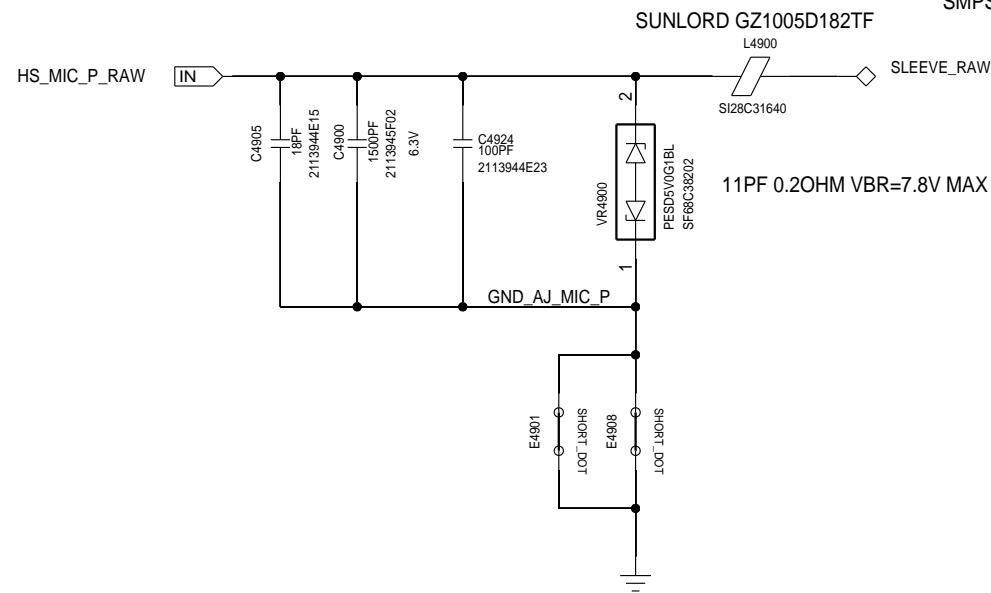
ADD STITCHING VIAS ALONGSIDE IF POSSIBLE



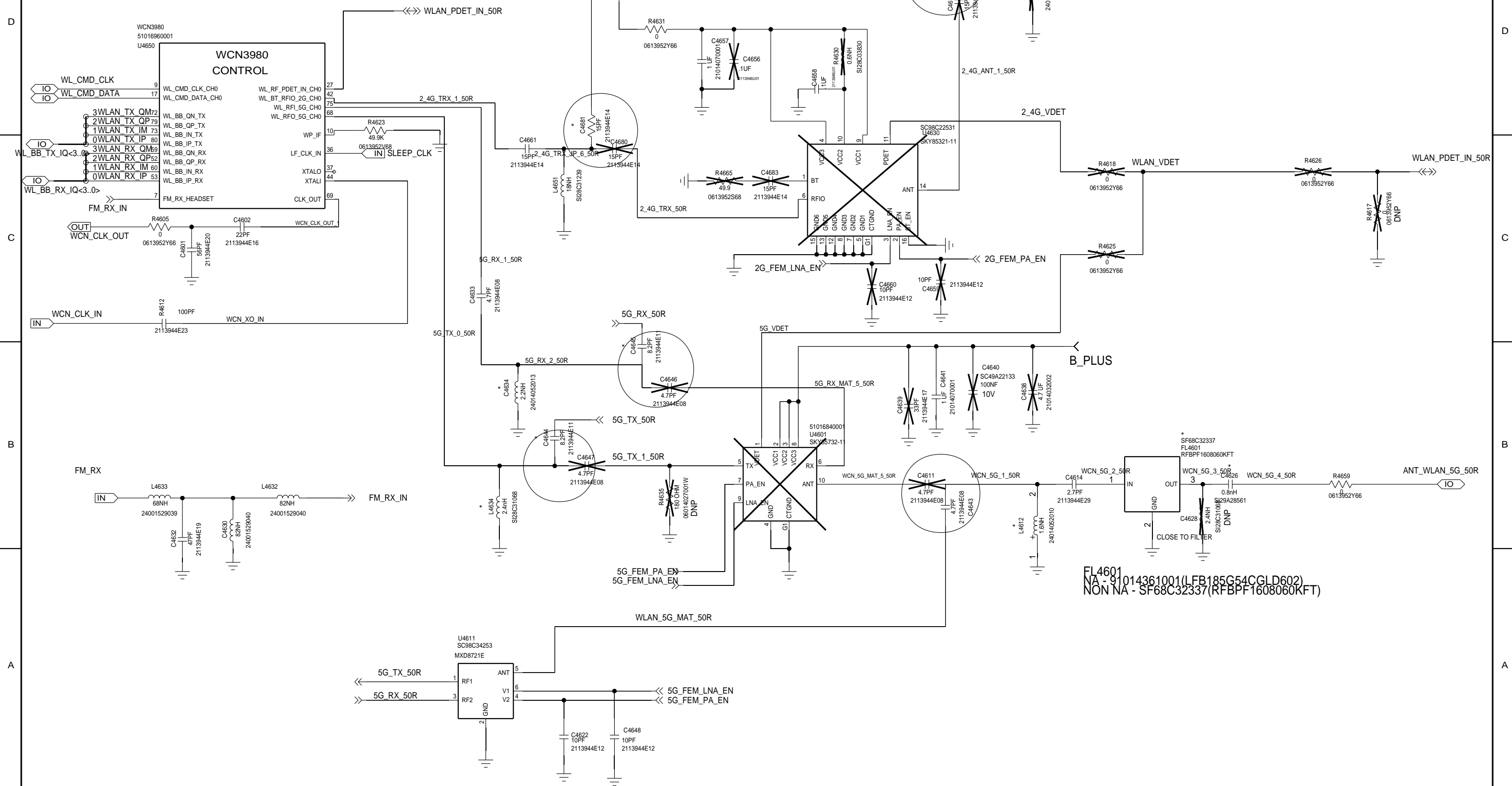
AFE: 3.5MM HEADSET

REF:4900-4949

GND SHIELD,
TRY TO AWAY FROM RFPA, CHARGER,
SMPS,AUDIO SMART PA,DDR



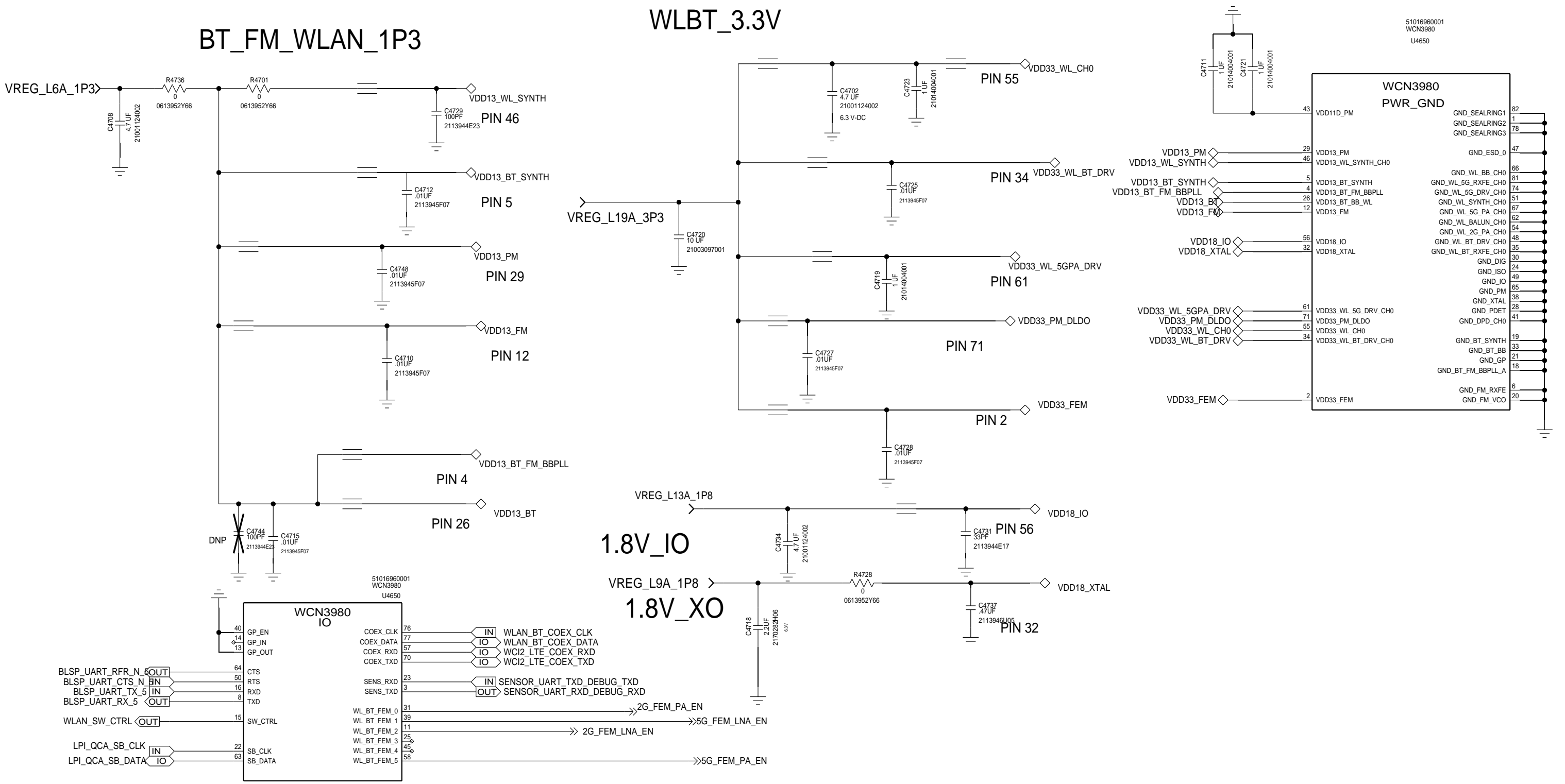
WCN3980 RF/CTRL
REF 4600-4699



BASED ON 80-WL022-41 REV. A

WCN3980 POWER AND GND,CONTROL

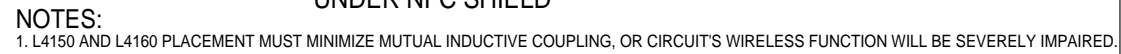
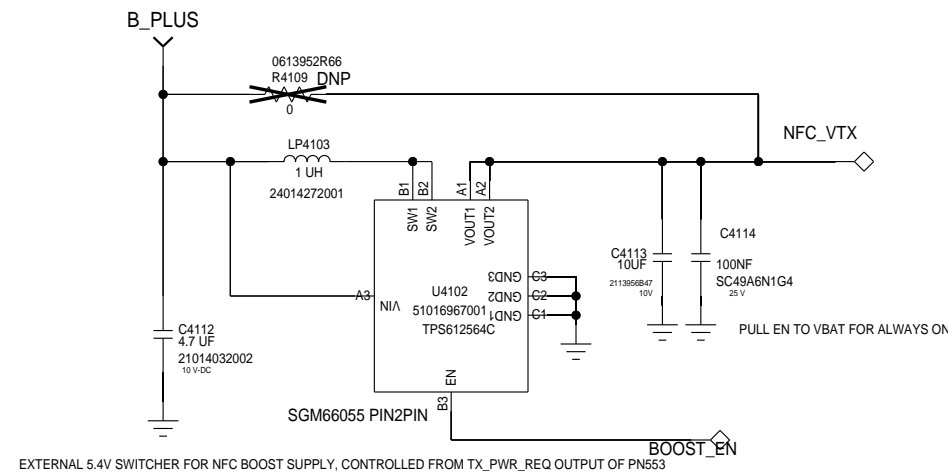
REF 4700-4799



BASED ON 80-WL022-41 REV. A

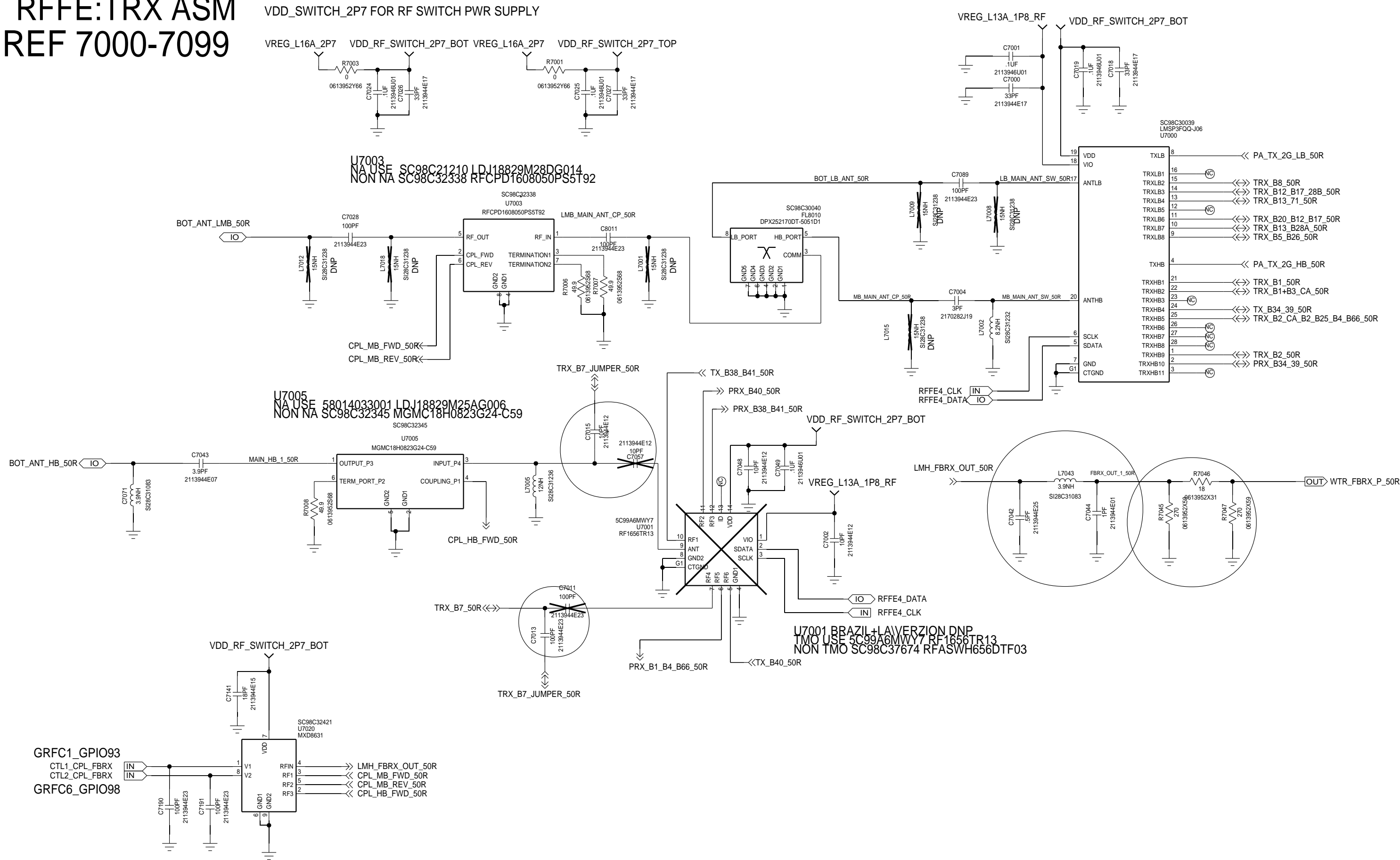
A

HIF2	HIF1	WRITE	READ
LOW	LOW	0X50	0X51
LOW	HIGH	0X52	0X53
HIGH	LOW	0X54	0X55
HIGH	HIGH	0X56	0X57

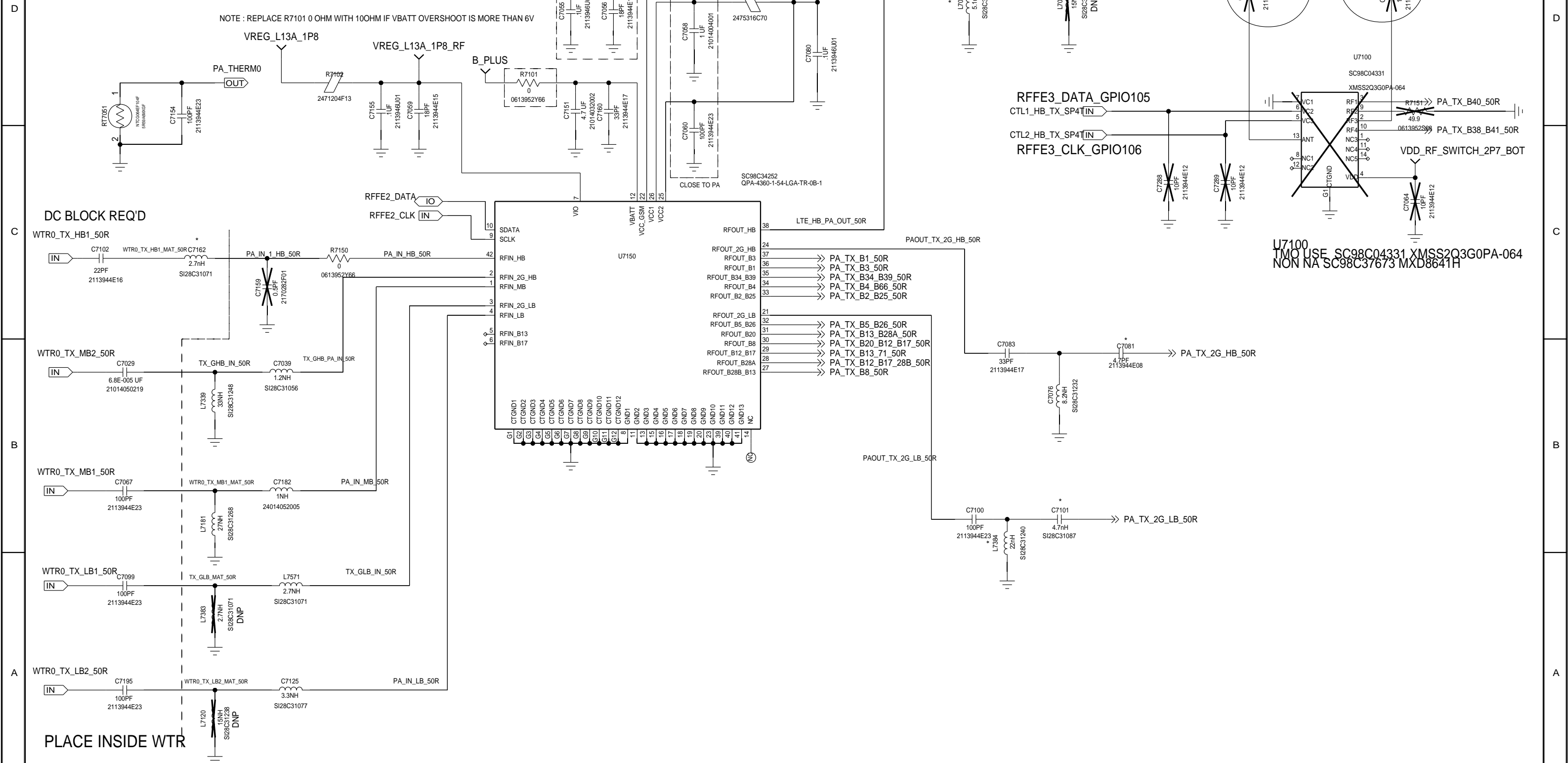


RFFE:TRX ASM

REF 7000-7099

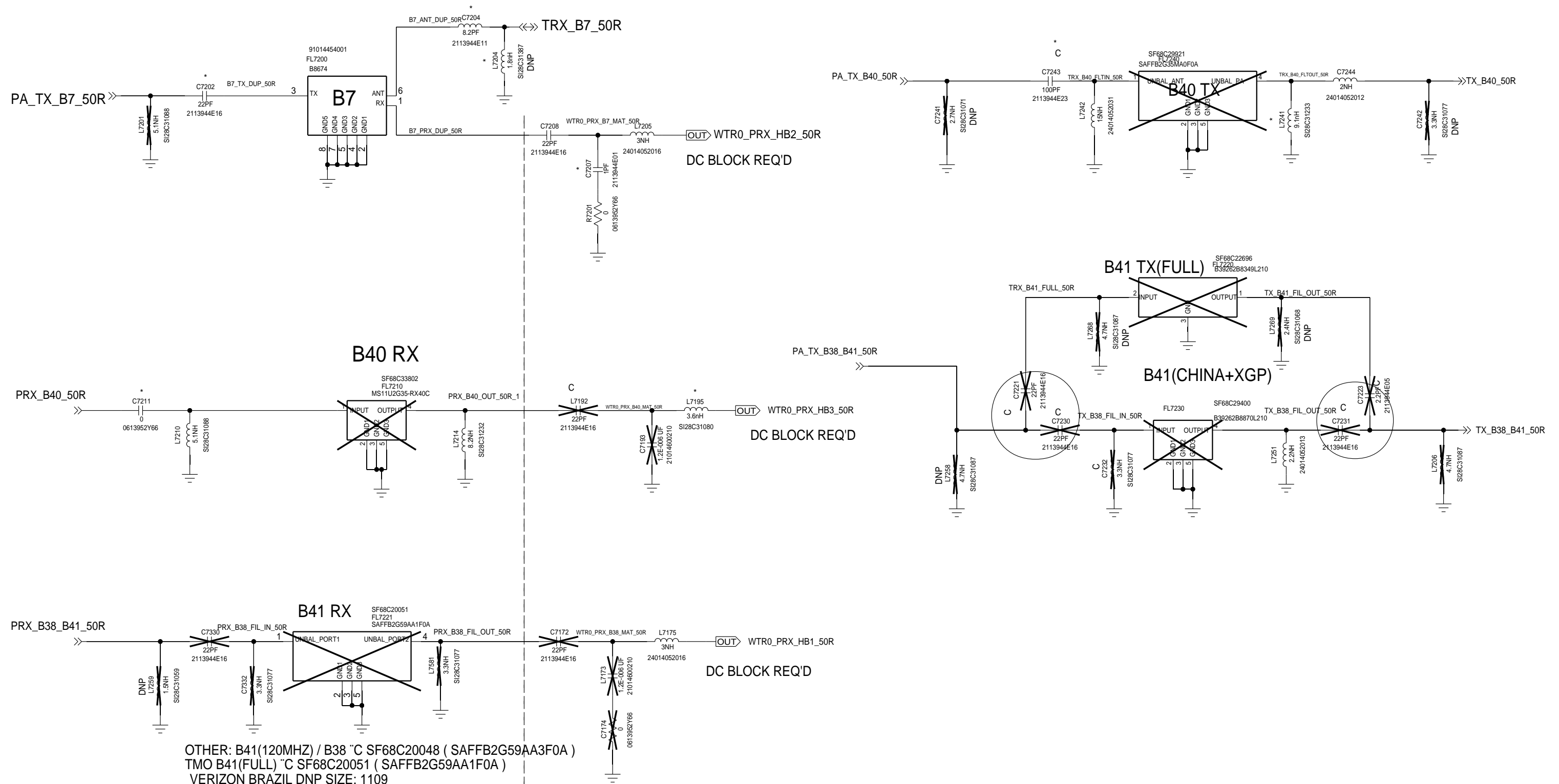


RFFE: QPA4360
REF 7150-7199



RFFE: HB_TRX_B7,38,40,41

REF 7200-7299



D



B

A

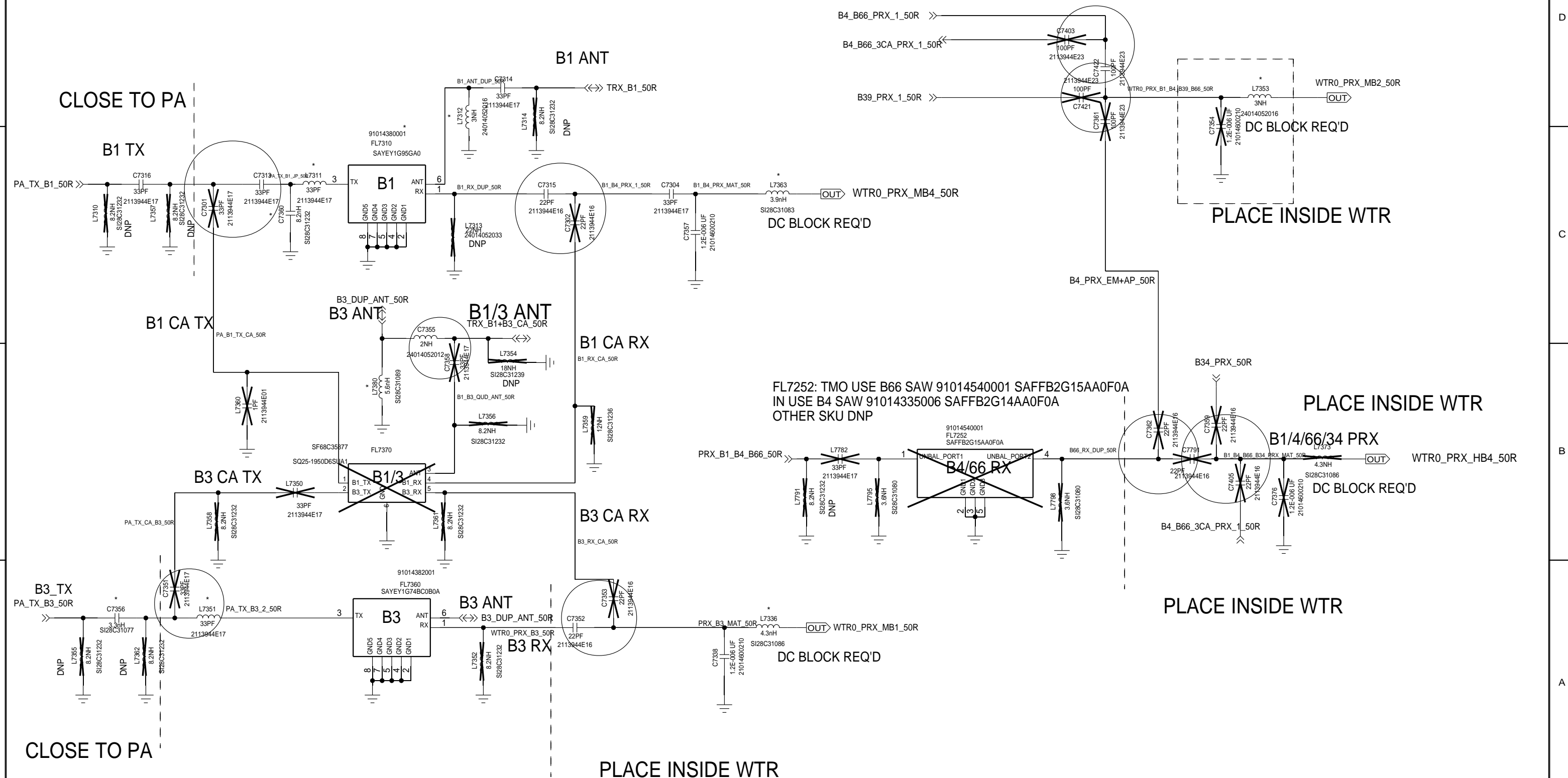
2

C

B

A

RFFE: MB_TRX_B1_3_B1/3 QUAD_G1800
REF 7350-7399



REF 7400-7499

TMO/VERIZON B12/17 91014387001 D5DÁ737M5K2H2
OTHER SKUS: B28B SF68C33257 SFX733DYJ02

DISCRETE LPF

— $\langle \leftrightarrow \rangle$ TRX_B12_B17_28B_50R

CLOSE TO PA

CLOSE TO DUP

VREG_L16A_2P7

DISCRETE LPF FOR B13

TRX_B13_B28A_50R

B13_B28A_PRX_DUP_50R

PLACE INSIDE WTR

TMO\VERIZON\WE+JANZ\IN: B20 SF68C33456 SD18-0847R8UUB1
BRAZIL+LA: B12/17 91014387001 D5DA737M5K2H2

D



A

REF 7600-7699

D

C

B

A

D

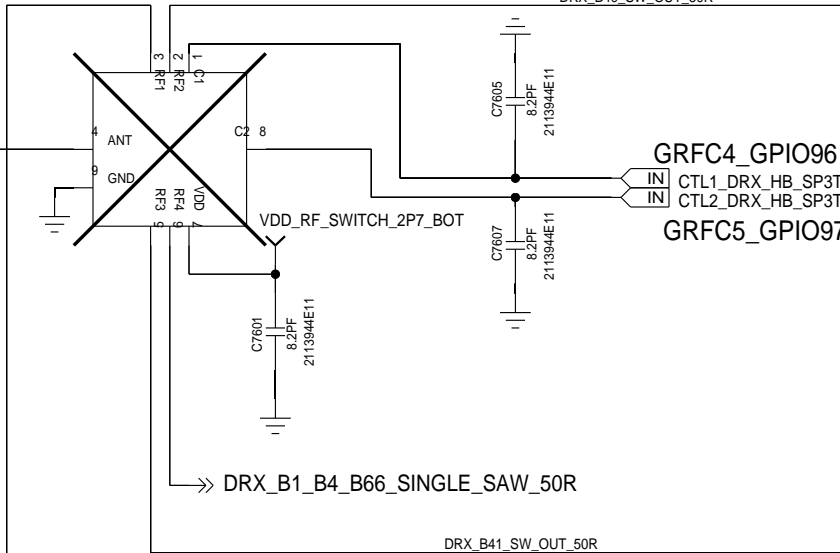
C

B

A

U7610
NA: SP4T RF1648B
EM+AP+IN+WE+JAPAN: RF1621
BRAZIL+LA: DNP

RF_SWITCH
SC98C29925
U7610
RF1648BTR13-5K



DRX_HB_MAT_5_50R

DRX_B7_JP_50R

TOP_ANT_HLB_INNER_50R

TOP_ANT_HLB_SURFACE_50R

DRX_ANT_HB_50R

DRX_ANT_LB_50R

DRX_B5_26_28_50R

DRX_B8+20_B5+8_50R

GRFC4_GPIO96
CTL1_DRX_HB_SP3T
CTL2_DRX_HB_SP3T
GRFC5_GPIO97

W_GRFC_1
CTL2_DRX_LB_SP4T
CTL1_DRX_LB_SP4T
W_GRFC_0

NON NA: B41(120MHZ) / B38 °C SF68C20048 (SAFFB2G59AA3F0A)
TMO B41(FULL) °C SF68C20051 (SAFFB2G59AA1F0A)
VERIZON\BRAZIL+LA DNP SIZE: 1109

B40
SF68C33802
FL7620
MS11U2G35-RX40C

B7
FL7660
SF68C33455

PLACE INSIDE WTR

DC BLOCK REQ'D

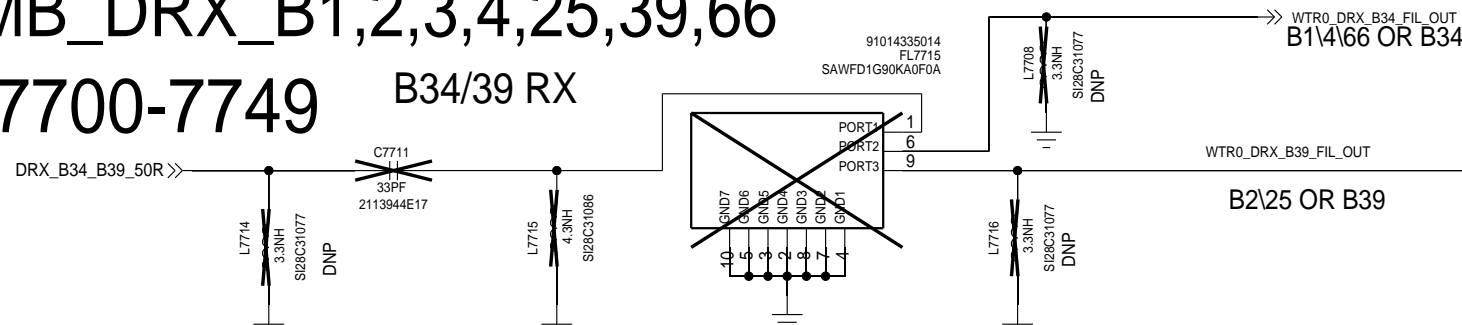
DC BLOCK REQ'D

DC BLOCK REQ'D

U7660
NA(SP4T): RF1648B SC98C29925
OTHER SKUS: SP3T RF1621

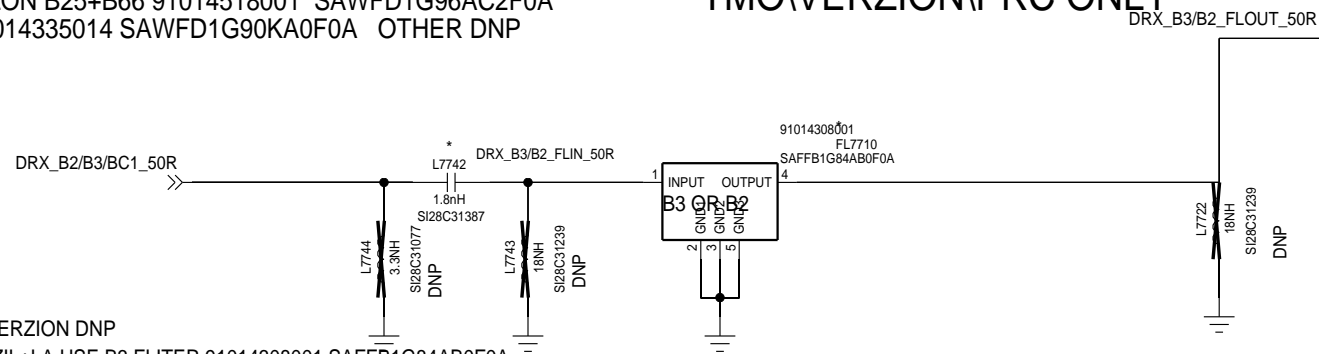
RFFE: MB_DRX_B1,2,3,4,25,39,66

REF 7700-7749 B34/39 RX



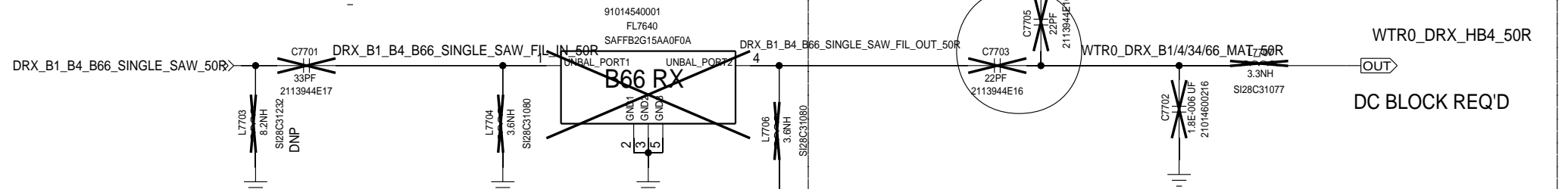
FL7715:TMO\VERIZON B25+B66 91014518001 SAWFD1G96AC2F0A
PRC B34+B39 91014335014 SAWFD1G90KA0F0A OTHER DNP

TMO\VERZION\PRC ONLY

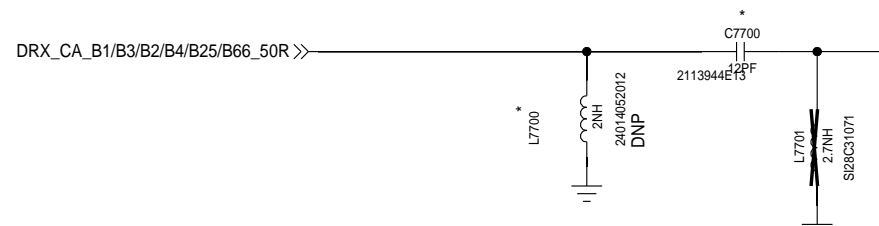


FL7710:VERZION DNP
TMOIBRAZIL+LA USE B3 FLITER 91014308001 SAFFB1G84AB0F0A
OTHER SKUS USE B2 FLITER 91014335005 SAFFB1G96AB0F0A

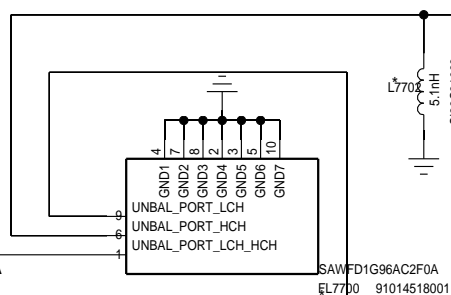
ALL SKUS DNP



B66/B4 OR B34

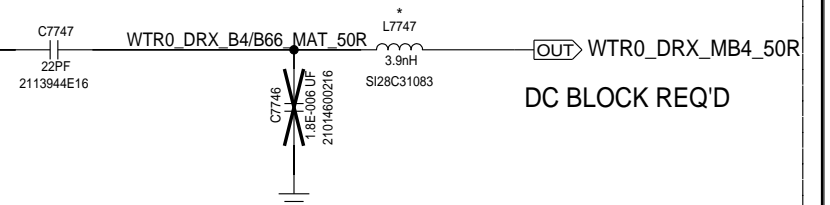


FL7700 :TMO USE B34+B39 DUAL SAW 91014335014 SAWFD1G90KA0F0A
FL7700 :BRAZIL+LA USE B25+B66 DUAL SAW 91014518001 SAWFD1G96AC2F0A
FL7700 :OTHER SKUS USE B1+B3 DUAL SAW 91014424001 SAWFD1G84AA0F0A

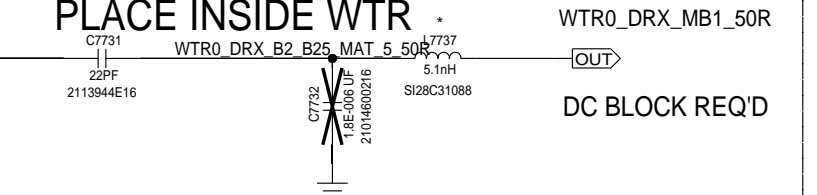


B25+B66 OR B1+B3 OR B34+B39

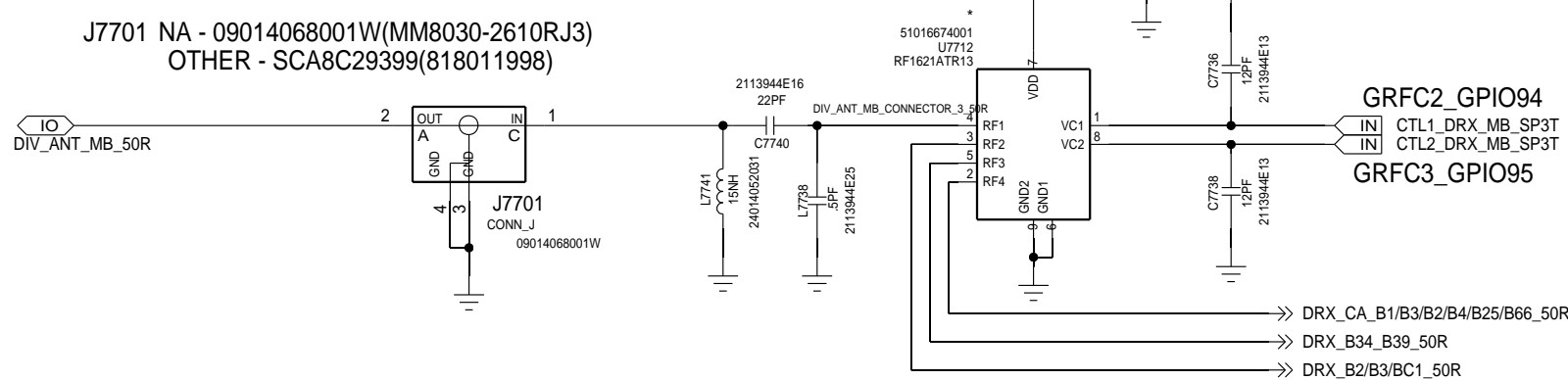
B25/B2 OR B39



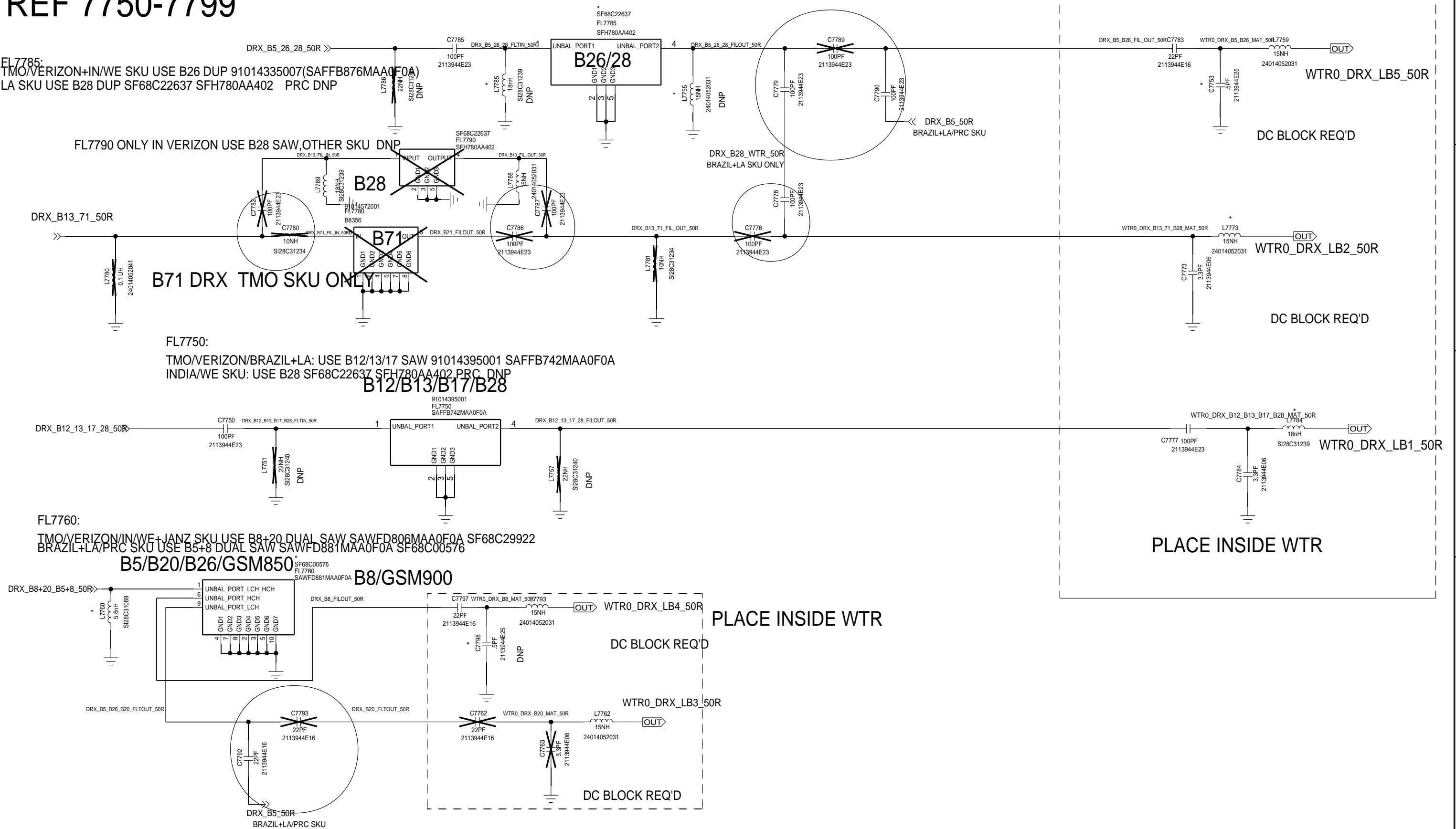
PLACE INSIDE WTR



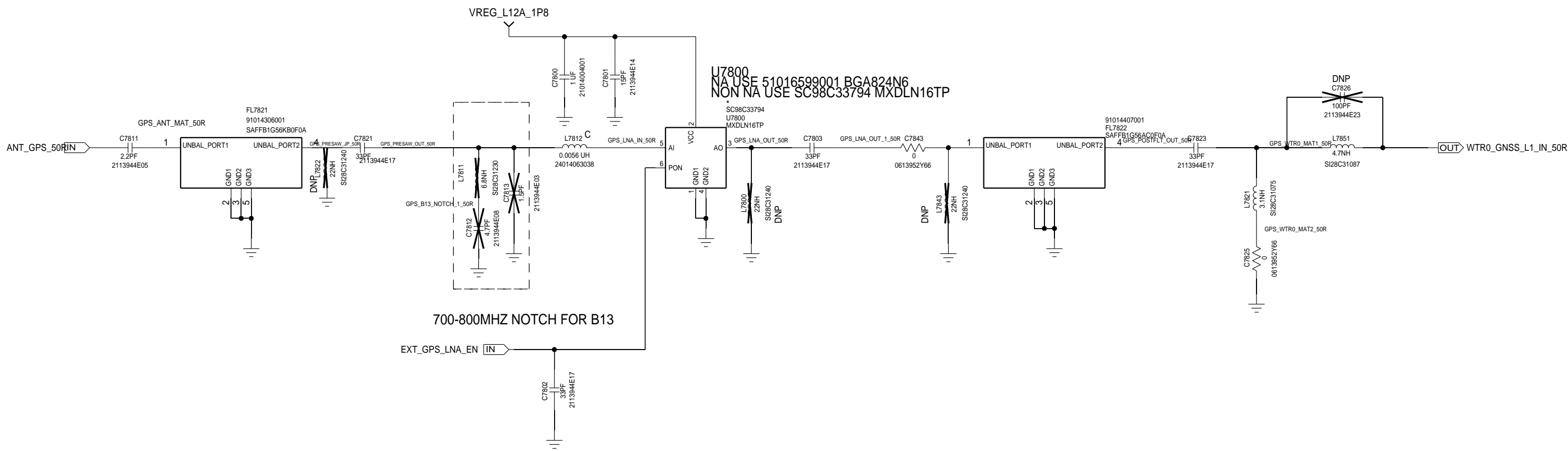
J7701 NA - 09014068001W(MM8030-2610RJ3)
OTHER - SCA8C29399(818011998)



RFFE: LB_DRX_B5,8,12,13,17,20,26,28
REF 7750-7799



RFFE: GPS
REF7800-7899



D



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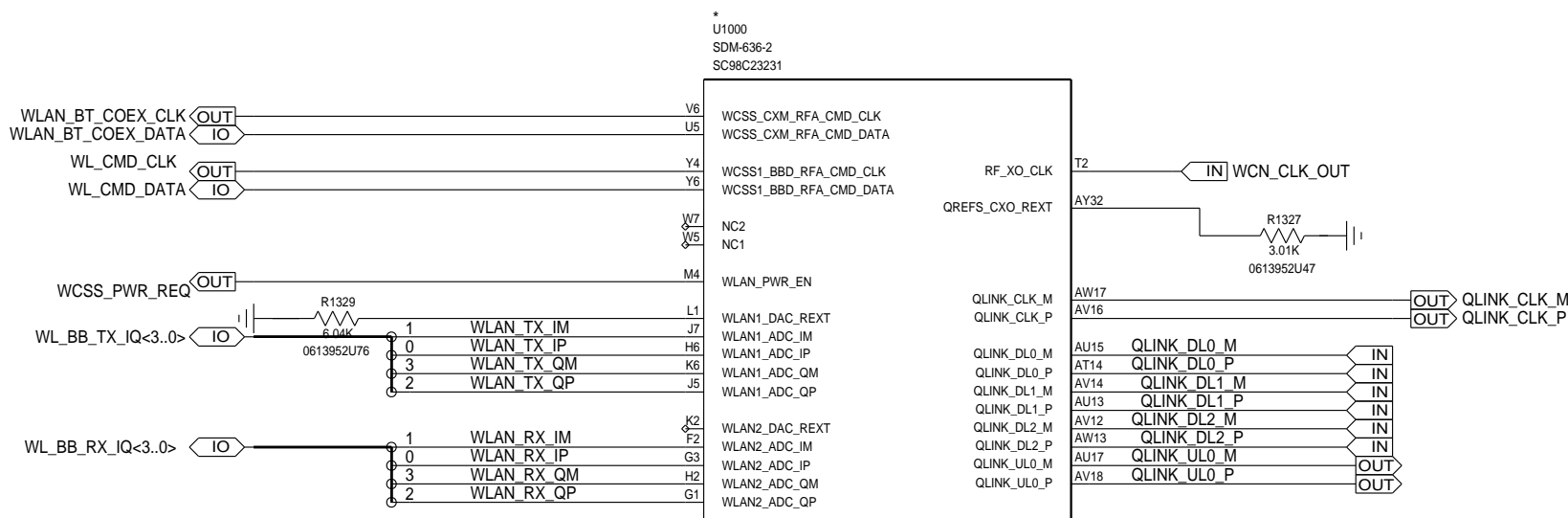
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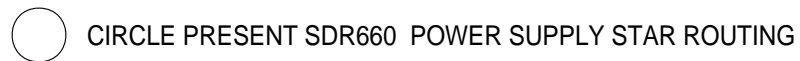
MSM: RF INTERFACE

REF 1200-1299



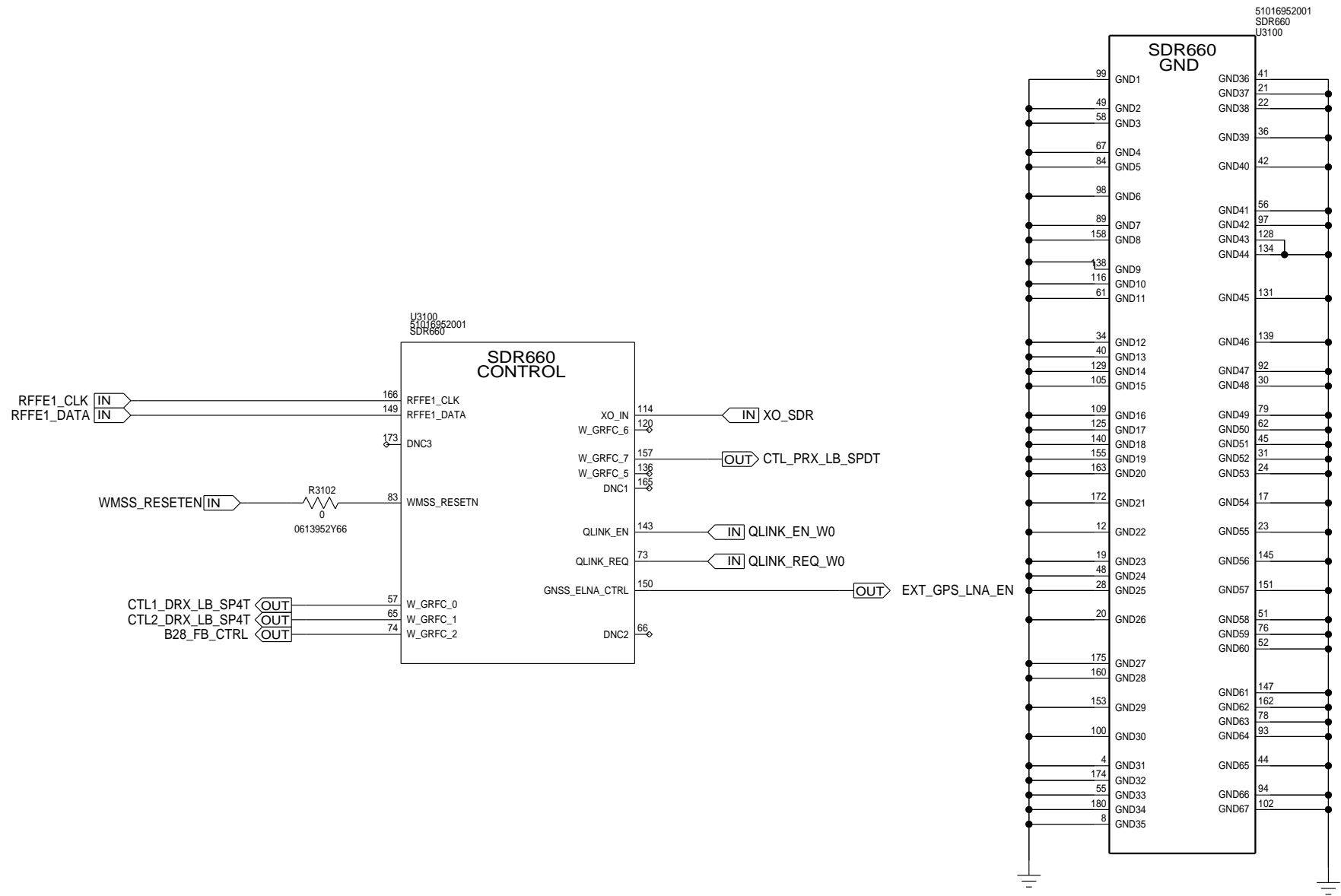
NEED TO APPLY LC FILTER FOR REDUCE B40 RX SPURS(QC 80-NP237-56)
LC FILTER COULD BE PLACED ANYWHERE BETWEEN WTR BBRX_IQ AND MSM BBRX_IQ PINS
BUT THE GND FOR SHUNT C SHOULD NOT BE SHARED WITH EITHER WTR OR MSM GND ON ANY LAYER.
A DIRECT VIA FROM SHUNT C TO MAIN GND IS RECOMMENDED

REF 3000-3099



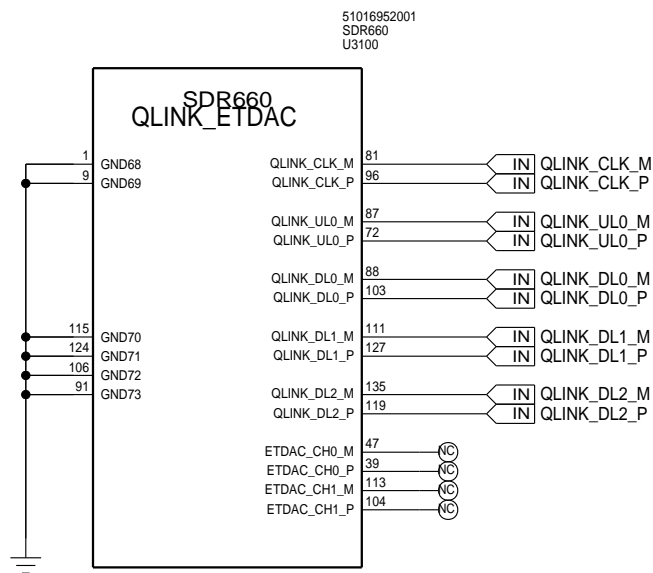
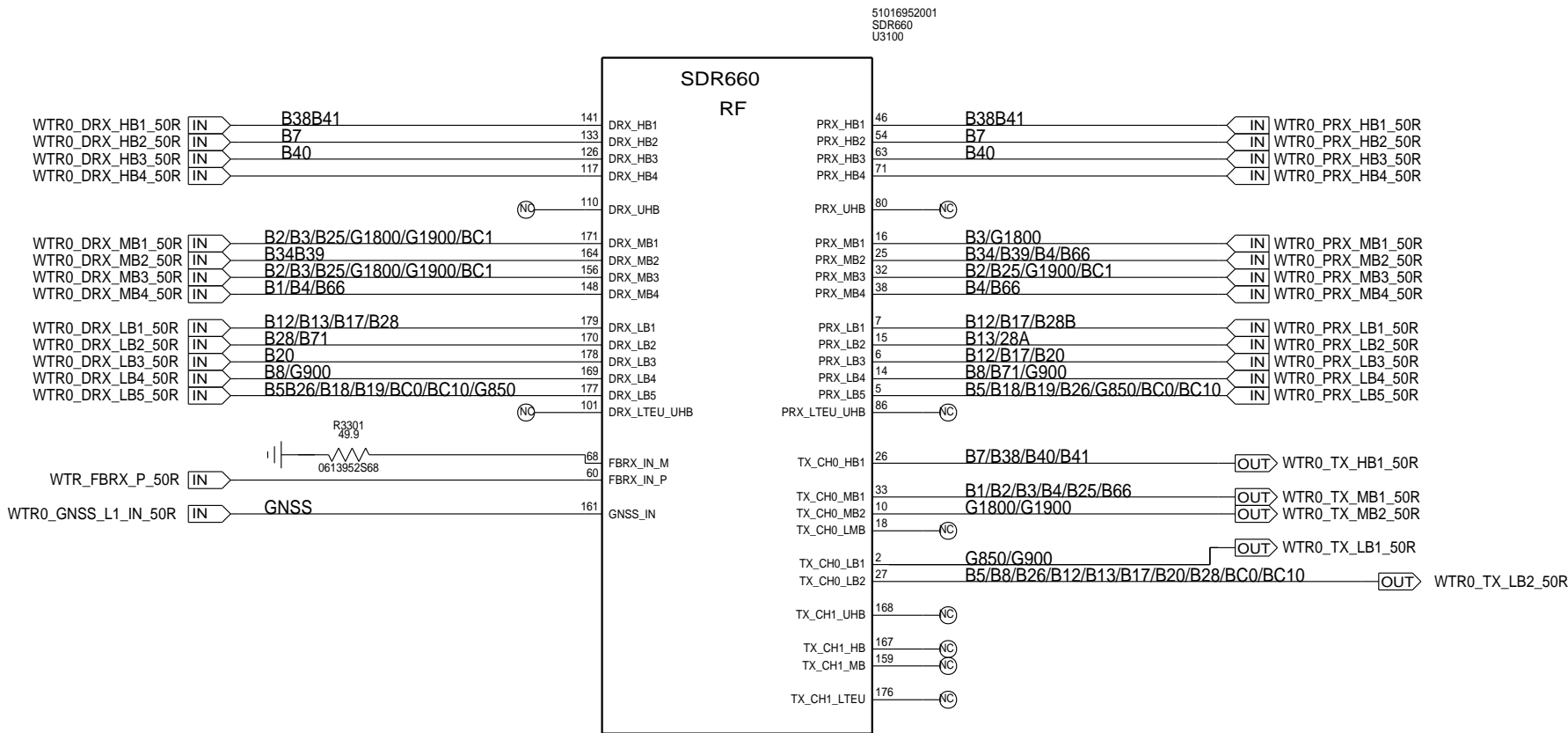
SDR660: TX/PWR/GROUND

REF 3100-3199



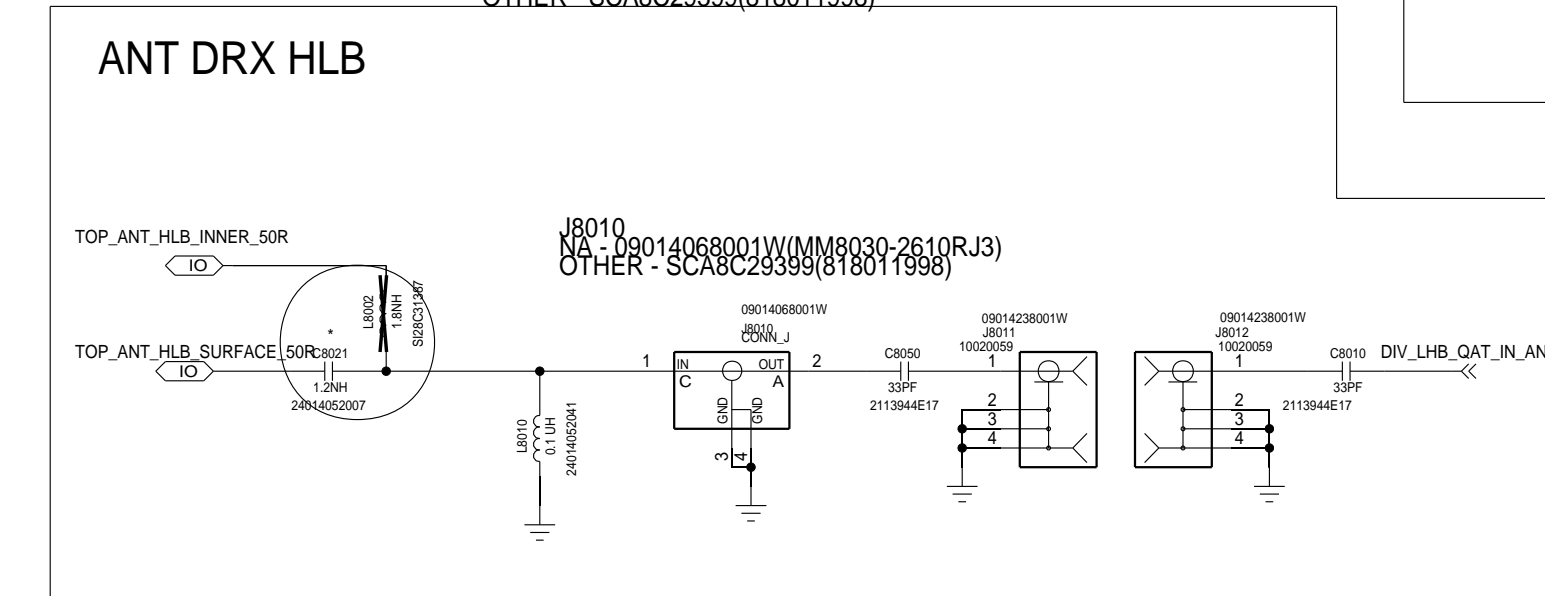
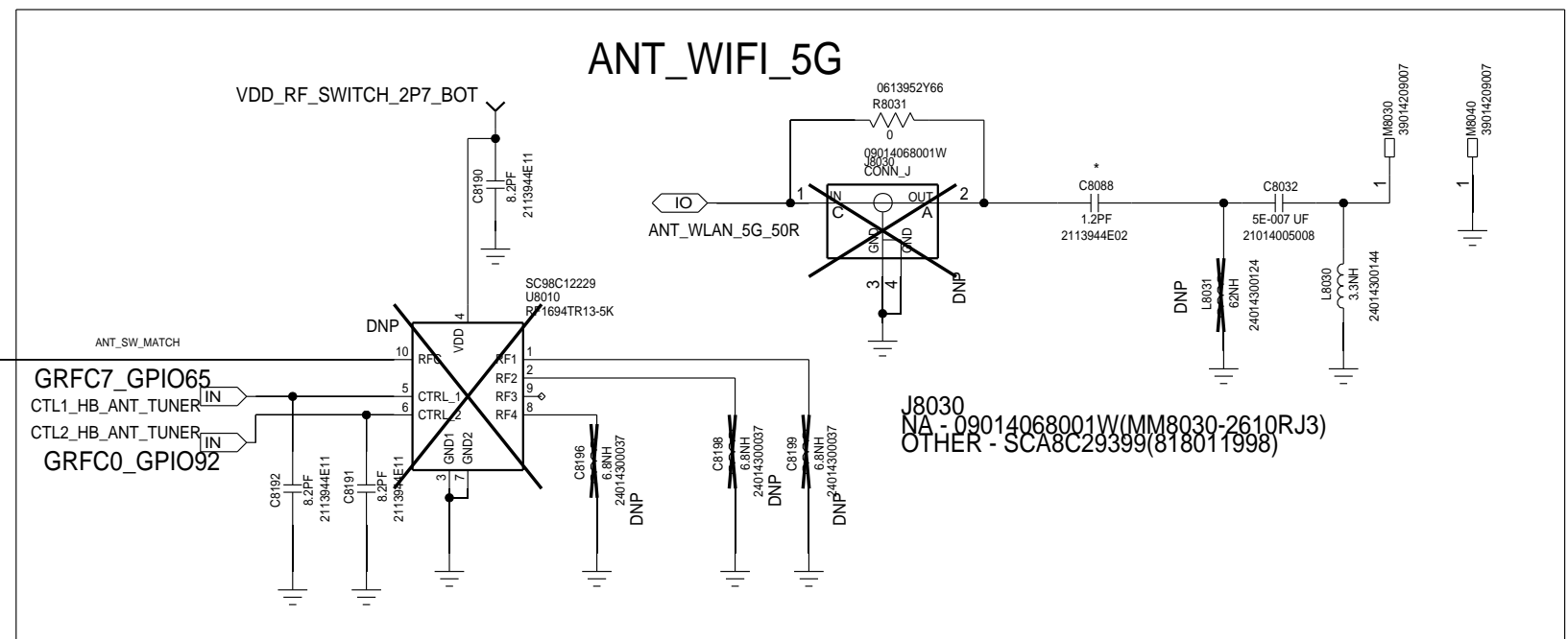
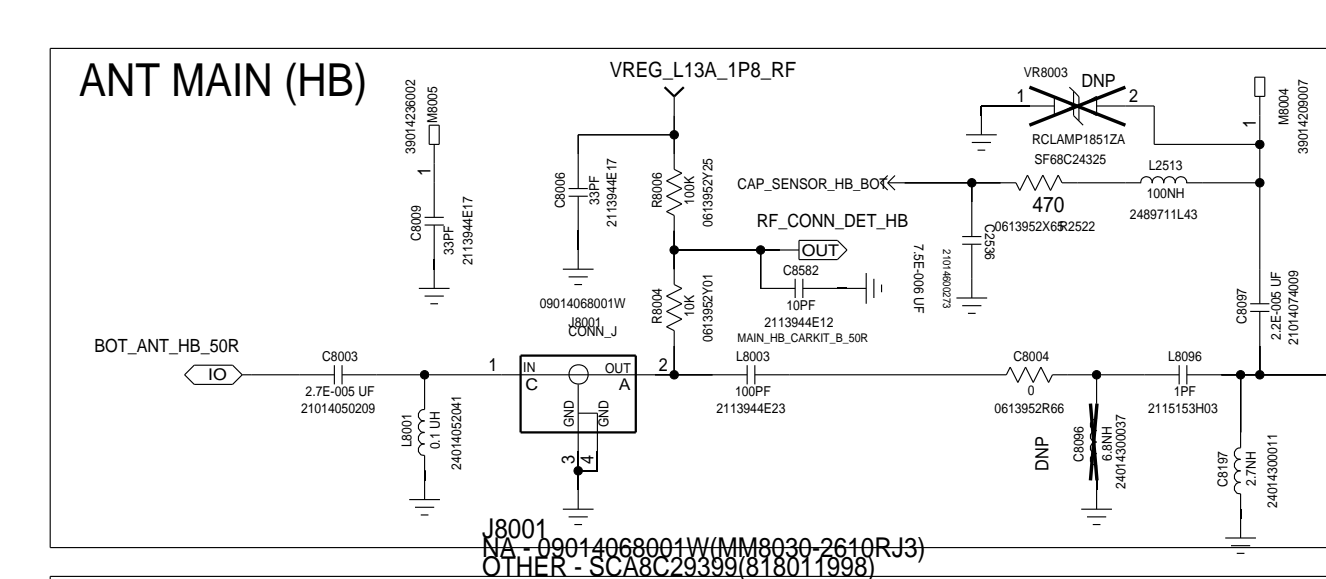
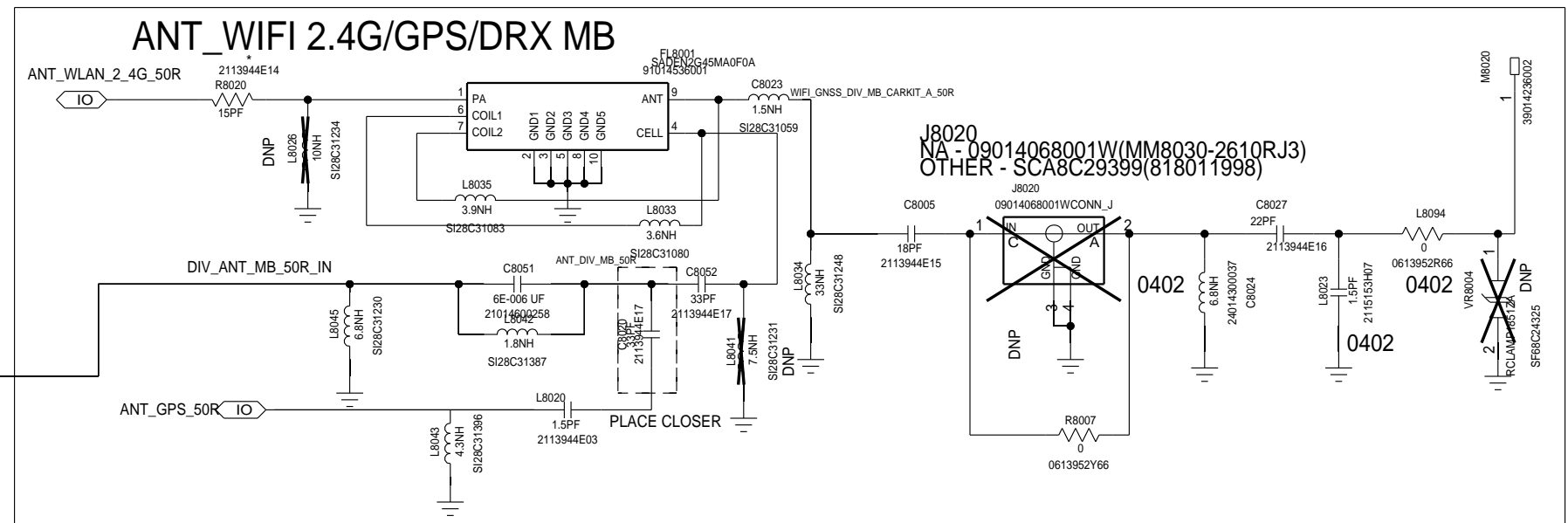
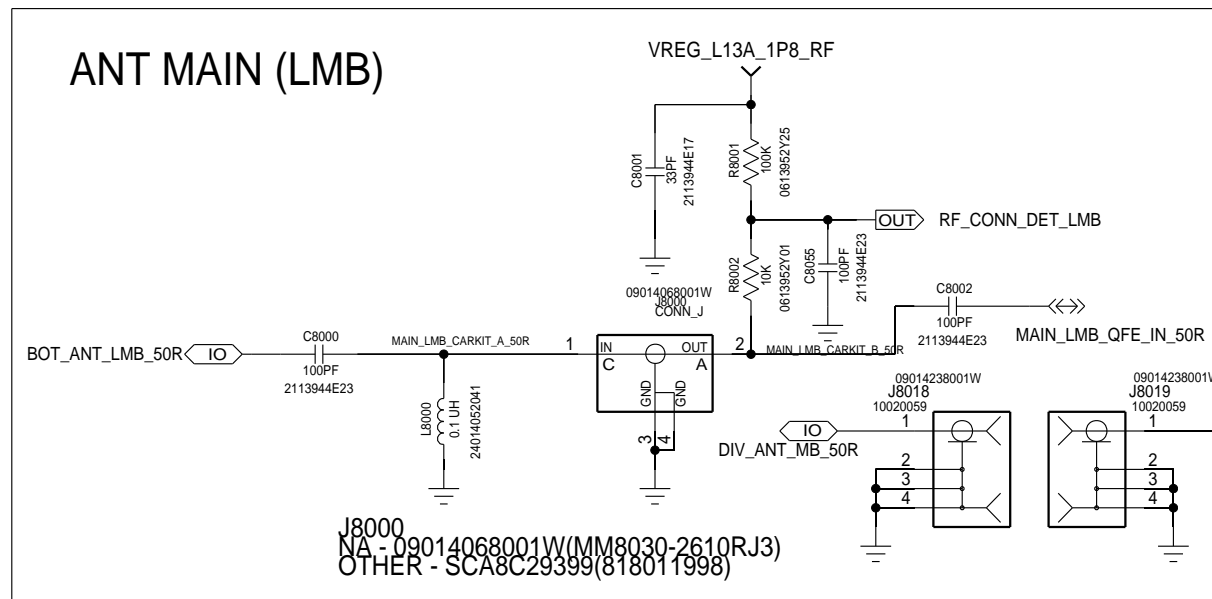
SDR660: PRX/DRX/GPS

REF 3200-3299

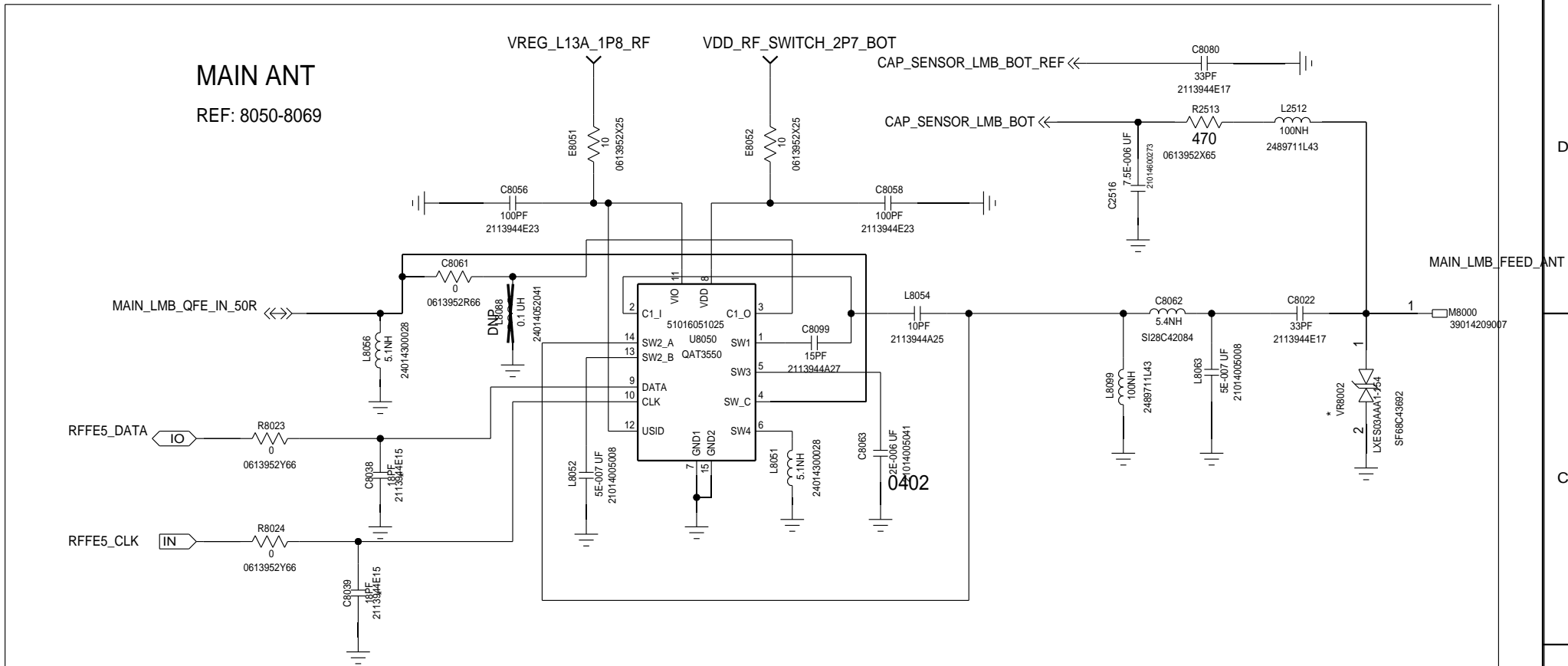


8000:antennas

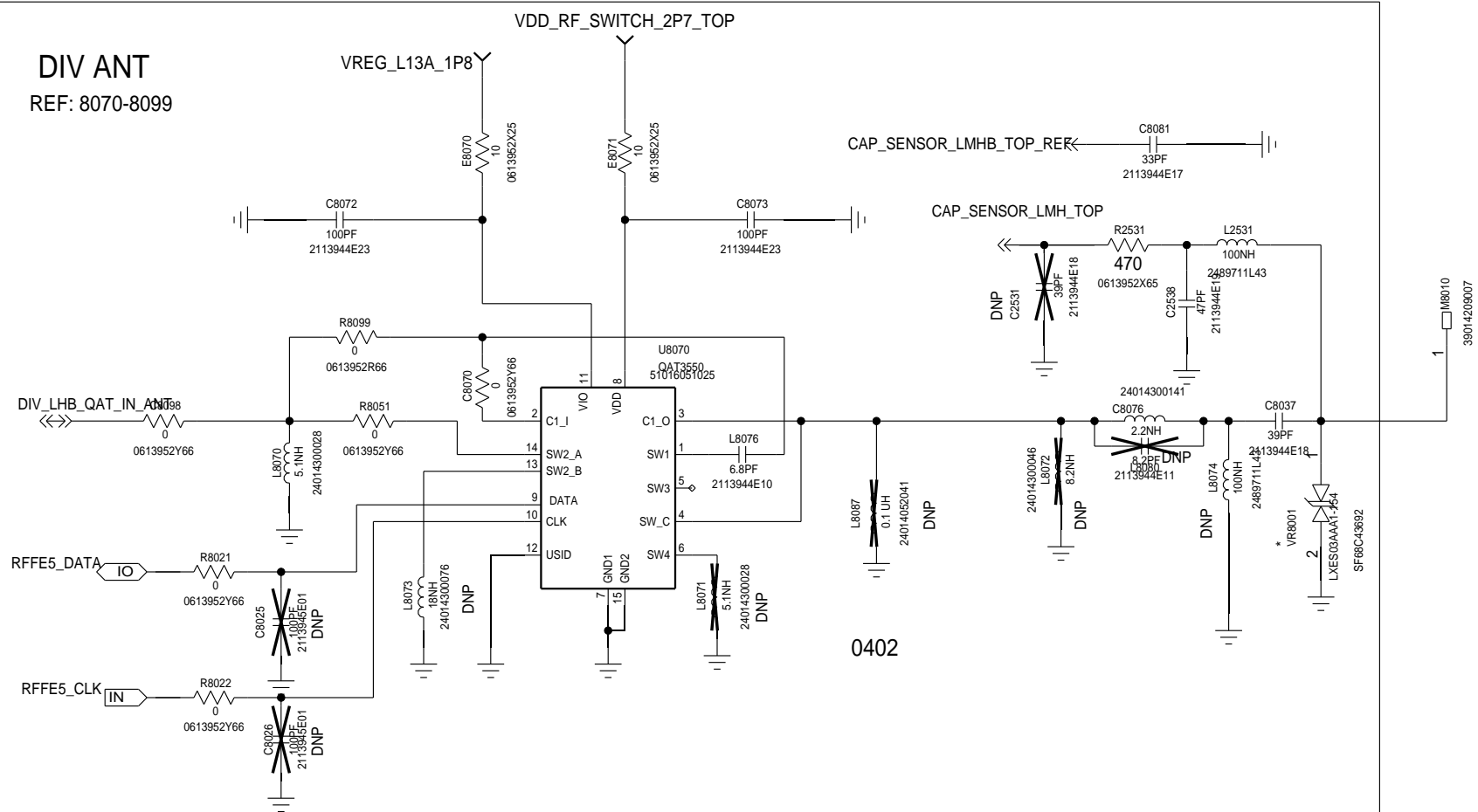
REF 8000-8049



REF 8050-8099



DIV ANT REF: 8070-8099



RFFE: CAP SENSOR
REF 2510-2530

The schematic diagram illustrates the RFFE CAP SENSOR circuit, showing the connection of the CAP SENSOR (SC98C30422) to various input signals and power sources. The sensor has pins for CSIO4, CSIO3, CSIO2, CSIO1, CSIO0, VDD, SCL, SDA, NIRQ, and GND. It is connected to CAP_SENSOR_LMHB_TOP_REF, CAP_SENSOR_LMB_BOT_REF, CAP_SENSOR_HB_BOT, CAP_SENSOR_LMB_BOT, and CAP_SENSOR_LMH_TOP. The circuit includes resistors R2515, R2524, R2516, R2511, R2512, R2510, and R2517. It also shows power regulation sections VREG_L8B_3P3 and VREG_L13A_1P8 with capacitors C2529, C2530, C2531, and C2532. The diagram is labeled with 'DNP' for 'Do Not Populate' components.

CAP SENSOR

VREG_L8B_3P3

VREG_L13A_1P8

CSIO4
CSIO3
CSIO2
CSIO1
CSIO0
VDD
SCL
SDA
NIRQ
GND

IN
IO
OUT

BLSP2_I2C_SCL
BLSP2_I2C_SDA
SAR_SENSOR_INT_N

CAP_SENSOR_LMHB_TOP_REF
CAP_SENSOR_LMB_BOT_REF
CAP_SENSOR_HB_BOT
CAP_SENSOR_LMB_BOT
CAP_SENSOR_LMH_TOP

R2515
R2524
R2516
R2511
R2512
R2510
R2517

C2529
C2530
C2531
C2532

D2504
D2505
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