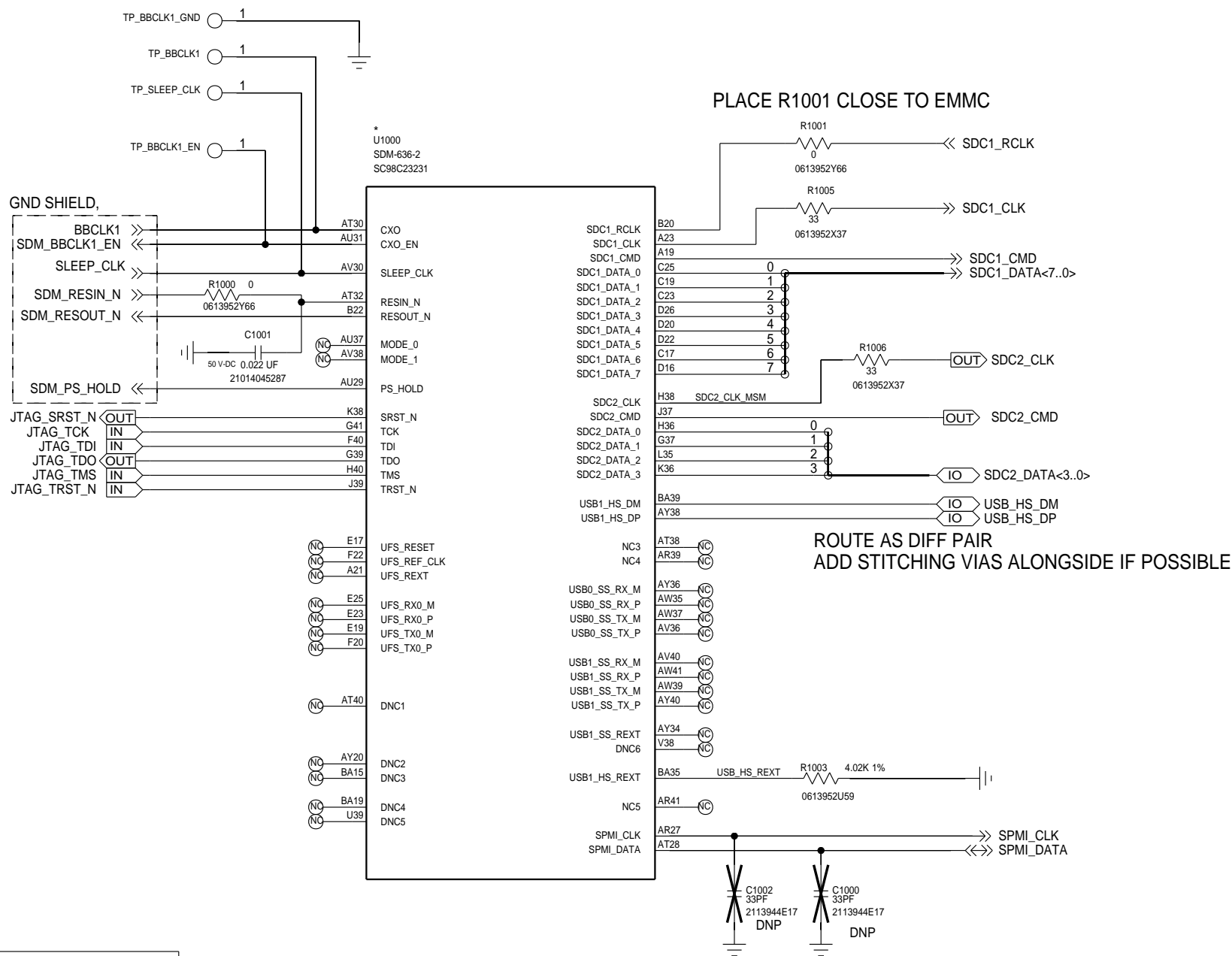


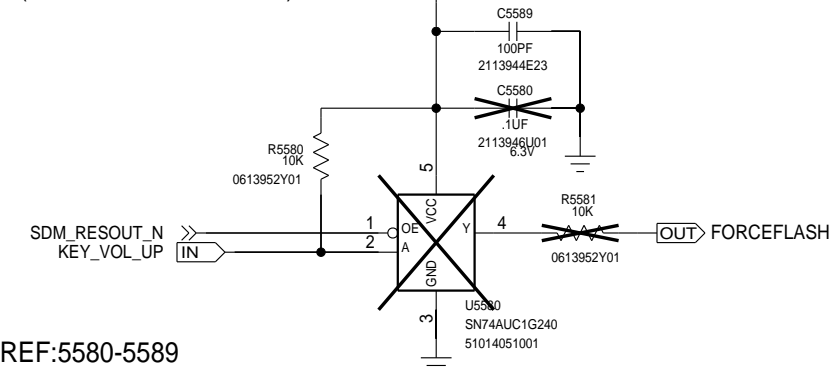
# SDM: CTRL

REF 1000-1099



## FORCE FLASH SUPPORT WITH VOL UP KEY

(DNI FOR PRODUCTION)



# SDM: MIPI/DSI/CSI/EBI0

## REF 1200-1299

D

D

C

C

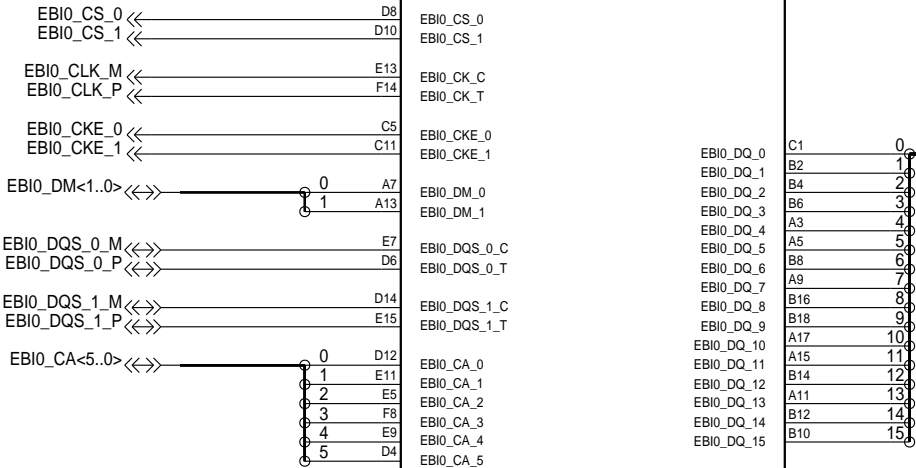
B

B

A

A

\*  
U1000  
SDM-636-2  
SC98C23231



GND SHIELD FOR EBI0 AS A GROUP IF POSSIBLE

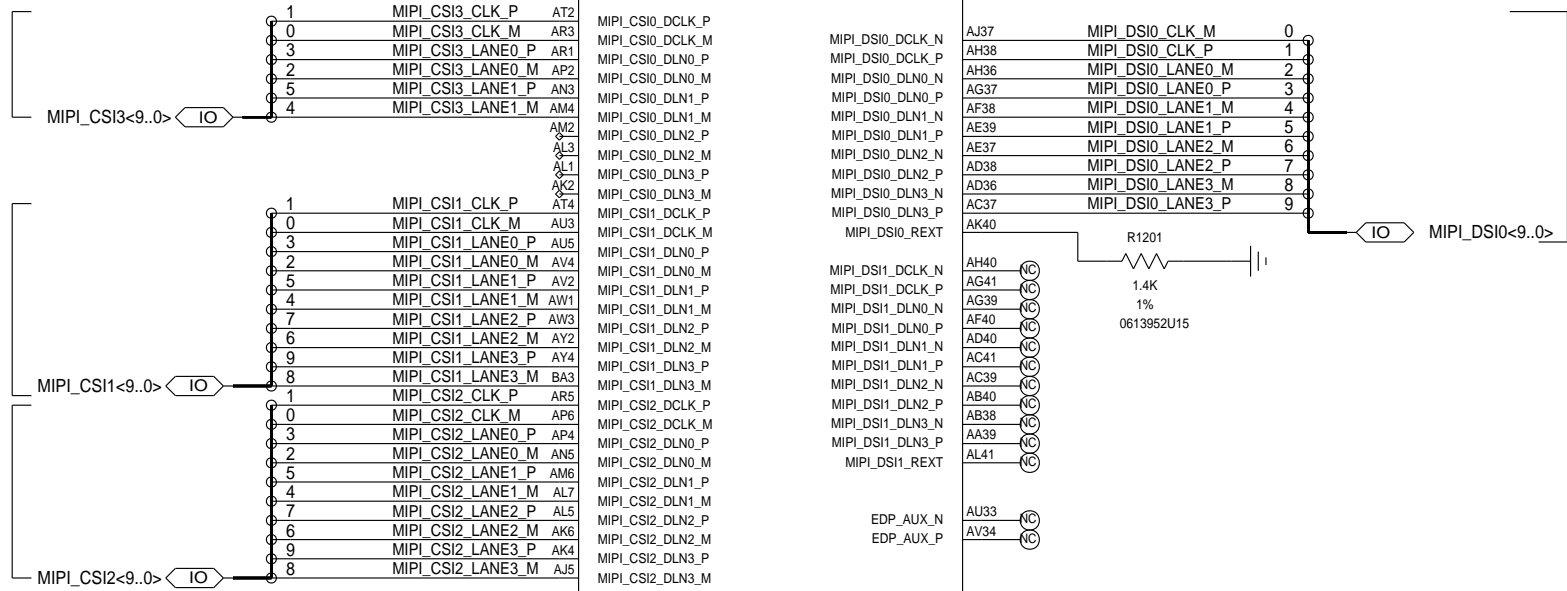
GND SHIELD FOR CSI3 AS A GROUP IF POSSIBLE

REAR AUX CAMERA

REAR CAMERA

FRONT CAMERA

\*  
U1000  
SDM-636-2  
SC98C23231



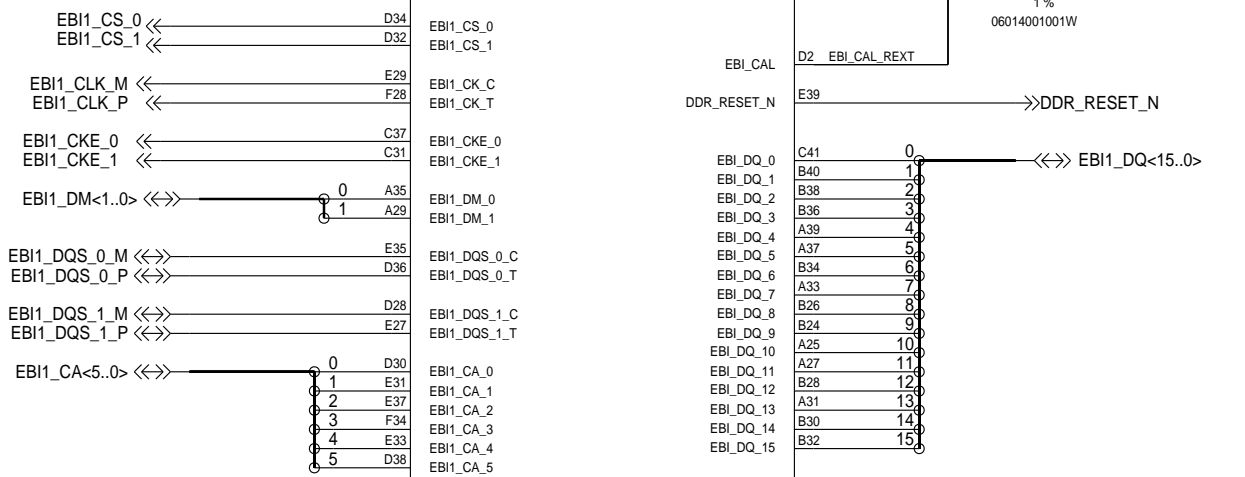
DISPLAY

GND SHIELD FOR CSI2 AS A GROUP IF POSSIBLE  
GND SHIELD FOR CSI1 AS A GROUP IF POSSIBLE

GND SHIELD FOR DSI0 AS A GROUP IF POSSIBLE

ADD GND STITCHING VIAS FOR EACH SIGNAL GROUP (EBI,CSI,DSI) WHEN POSSIBLE

\*  
U1000  
SDM-636-2  
SC98C23231



GND SHIELD FOR EBI1 AS A GROUP IF POSSIBLE

# SDM: GPIO (0-141) REF 1200-1299

D

C

B

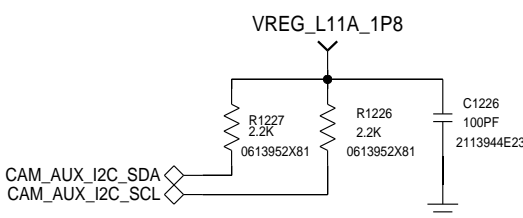
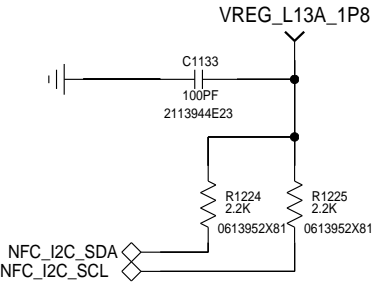
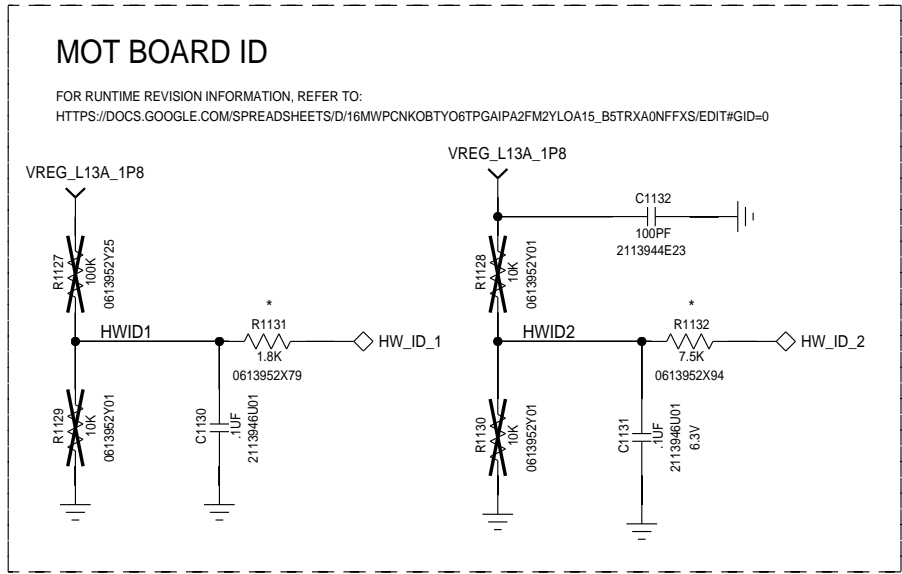
A

D

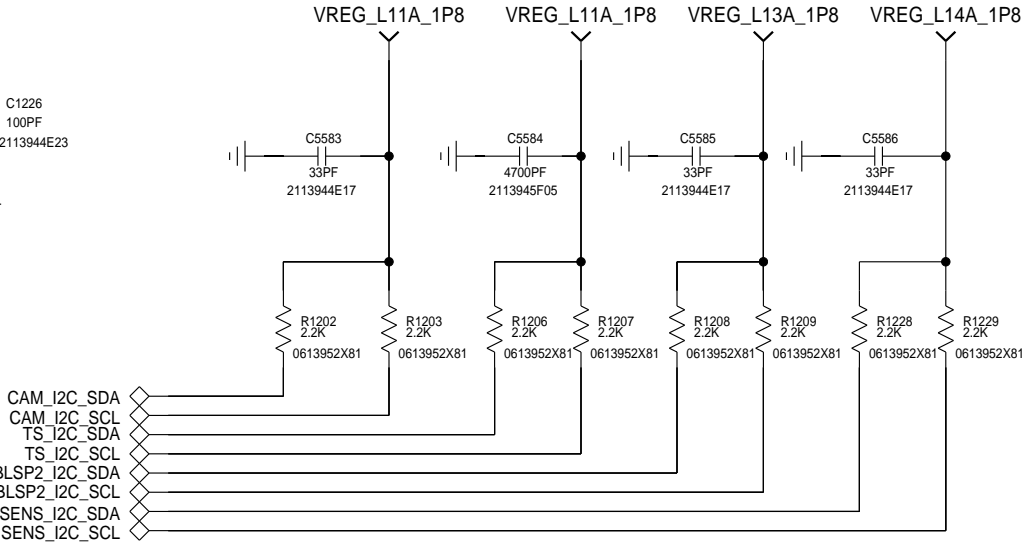
C

B

A



I2C2 ADDRESS	
DRV2624	0xBA,0XB5
AW8690	0XB6,0XB7
SAR9331	0X50,0X51
SAR9325	0X50,0X51
SMART PA TOP	0X82,0X83
SMART PA	0X80,0X81
BQ25970	0X65,0X67
RT5738	0XAC,0XAD
TPS61280	0XEA,0XEB
PM3003A	0XC0,0XC1
BQ27426	0XAA,0XAB

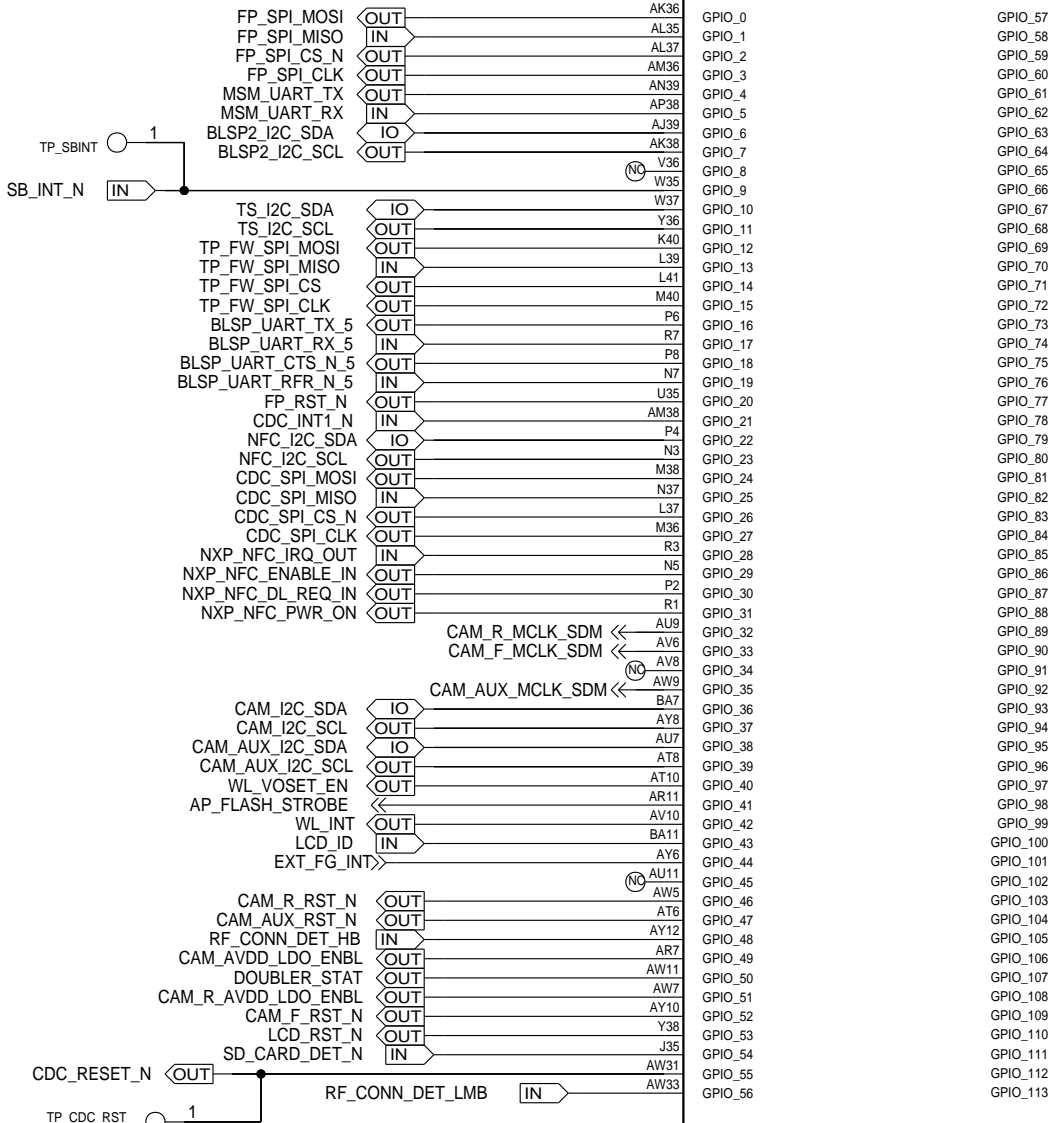


\* U1000  
SDM-636-2  
SC98C23231

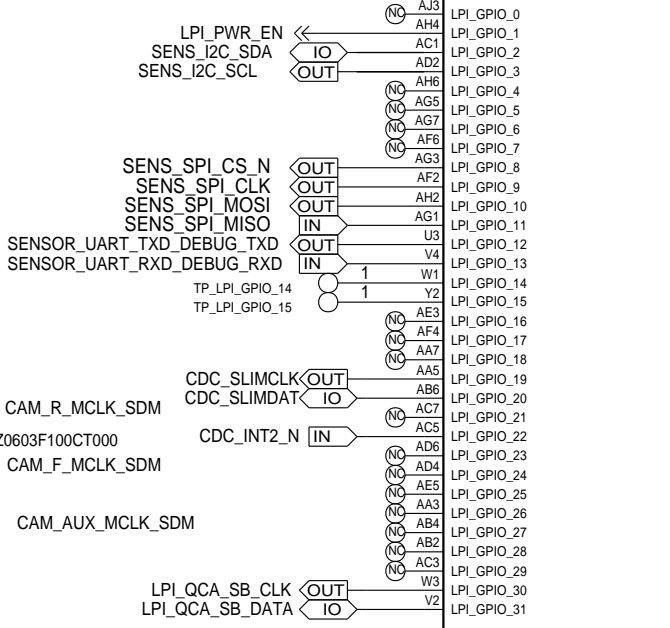
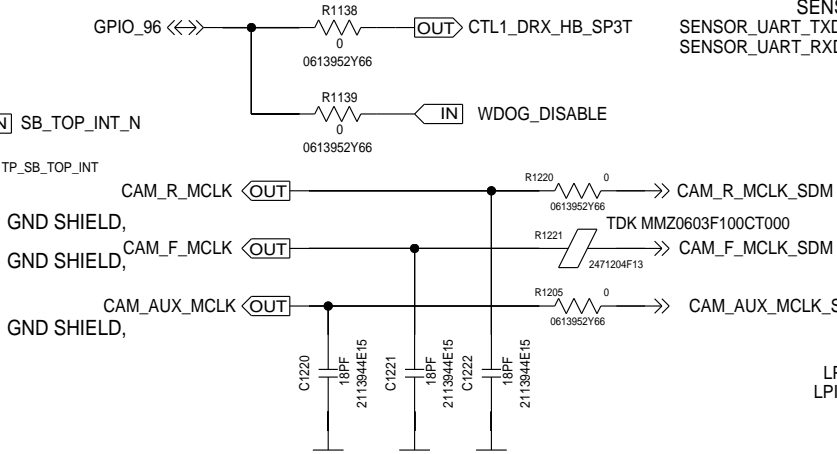
GPIO92,  
GPIO93,  
GPIO64,  
GPIO63,  
GPIO113,  
GPIO53,  
FOR SECURE BOOT, MAKE SURE NO PULLUP.

GPIO96,  
GPIO97,  
GPIO98,  
GPIO60,  
GPIO111,  
FAST BOOT DEFAULT 0X00, MAKE SURE NO PULLUP.

\* U1000  
SDM-636-2  
SC98C23231



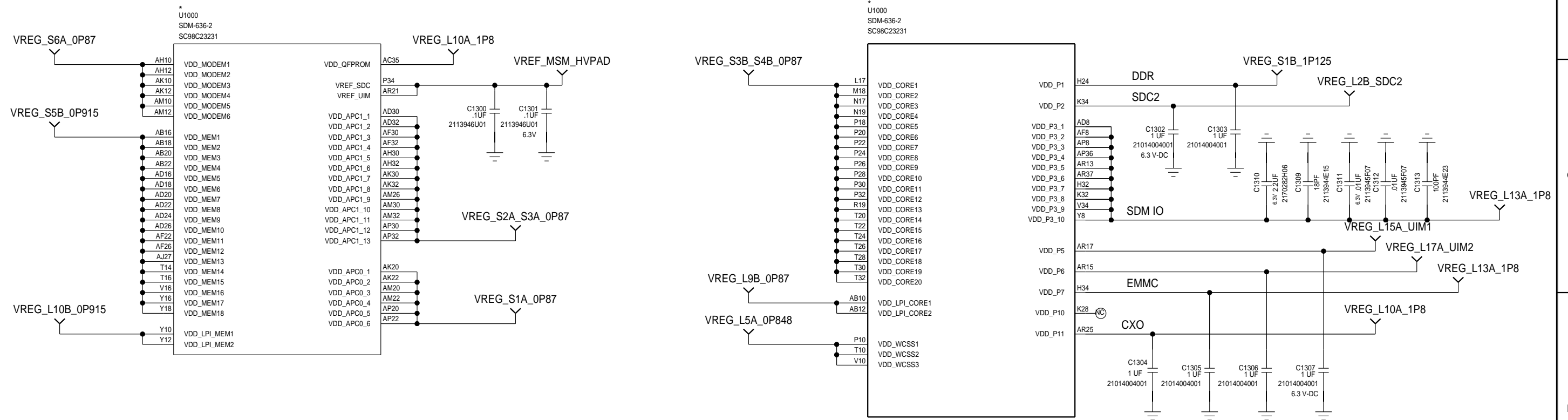
ENSURE THAT THERE ARE NO EXTERNAL PULLS ON THE GPIOs (92, 93, 64, 63,113, AND 53) IF SECURE BOOT IS NOT REQUIRED



# SDM: PWR1

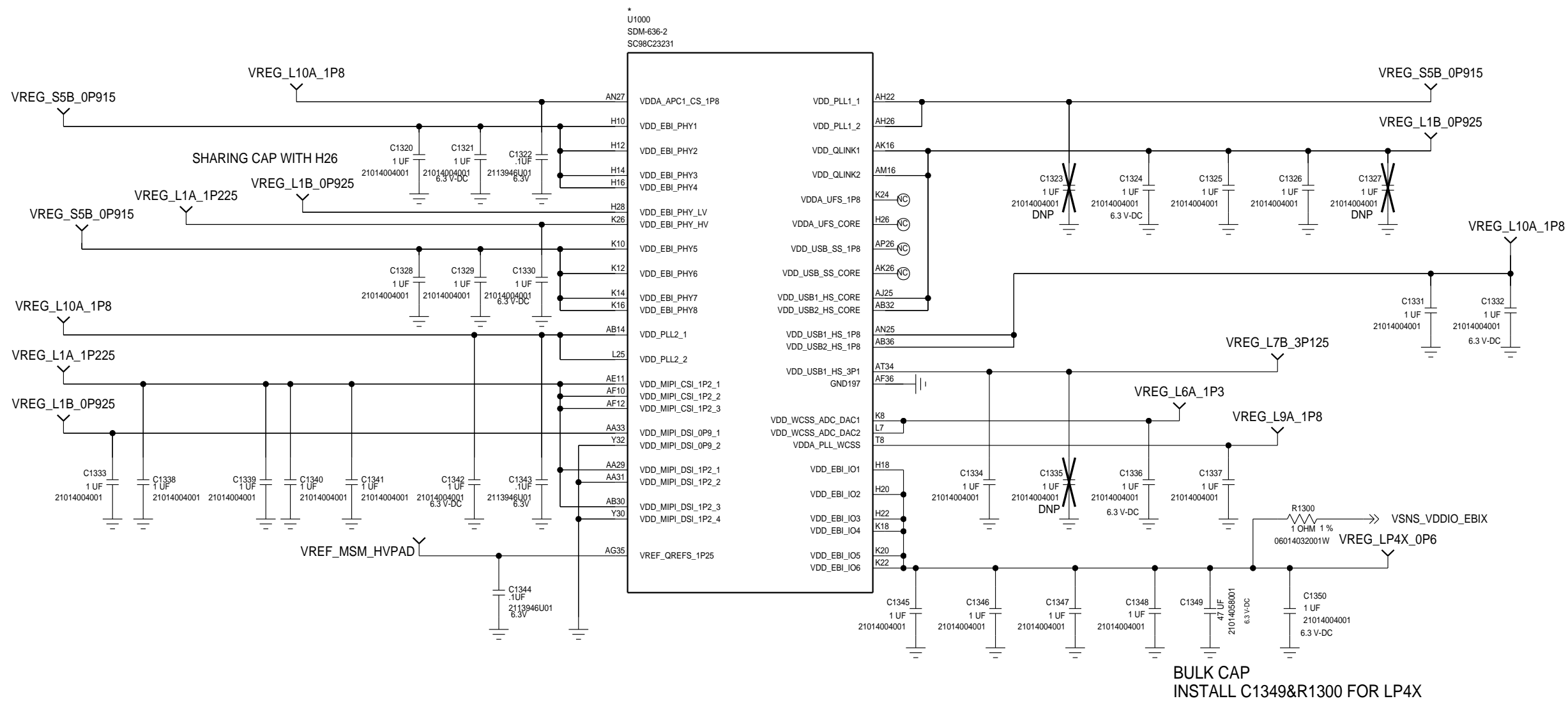
REF 1300-1399

CAD NOTE: PLACE ONE DECAP NEAR PINS V5, V6 AND  
THE OTHER DECAP NEAR PIN R5



# SDM: PWR2

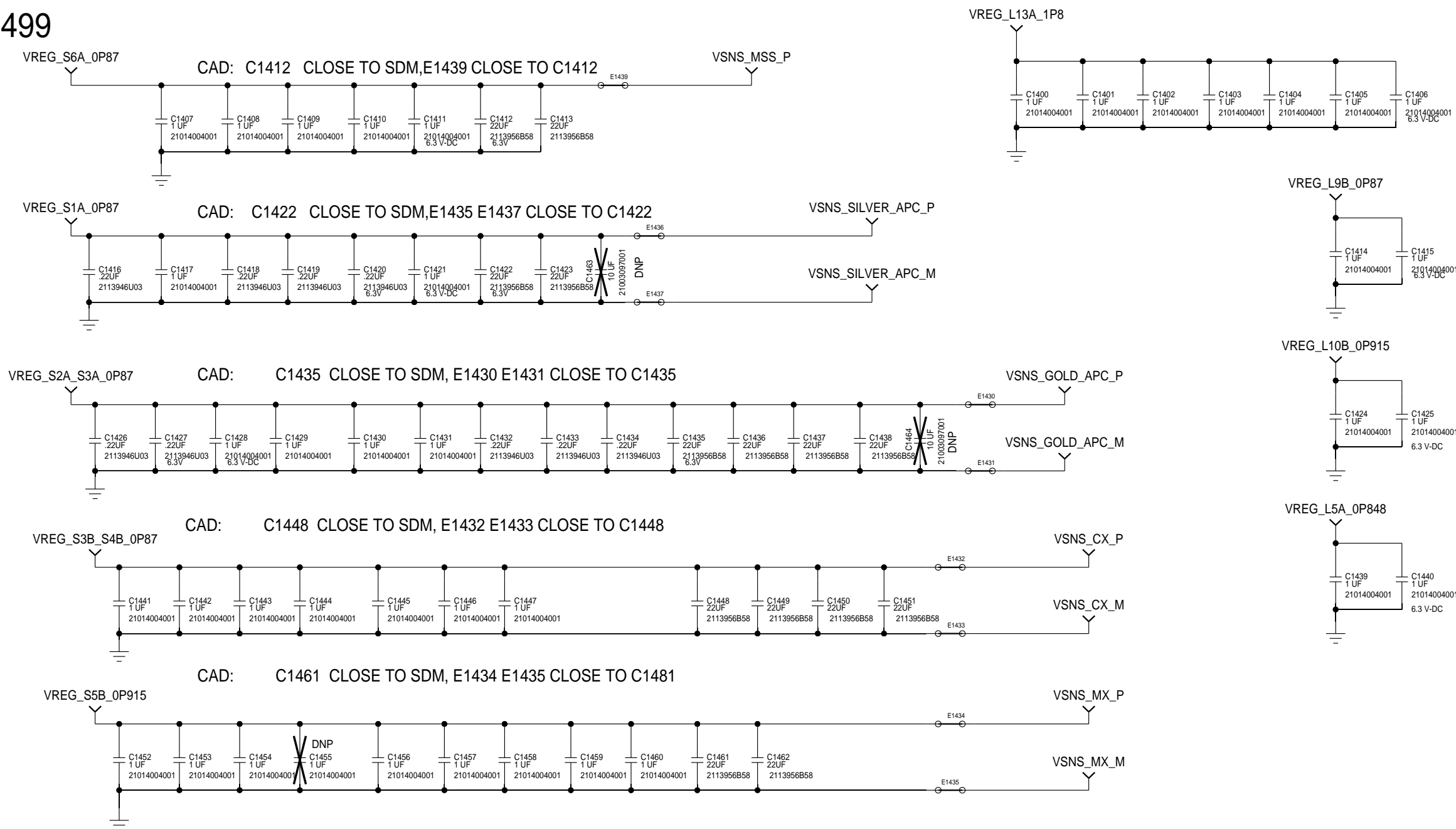
REF 1300-1399



CAD: C1348 CLOSE TO SDM, R1300 CLOSE TO C1348

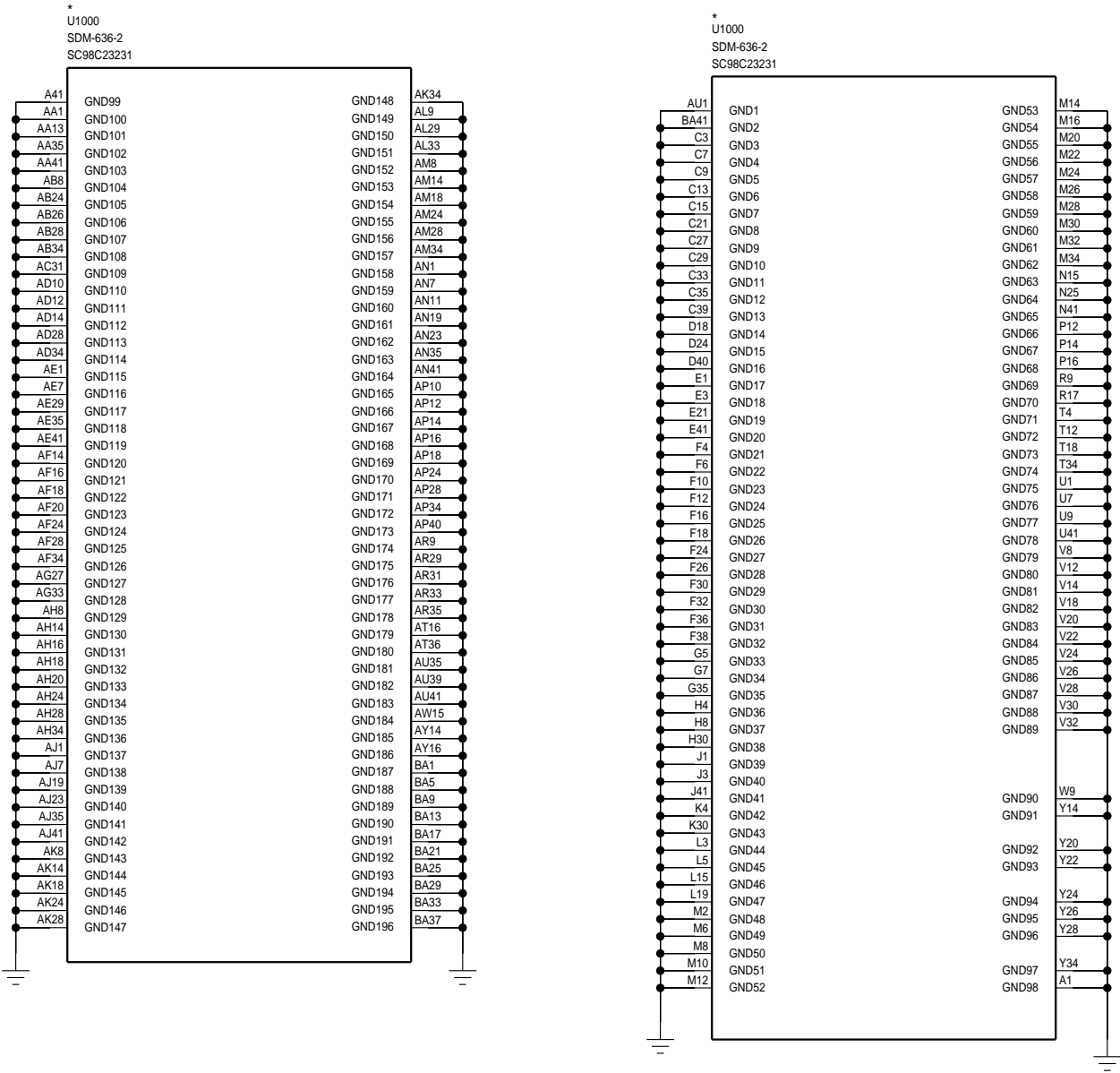
# SDM: BYPASS CAPS

REF 1400-1499





# SDM: GROUND/DNC



REF: 1600-1699

D

C

B

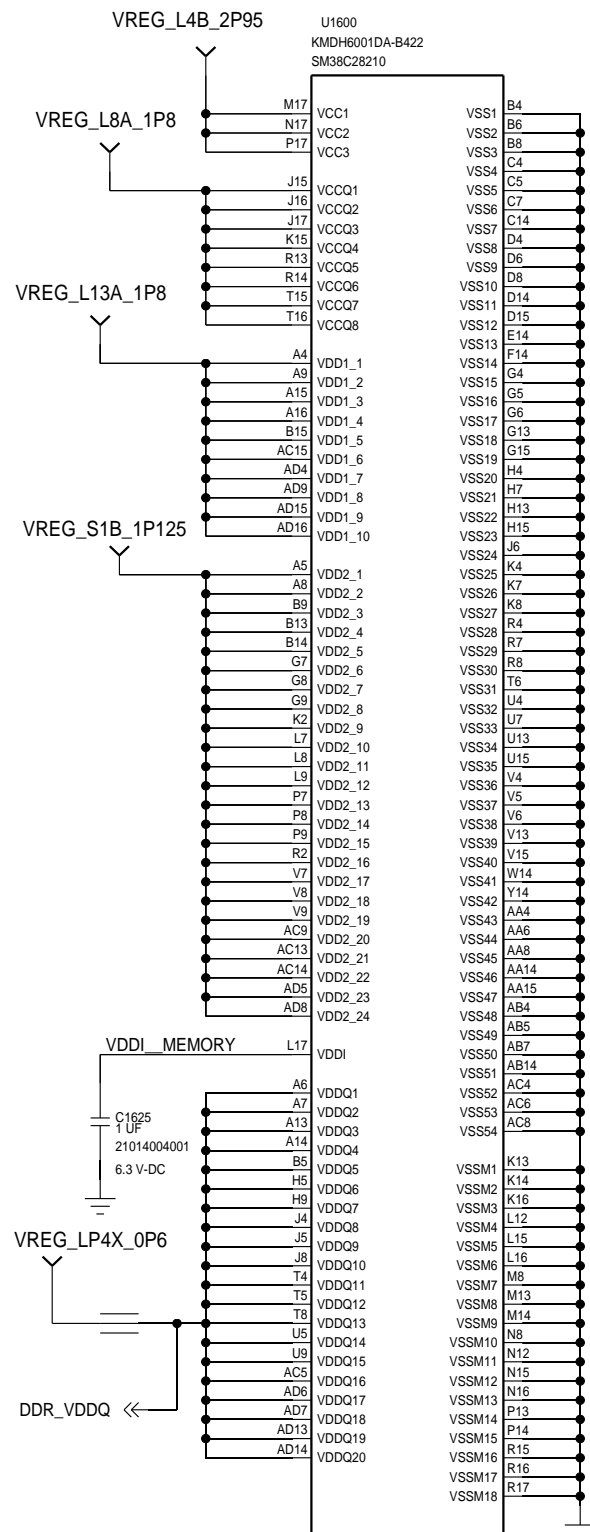
A

D

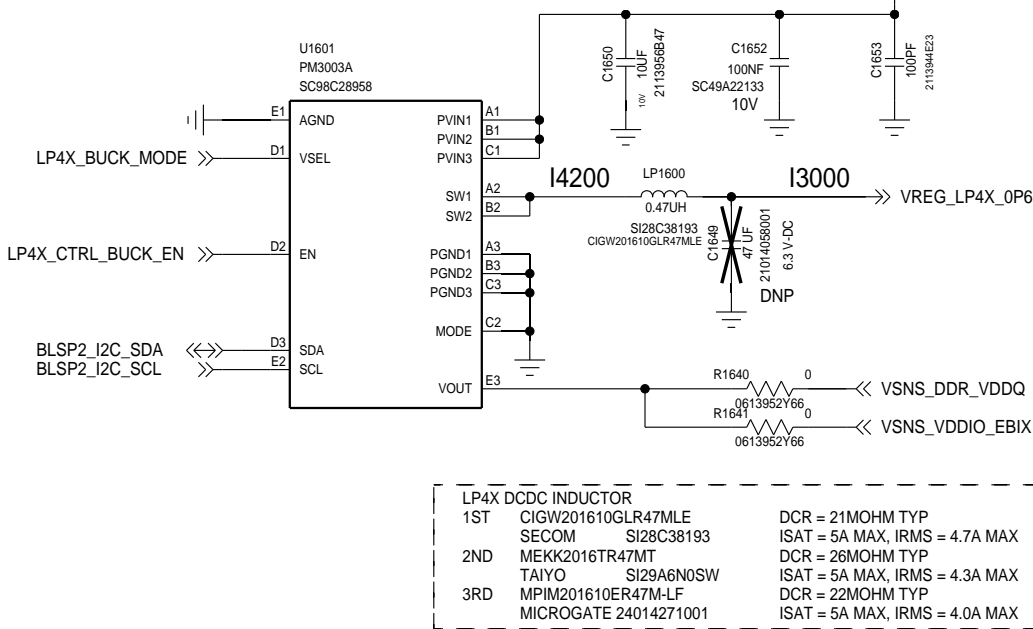
C

B

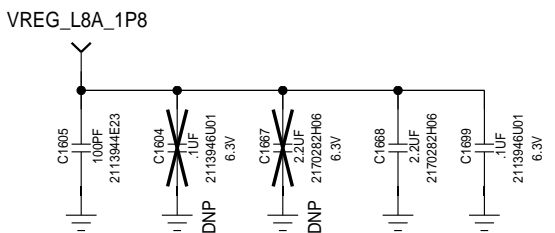
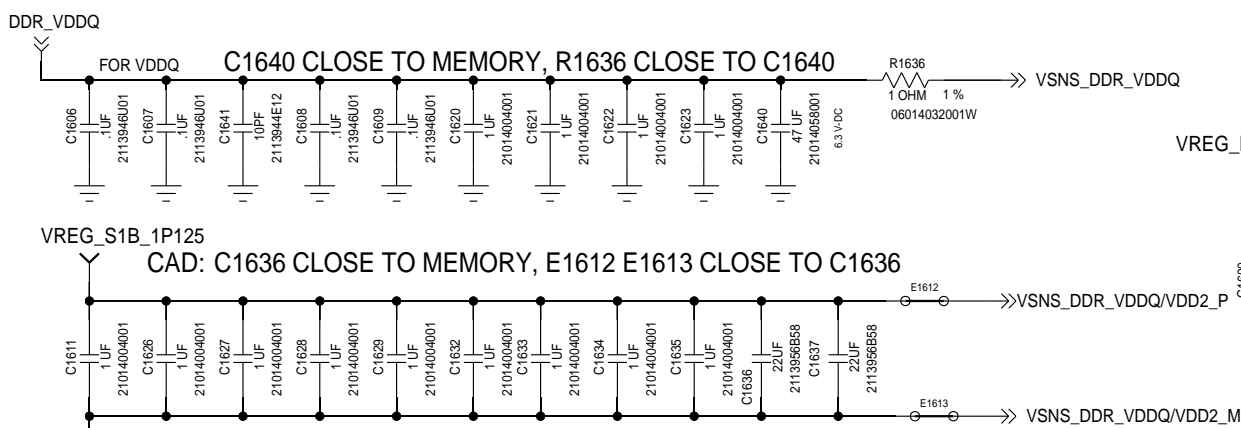
A



REF: 1600-1699



MEMORY			
DENSITY	VENDOR PN	VENDOR	LENOVO PN
4+64	KMDH6001DA-B422	SAMSUNG	SM38C28210
	H9HP52ACPMADAR-KMM	HYNIX	SM38C33882
	MT29VZZAD8DQKSL-046	MICRON	SM38C35427
4+128	MT29VZZAD9DQKSM-046	MICRON	SM38C46633
6+128	KM3V6001CM-B705	SAMSUNG	SM38C20203

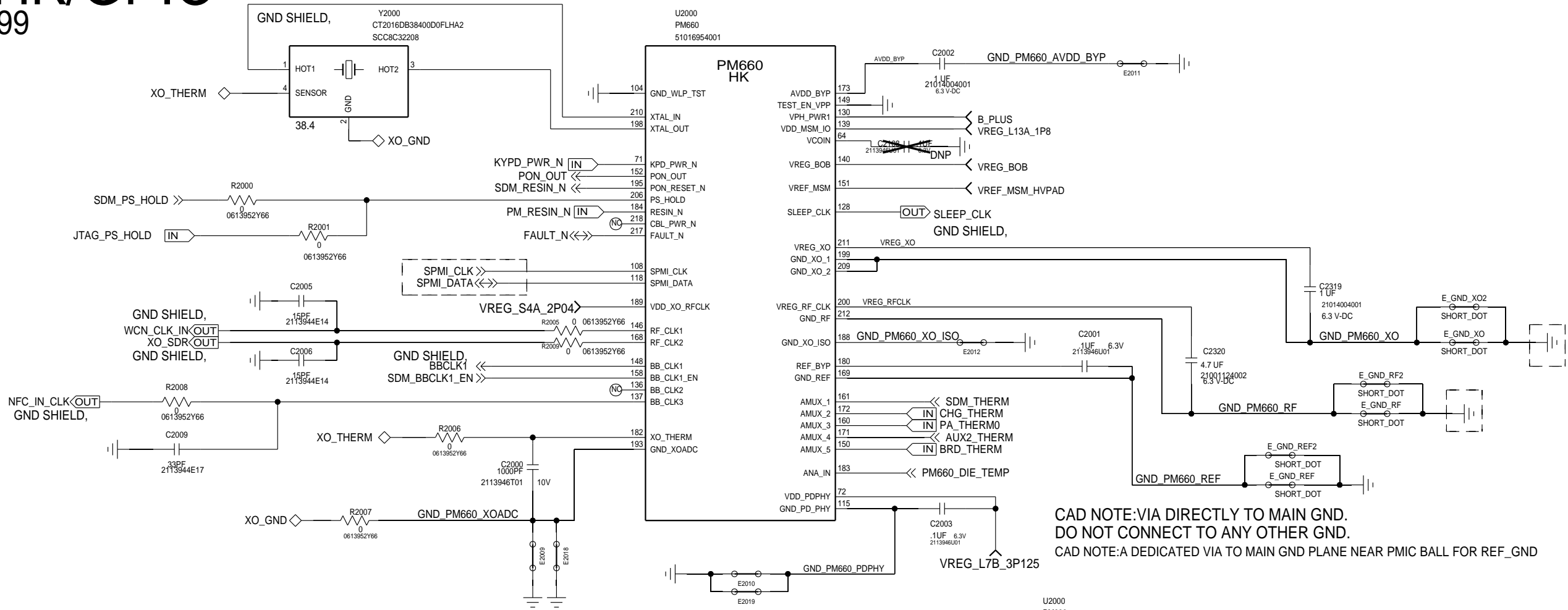


ADD STITCHING VIAS ALONG THE OUTLINE OF DDR AND SDC SIGNALS  
THOSE PARALLEL INTERFACE CAN HAVE LARGE CURRENT, SSN ARE HIGH  
MAY CAUSE RF DESNE IF RETURN CURRENT ARE NOT WELL LIMITED  
AVOID ROUTING SENSITIVE SIGNAL CORSS THIS SECTION  
SENSITIVE SIGNALS CAN BE CLOCKS, (CK, MCLK), ANALOG SIGNALS AND SO ON

# PM660: HK/GPIO

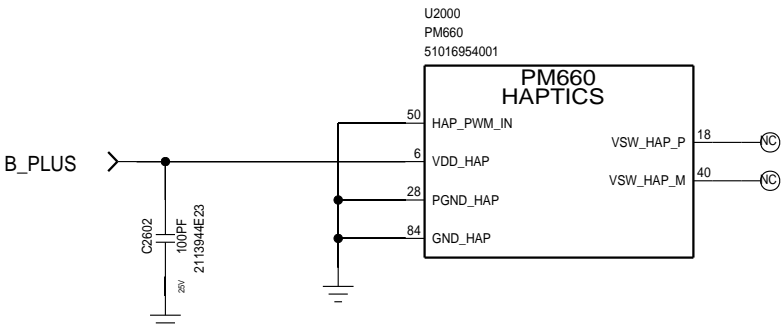
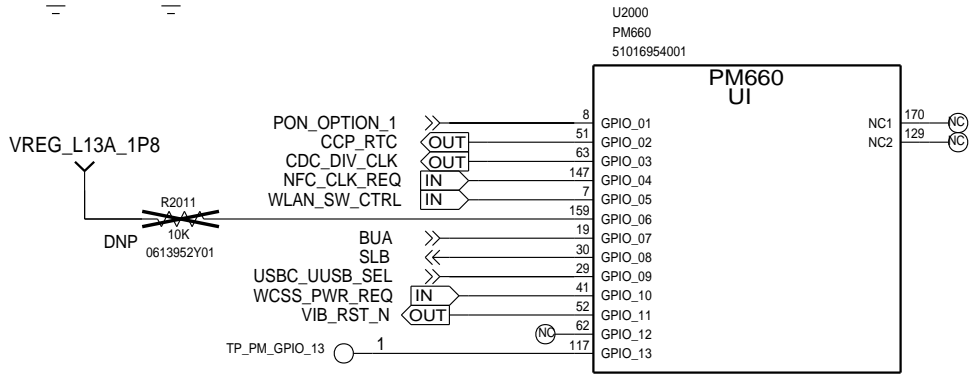
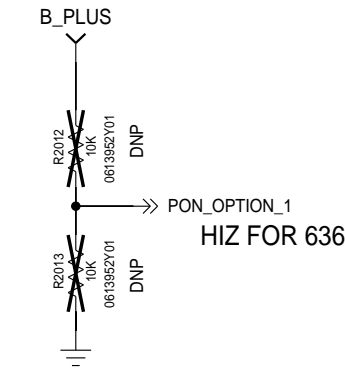
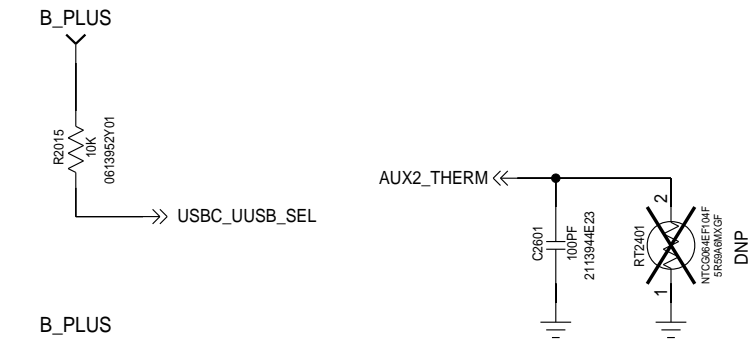
REF: 2000-2099

XTAL\_IN, AND XTAL\_OUT ROUTED ON 1ST LAYER, NOT AS DIFFERENT PAIR. REFER TO 80-P7747-3 REV. B FOR CRYSTAL LAYOUT GUIDE



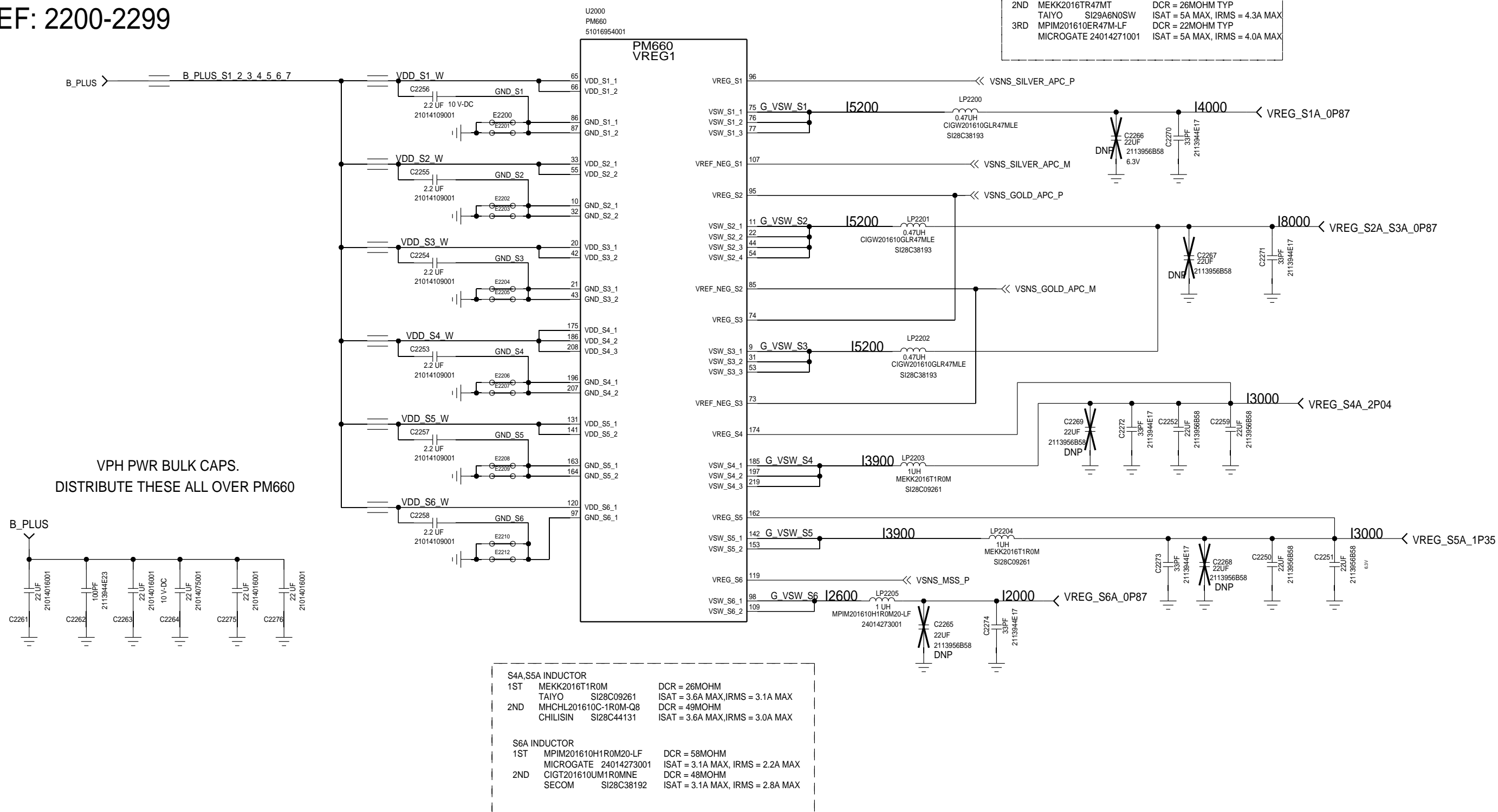
CAD NOTE: VIA DIRECTLY TO MAIN GND.  
DO NOT CONNECT TO ANY OTHER GND.  
CAD NOTE: A DEDICATED VIA TO MAIN GND PLANE NEAR PMIC BALL FOR REF\_GND

USBC\_UUSB\_SEL IS HIGH/HI-Z FOR TYPE-C

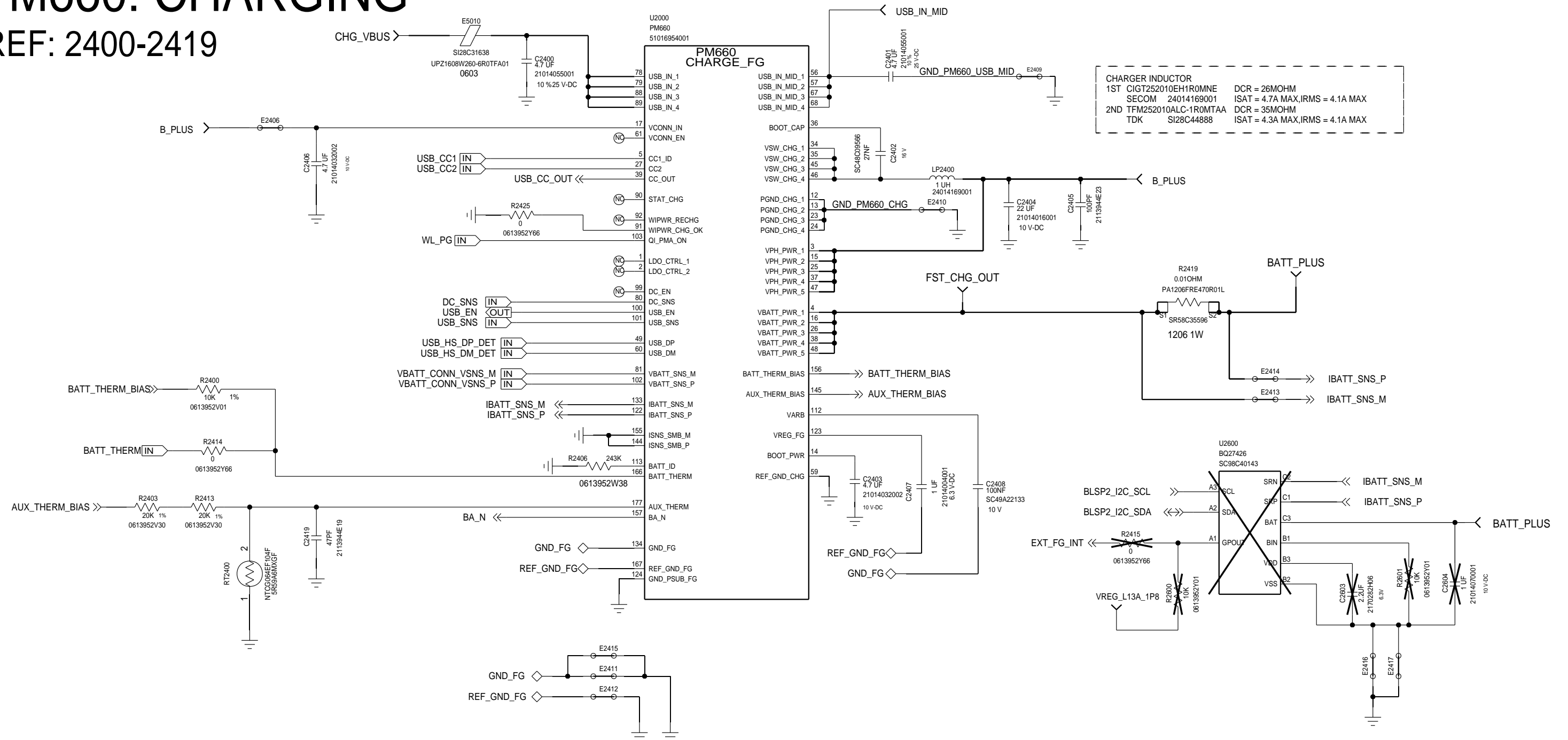


# PM660: SMPS

REF: 2200-2299

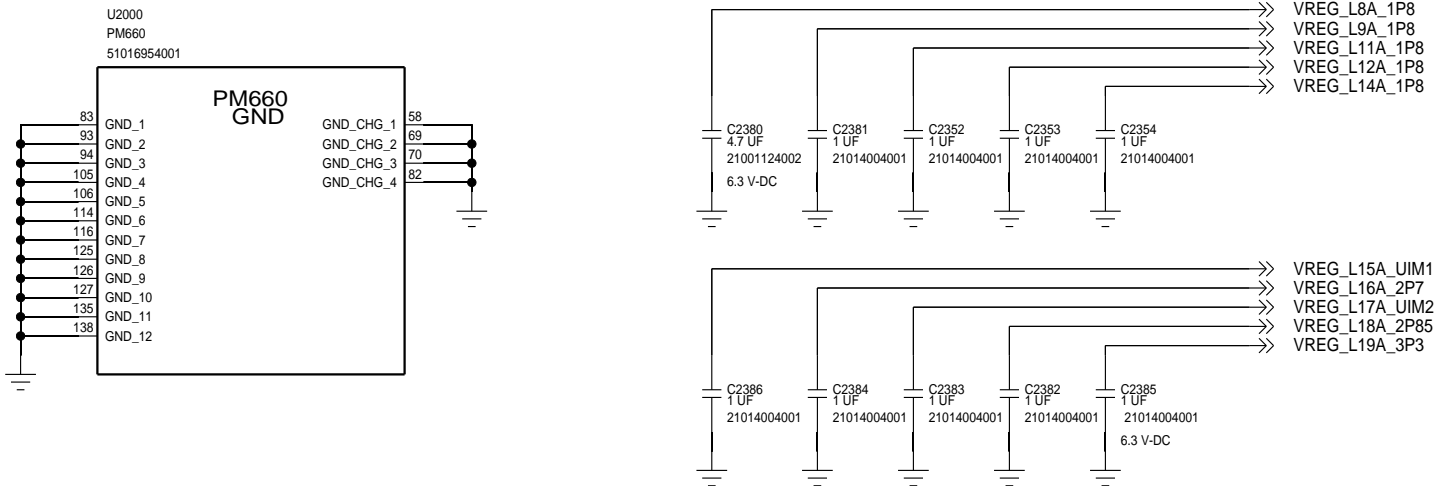
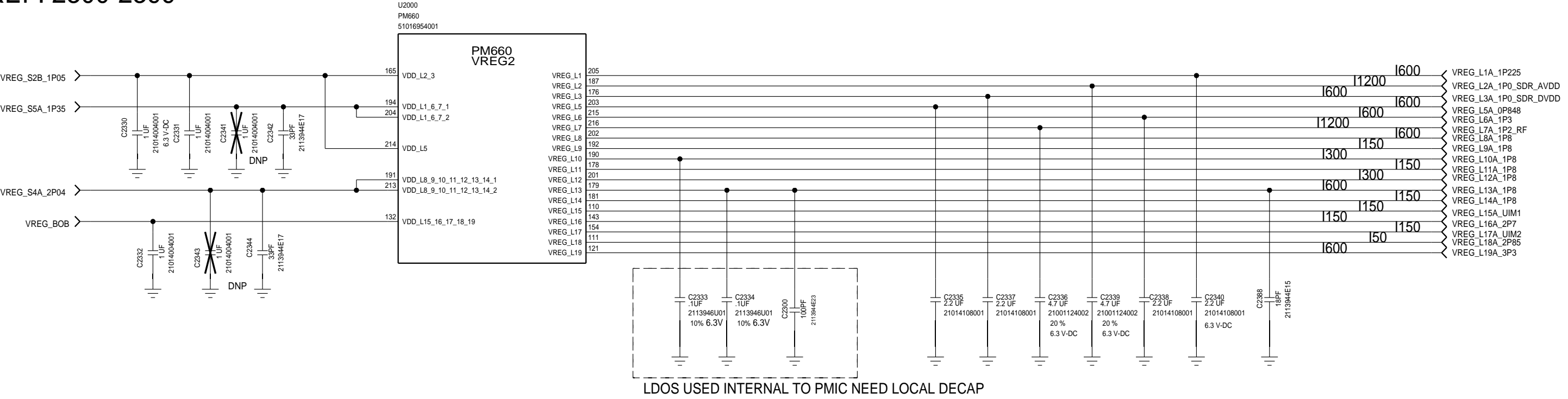


REF: 2400-2419



# PM660: VREGS

REF: 2300-2399

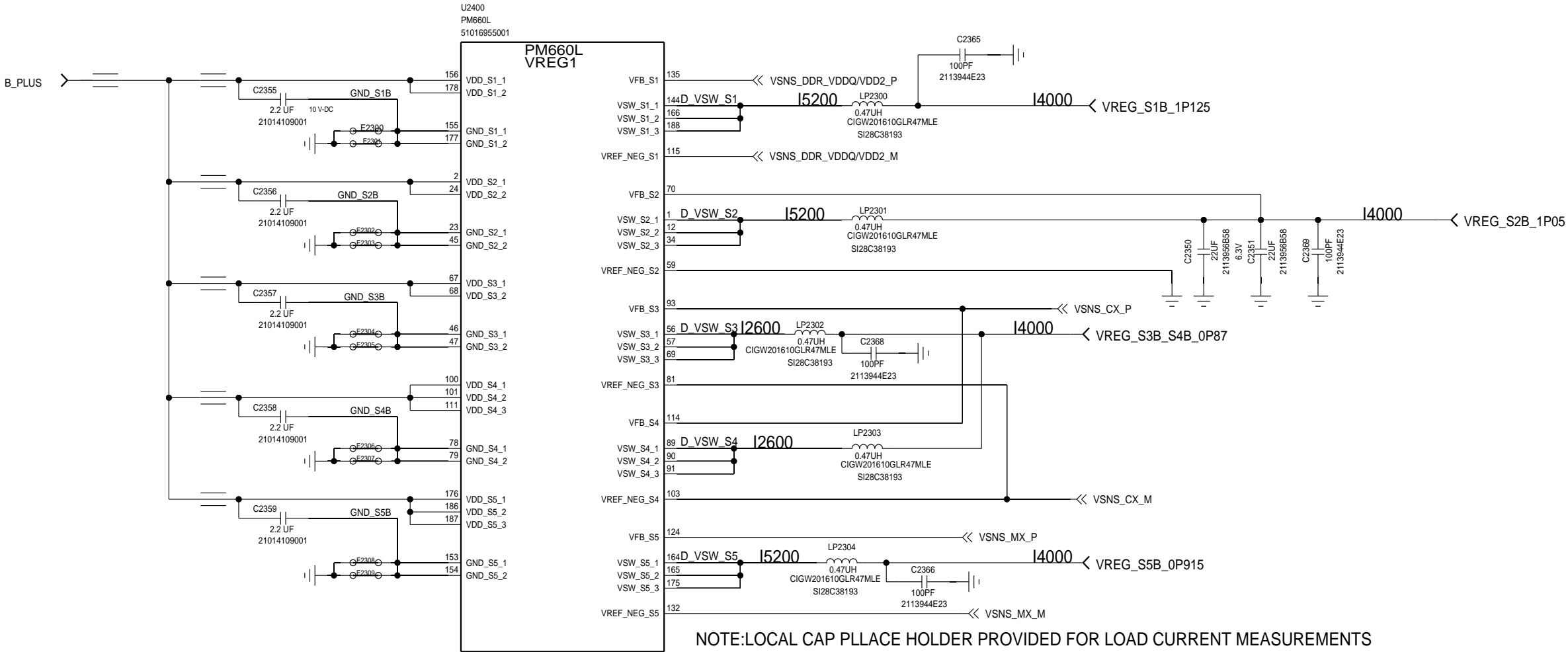


PSEUDO CAPLESS LODS  
FOR CAPLESS LODS:IF DECAPS ON THE LOAD SIDE DO NOT ADD UP TO LDO SPEC, THEN INSTALL THE CAP CLOSE TO THE PMIC

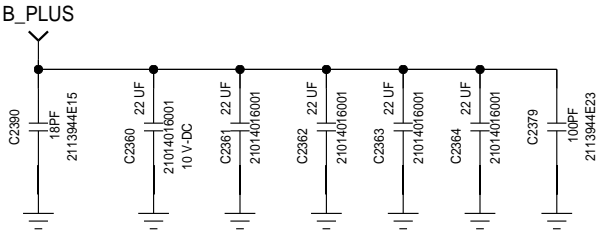
# PM660L: SMPS

REF: 2300-2399

S1B-S5B INDUCTOR		
1ST	CIGW201610GLR47MLE	DCR = 21MOHM TYP
	SECOM SI28C38193	ISAT = 5A MAX, IRMS = 4.7A MAX
2ND	MEKK2016TR47MT	DCR = 26MOHM TYP
	TAIYO SI29A6N0SW	ISAT = 5A MAX, IRMS = 4.3A MAX
3RD	MPIM201610ER47M-LF	DCR = 22MOHM TYP
	MICROGATE 24014271001	ISAT = 5A MAX, IRMS = 4.0A MAX



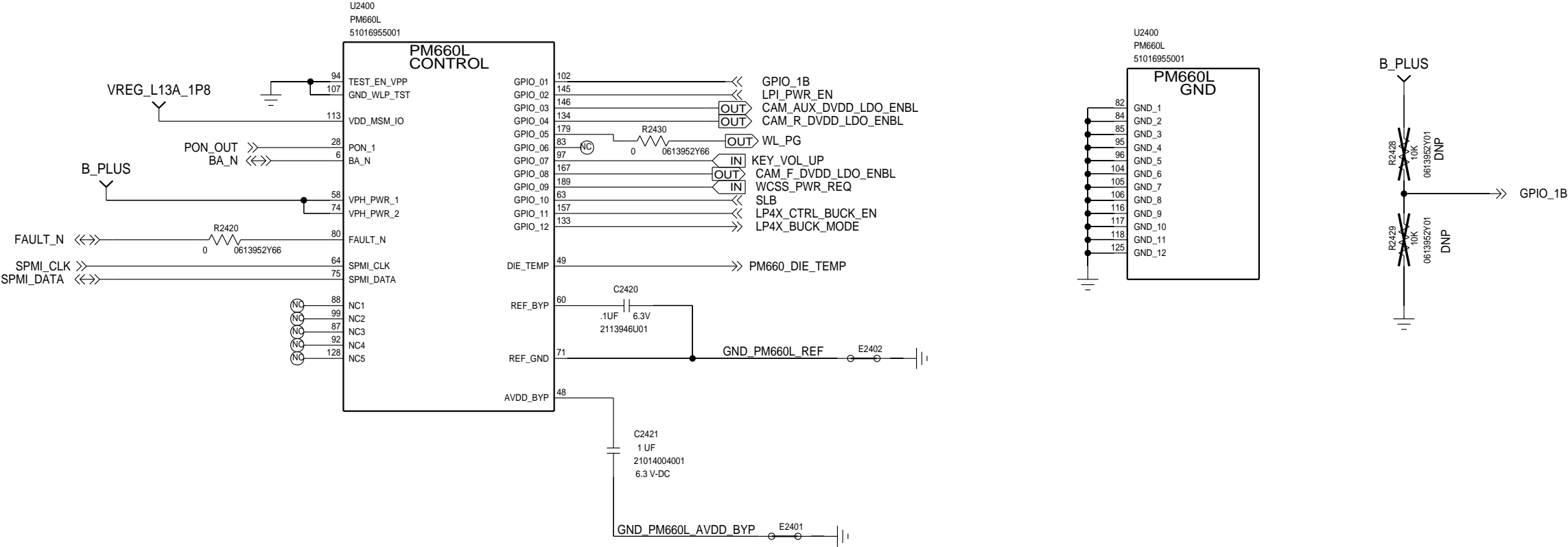
NOTE:LOCAL CAP PLLACE HOLDER PROVIDED FOR LOAD CURRENT MEASUREMENTS



BULK CAPS.DISTRIBUTE THESE ALL OVER U2400

# PM660L: CONTROL

## REF: 2420-2439



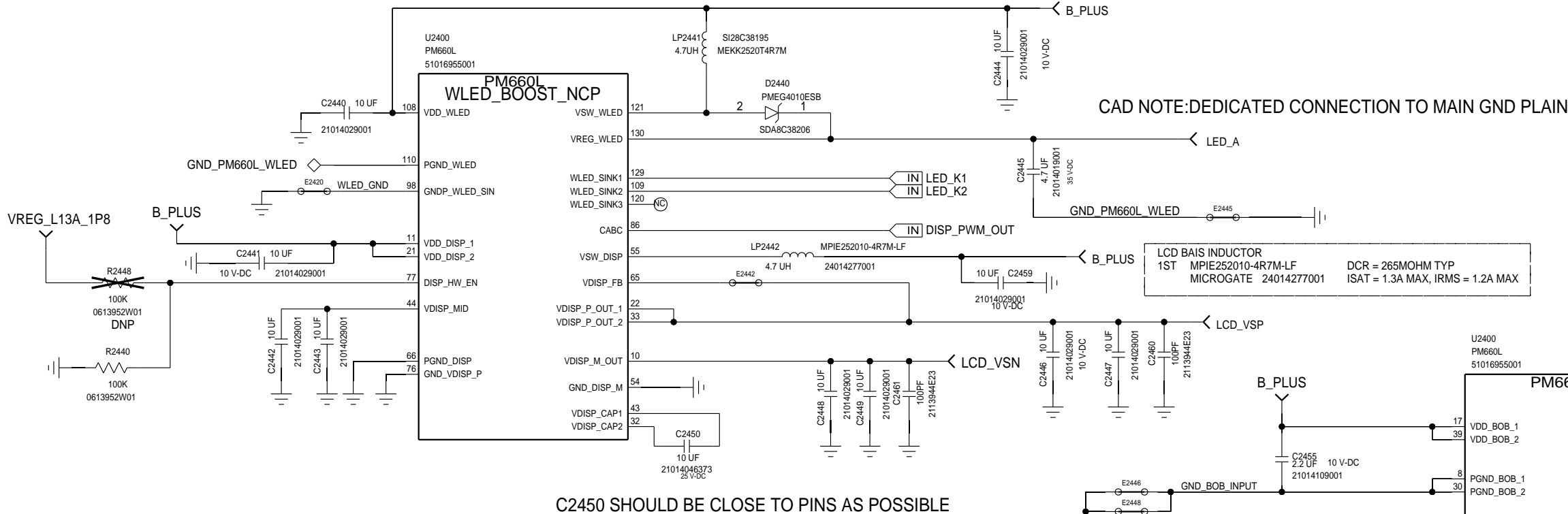
GND SHIELDED IF POSSIBLE, ROUTE DIFFERENTIALLY AND SPACING WITH 2X LW



# PM660L: REG\_WLED

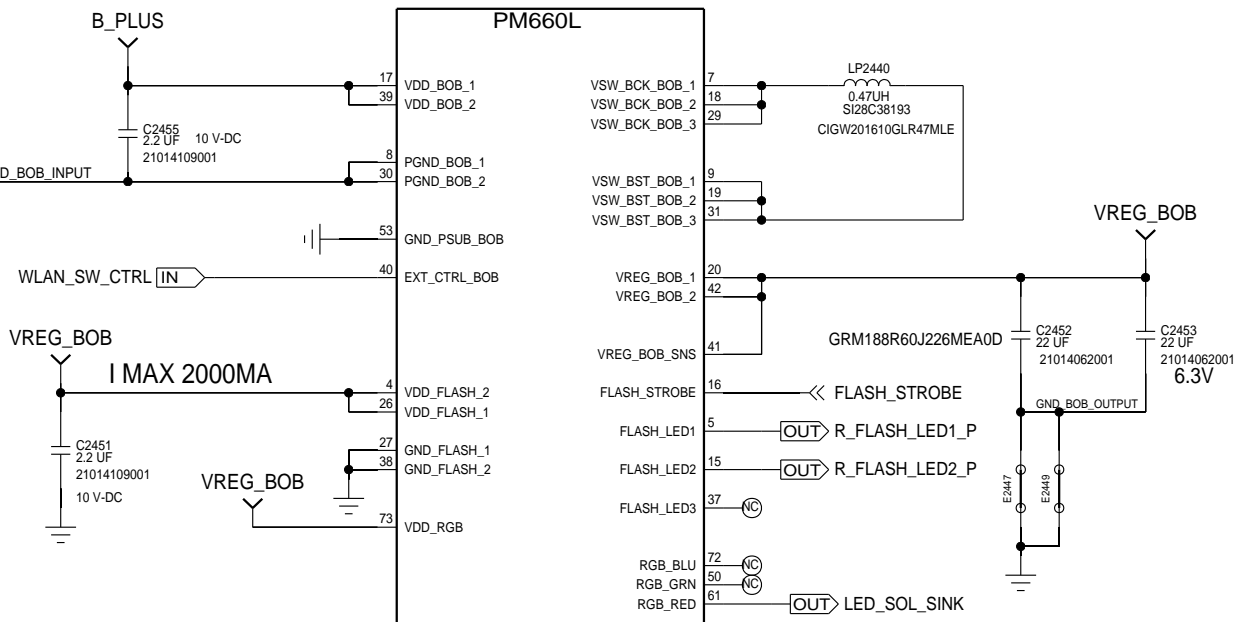
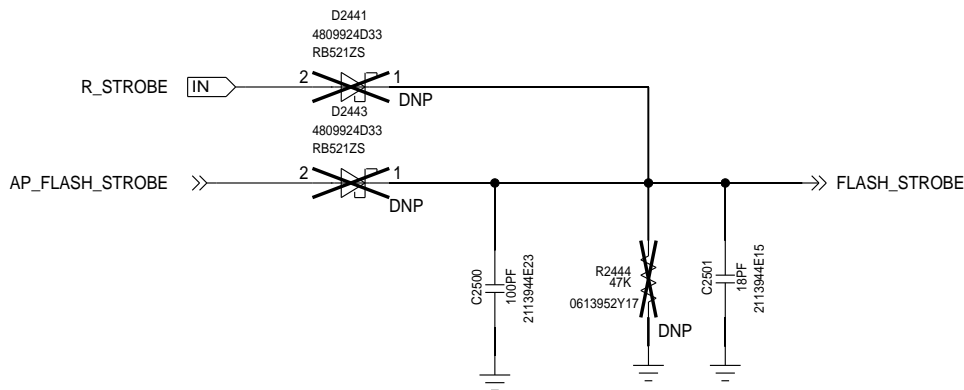
REF: 2440-2459

WLED INDUCTOR			
1ST	MEKK2520T4R7M	DCR = 215MOHM TYP	ISAT = 1.6A MAX, IRMS = 1.5A MAX
	TAIYO SI28C38195	DCR = 180MOHM TYP	ISAT = 1.4A MAX, IRMS = 1.4A MAX
2ND	CIGW252010GL4R7MNE	DCR = 260MOHM TYP	ISAT = 1.5A MAX, IRMS = 1.15A MAX
	SECOM SI28C15532		
3RD	MPIA252010-4R7M-LF		
	MICROGATE 24014274001		



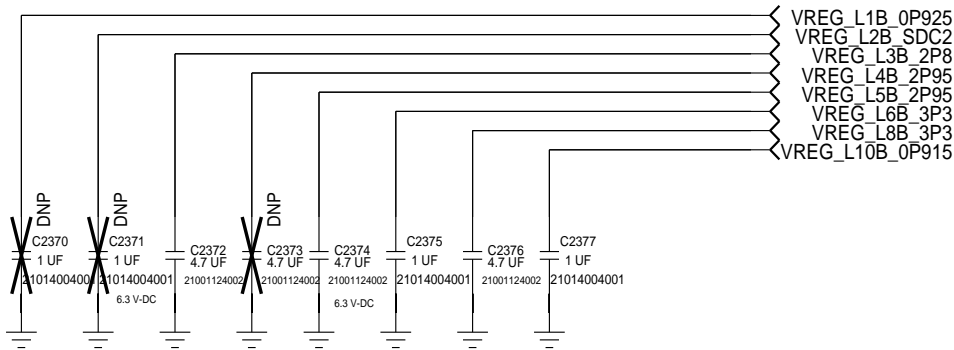
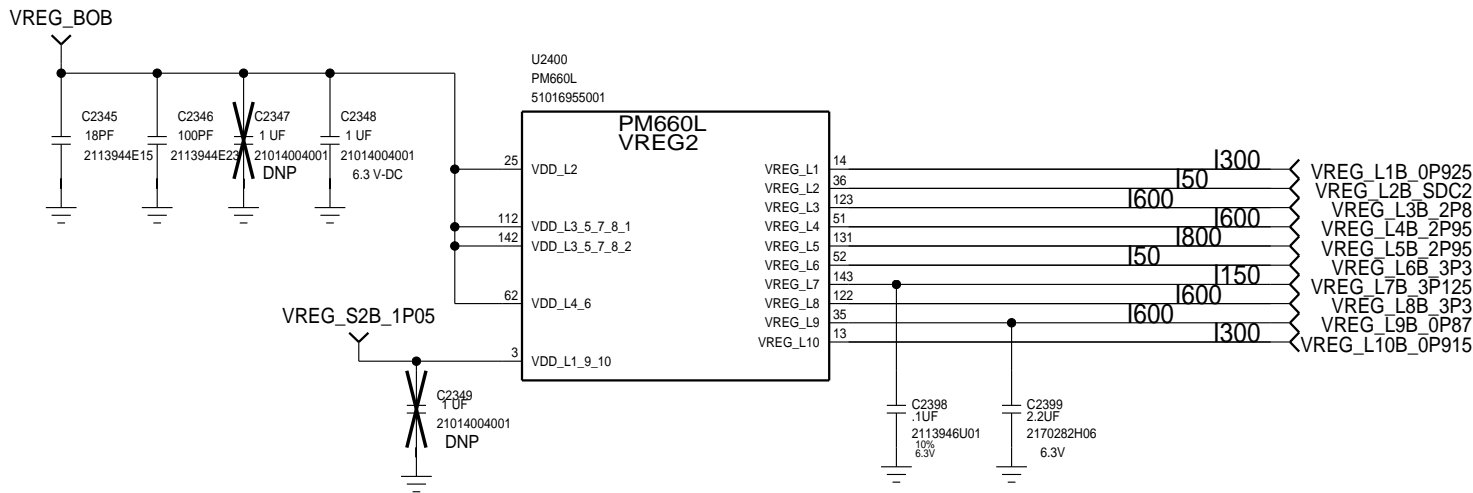
BOB INDUCTOR			
1ST	CIGW201610GLR47MLE	DCR = 21MOHM TYP	ISAT = 5A MAX, IRMS = 4.7A MAX
	SECOM SI28C38193	DCR = 26MOHM TYP	ISAT = 5A MAX, IRMS = 4.3A MAX
2ND	MEKK2016TR47MT	DCR = 22MOHM TYP	ISAT = 5A MAX, IRMS = 4.0A MAX
	TAIYO SI29A6N0SW		
3RD	MPIM201610ER47M-LF		
	MICROGATE 24014271001		

## DNP FLASH STROBE LOGIC



# PM660L: VREGS

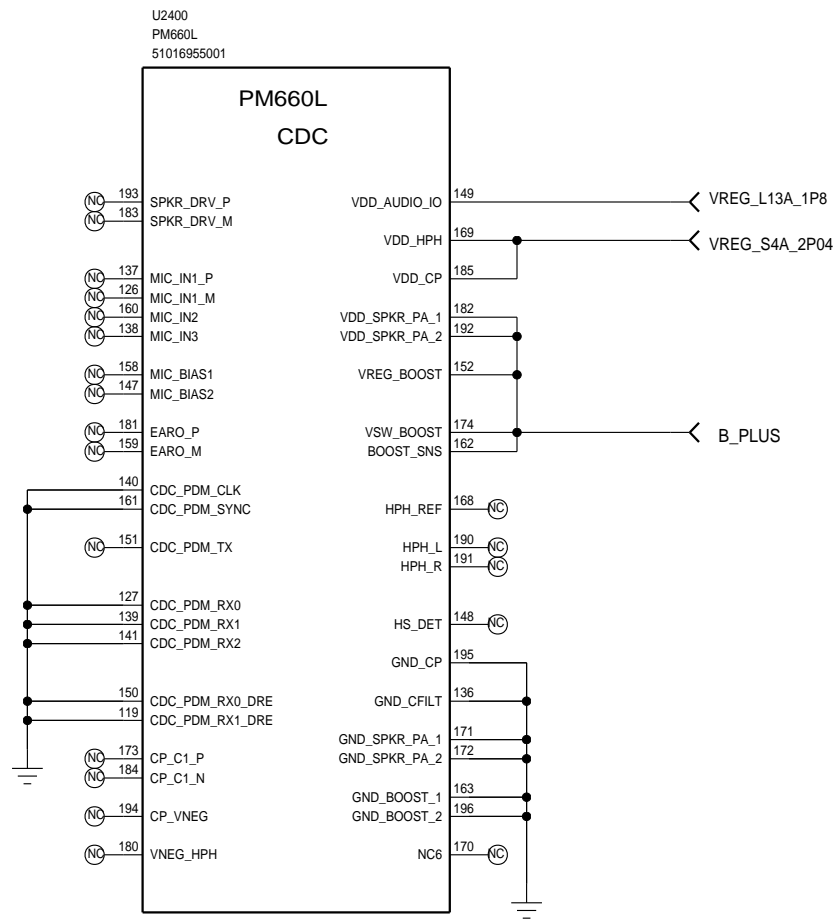
REF: 2300-2399



PSEUDO CAPLESS LODS  
FOR CAPLESS LODS:IF DECAPS ON THE LOAD SIDE DO NOT ADD UP TO LDO SPEC, THEN INSTALL THE CAP CLOSE TO THE PMIC

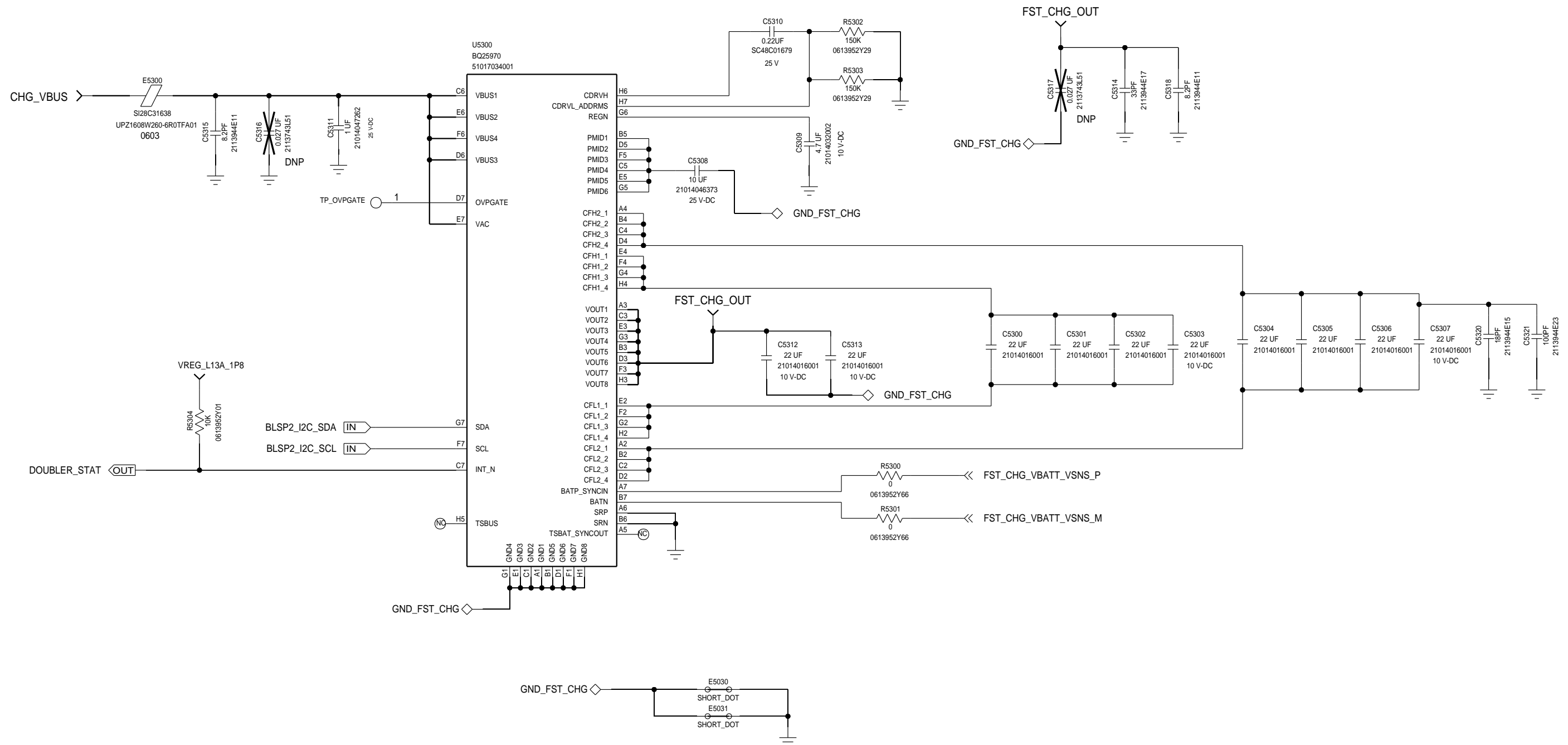
# PM660L: CODEC

REF 2500-2579



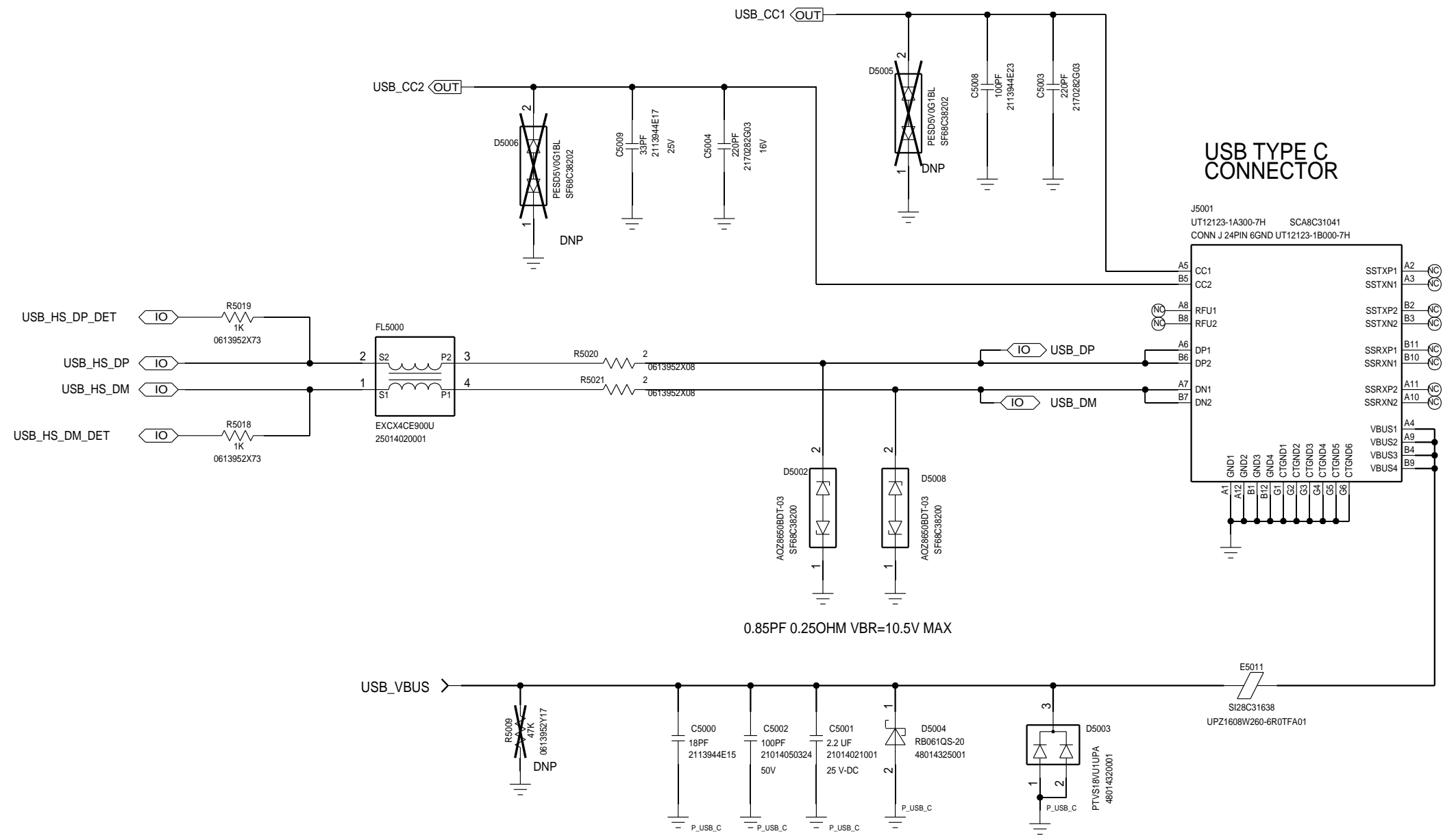
# PWR: FAST CHARGER

REF:5300 - 5399



# PWR: USB-C

REF:5300 - 5399



PWR: BATT

REF:510 - 550

BATT\_THERMISTOR\_10KOHM  
BETA < 3900

BATT\_THERM

OUT

TP\_BATT\_THERM

IN

E512

SHORT\_DOT

D511  
PESD5V0G1BL  
SF68C38202

11PF 0.2OHM VBR=7.8V MAX

DNP

CLOSE TO CONN

C510  
33PF  
2113944E17

RTH+

BATTERY BLOCK

USE PMI INTERNAL SENSE

BATT\_PLUS

C512  
8.2PF  
2113944E11

D510  
ESD56201D04-2\_TR  
SF68C38204

C513  
100PF  
2113944E23

C514  
10 UF  
21014029001  
10 V-DC

G3  
P510  
28014040001

4  
P510  
28014040001

2  
P510  
28014040001

G2  
P510  
28014040001

G4  
P510  
28014040001

3  
P510  
28014040001

1  
P510  
28014040001

G1  
P510  
28014040001

FST\_CHG\_VBATT\_VSNS\_P

FST\_CHG\_VBATT\_VSNS\_M

VBATT\_CONN\_VSNS\_P

VBATT\_CONN\_VSNS\_M

CLOSE TO CONN

C515  
33PF  
2113944E17

C516  
33PF  
2113944E17

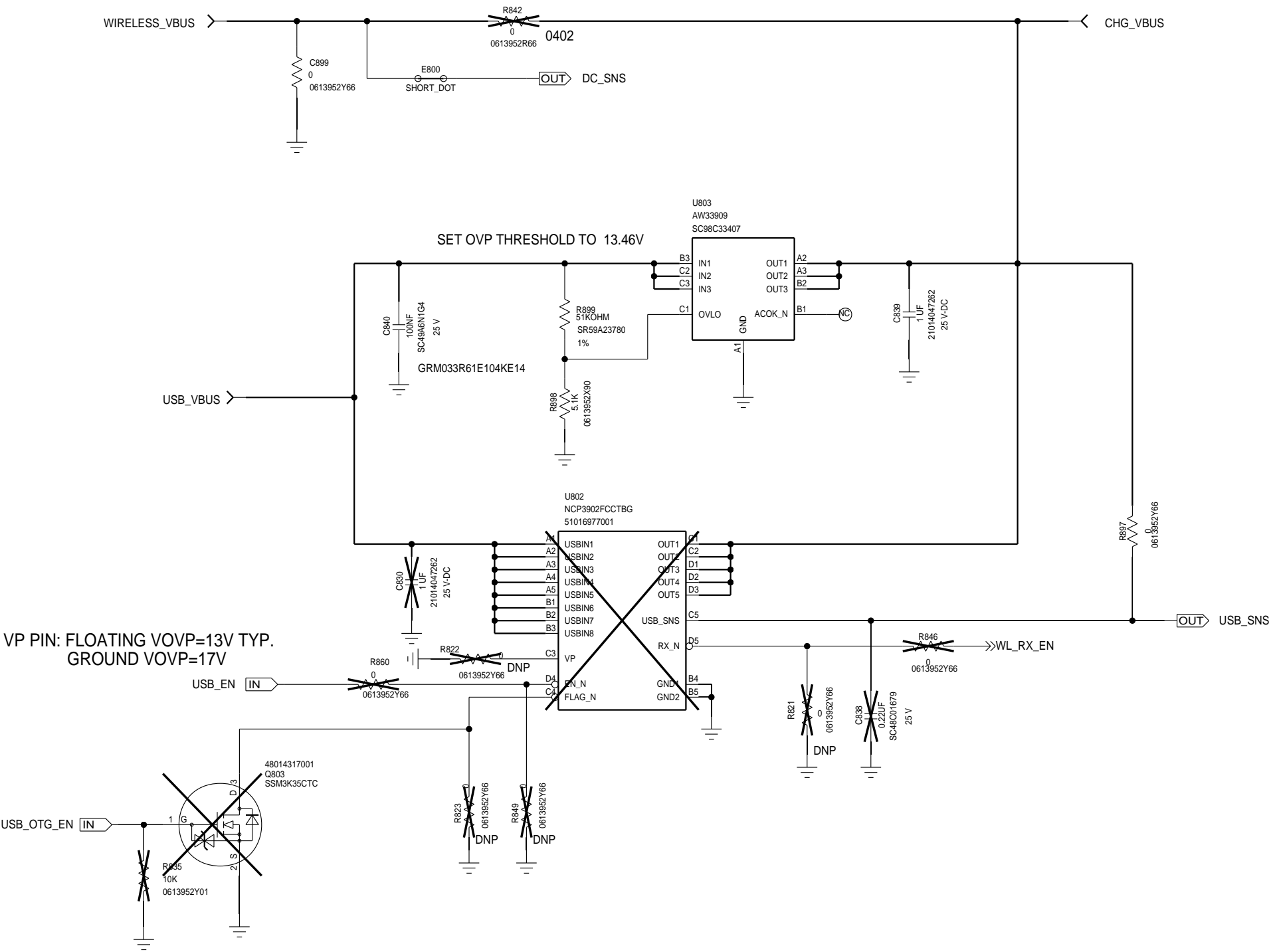
D512  
VS3V3BB1EST15R  
48014232001

10PF 0.1OHM VBR=6.7V MAX

# PWR: OVP

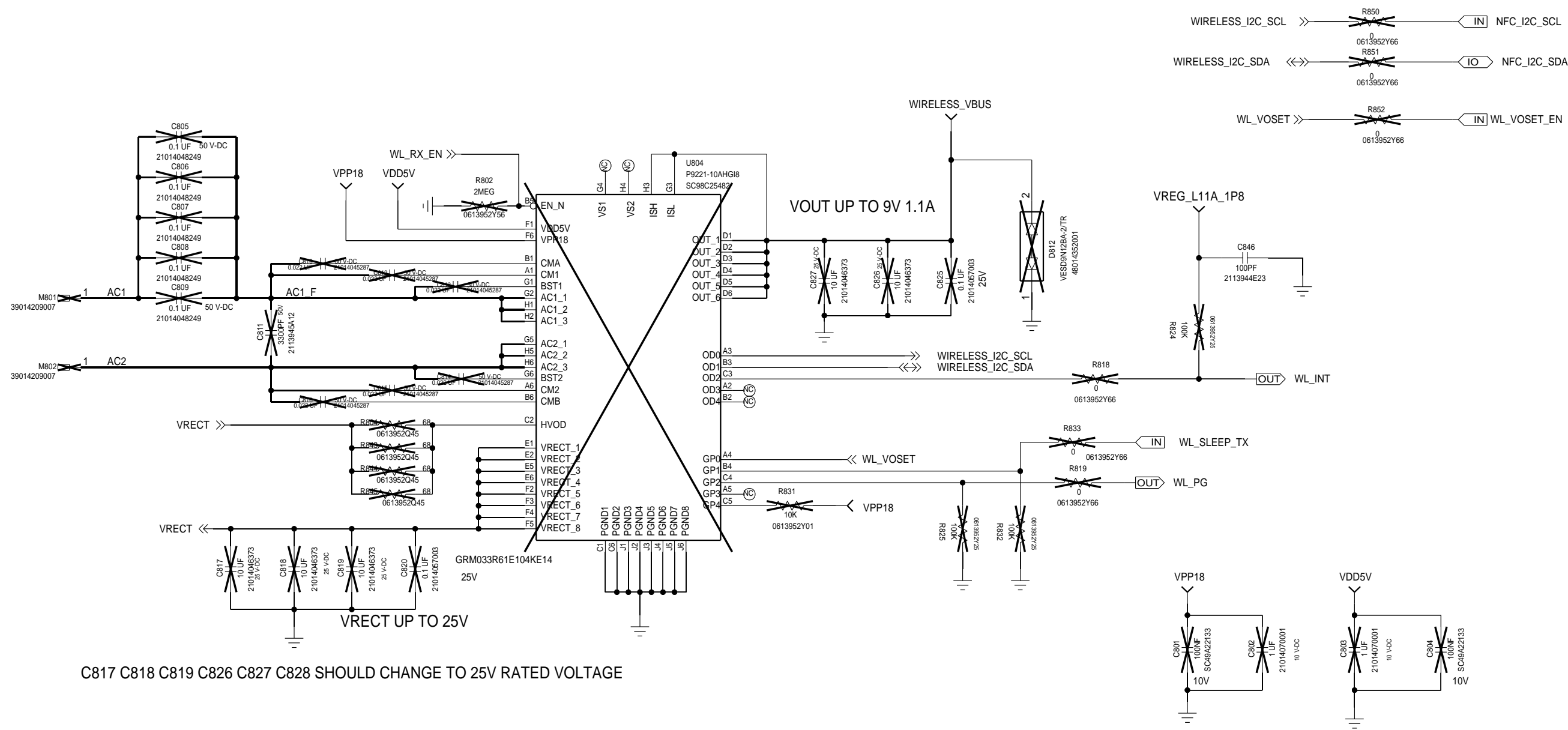
REF:800 - 899

0402 RES 00HM RATED CURRENT 1A.



# WIRELESS CHARGER

REF:800 - 899

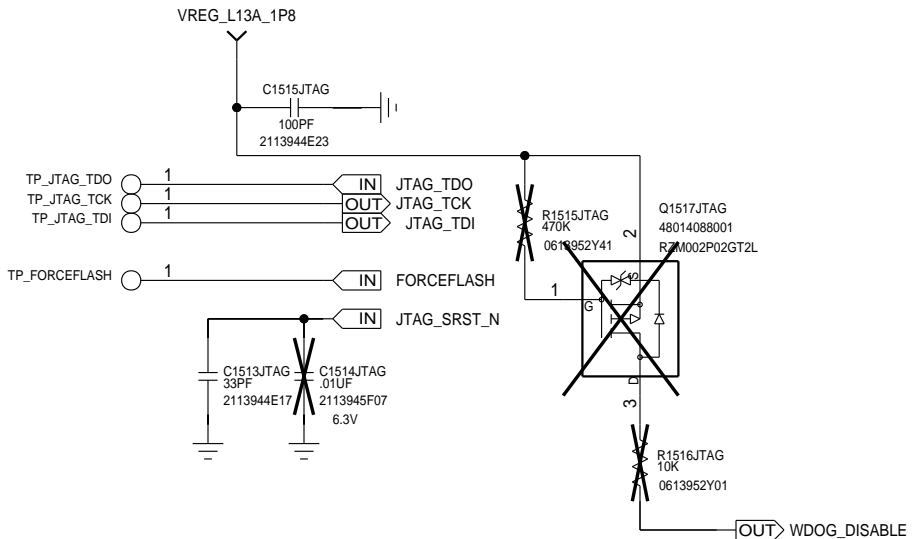
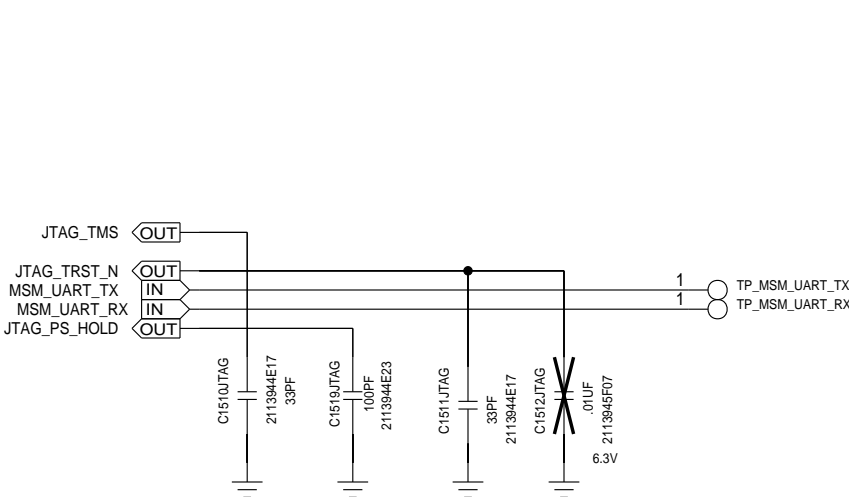


C817 C818 C819 C826 C827 C828 SHOULD CHANGE TO 25V RATED VOLTAGE



# DEBUG PORT

REF 1000-1099



## A

A

A

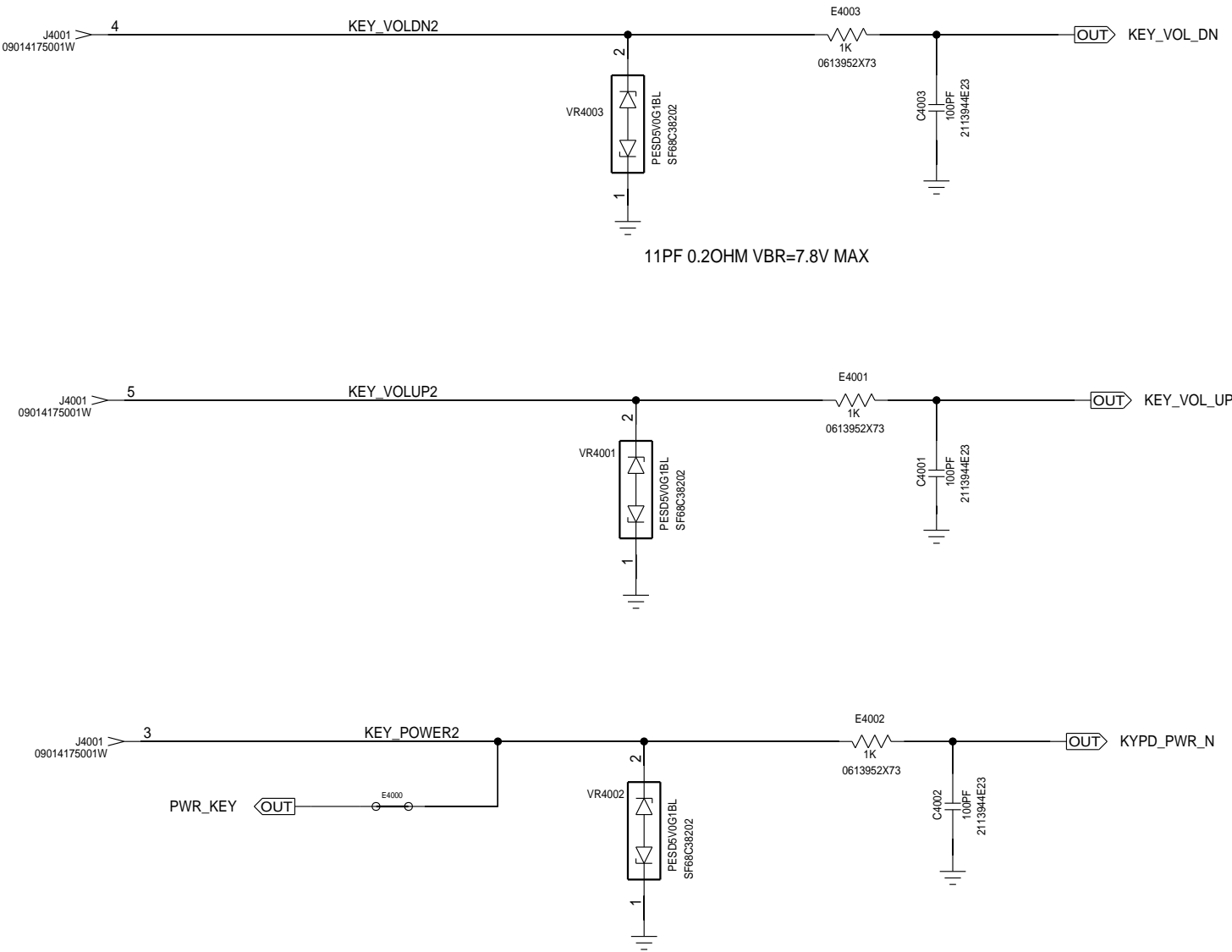
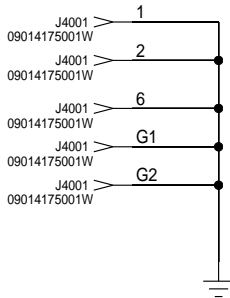
A |

# UI: SIDE KEY

REF:4000-4199

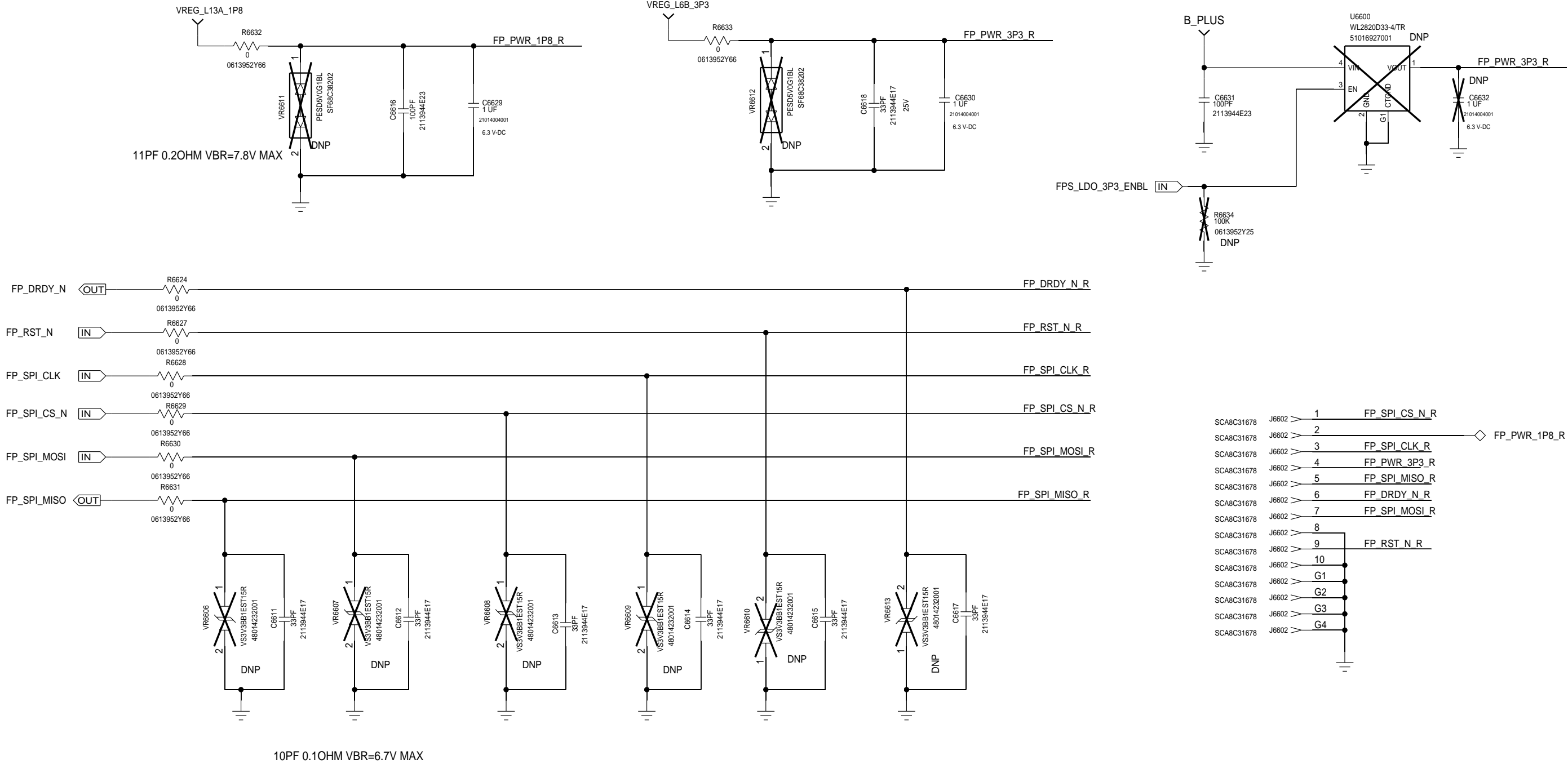
MECHANICAL KEYS

VOL\_UP/DOWN/GND TPS NEED P0 BRINGUP ONLY



# UI: FPS

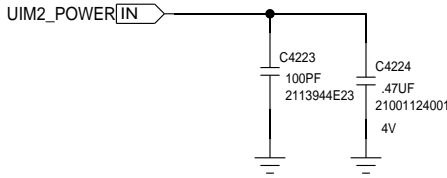
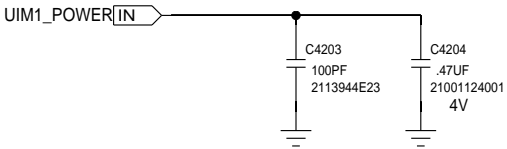
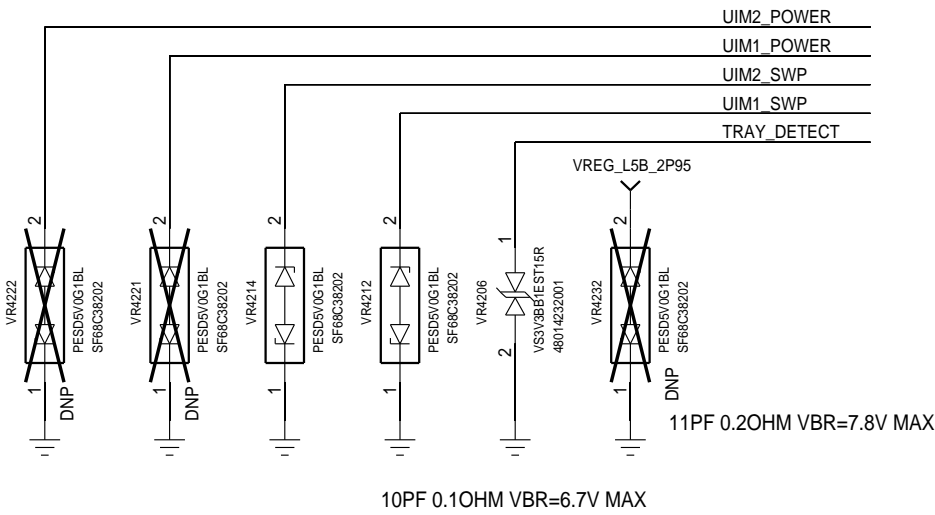
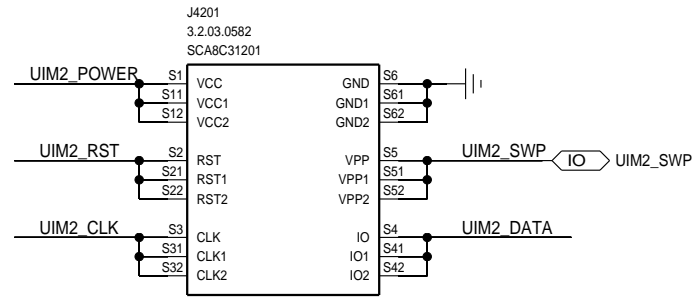
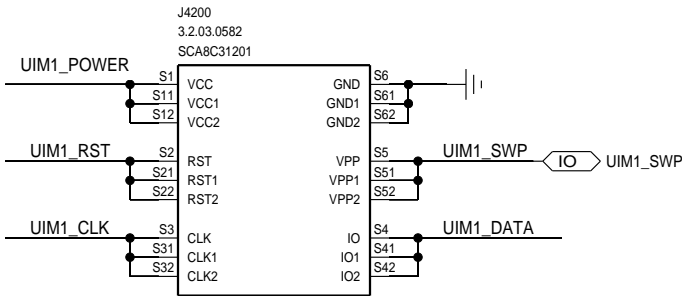
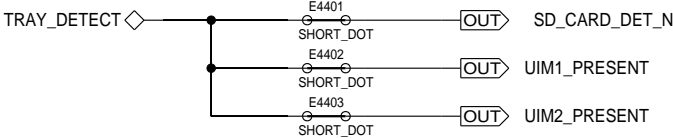
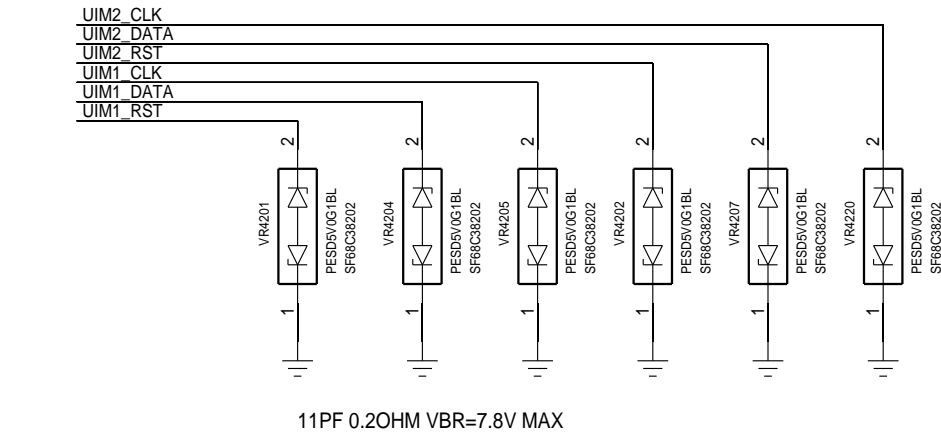
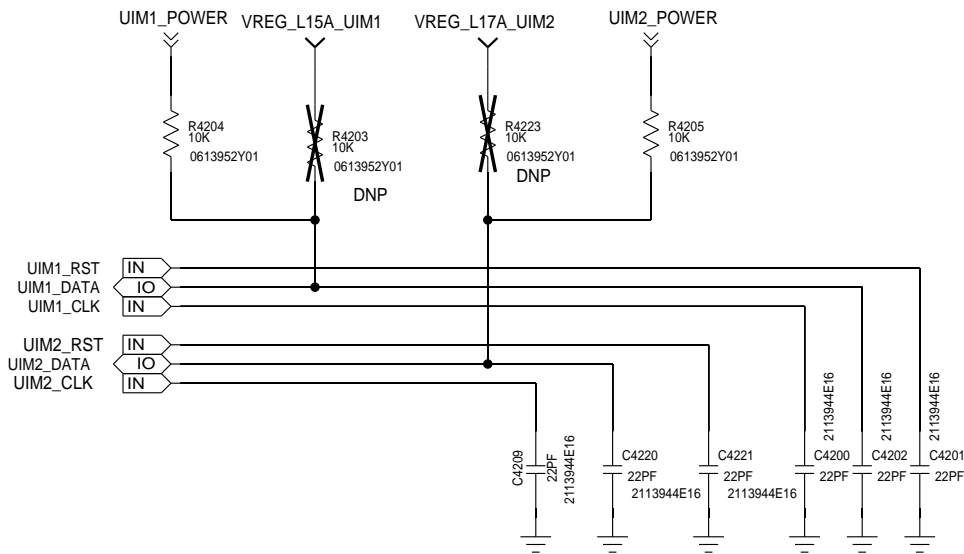
REF: 6600 - 6699



SCA8C31678	J6602 > 1	FP_SPI_CS_N_R	
SCA8C31678	J6602 > 2	FP_PWR_1P8_R	◇
SCA8C31678	J6602 > 3	FP_SPI_CLK_R	
SCA8C31678	J6602 > 4	FP_PWR_3P3_R	
SCA8C31678	J6602 > 5	FP_SPI_MISO_R	
SCA8C31678	J6602 > 6	FP_DRDY_N_R	
SCA8C31678	J6602 > 7	FP_SPI_MOSI_R	
SCA8C31678	J6602 > 8		
SCA8C31678	J6602 > 9	FP_RST_N_R	
SCA8C31678	J6602 > 10		
SCA8C31678	J6602 > G1		
SCA8C31678	J6602 > G2		
SCA8C31678	J6602 > G3		
SCA8C31678	J6602 > G4		

UI: UIM

REF:4200-4499

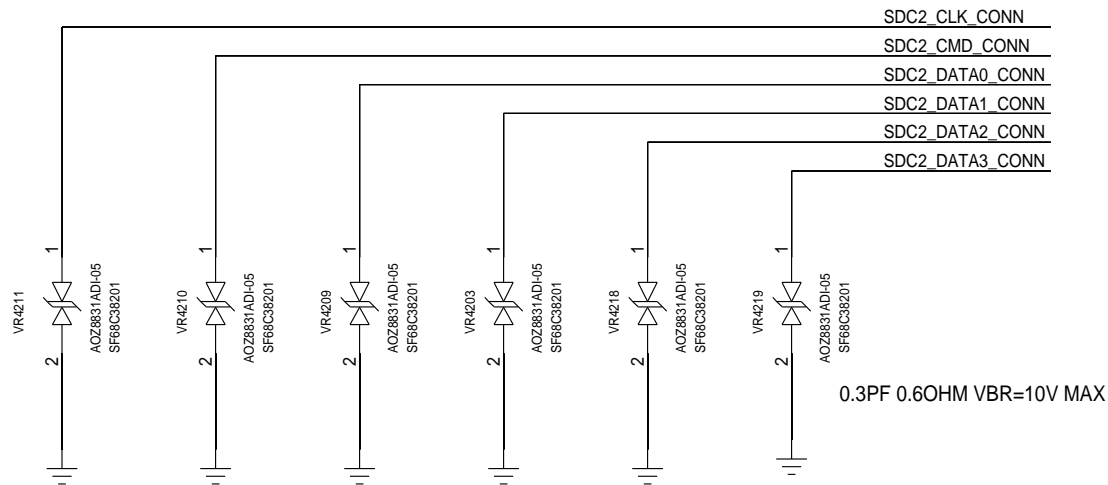
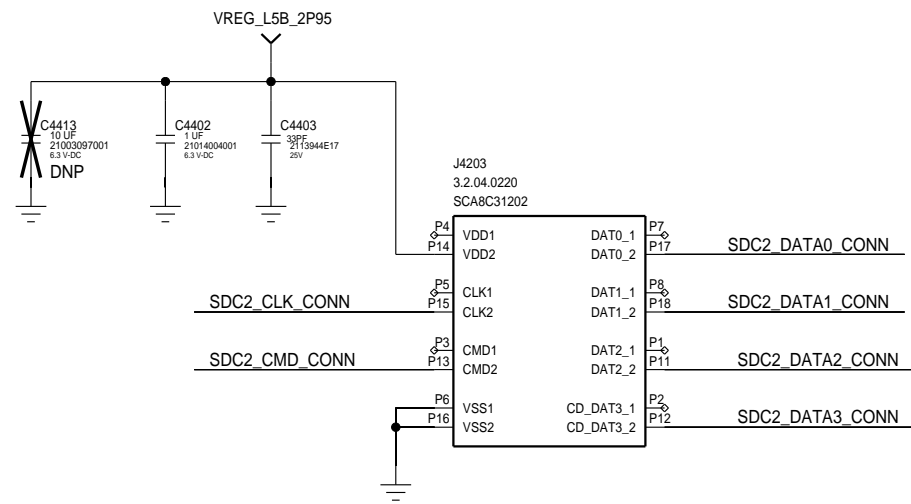
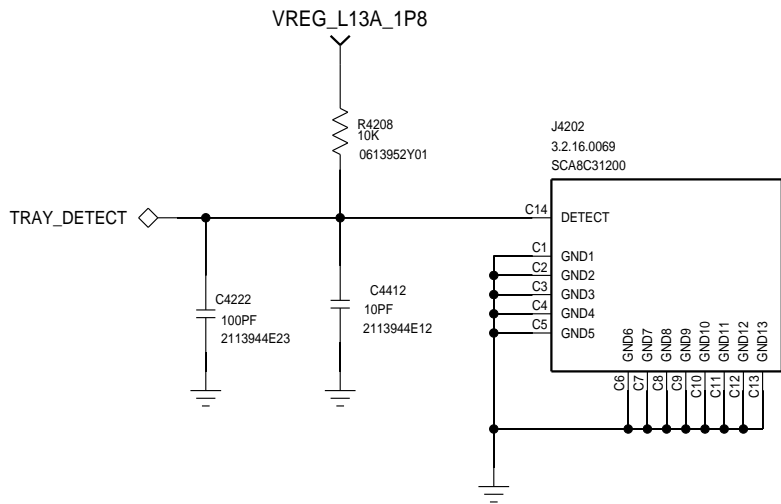
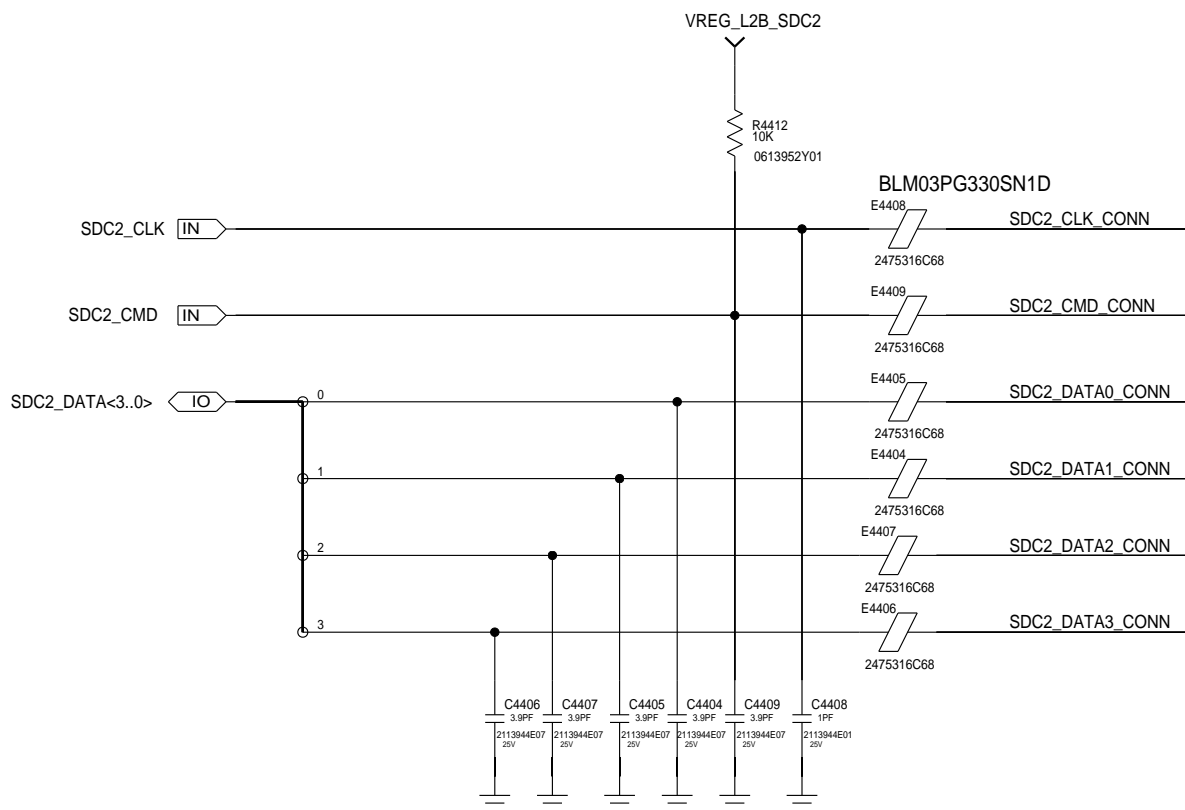


NOTE 1: USE 24.9K PULL-UP TO SUPPORT HOT SWAP WITH FREEZE-IO FEATURE.

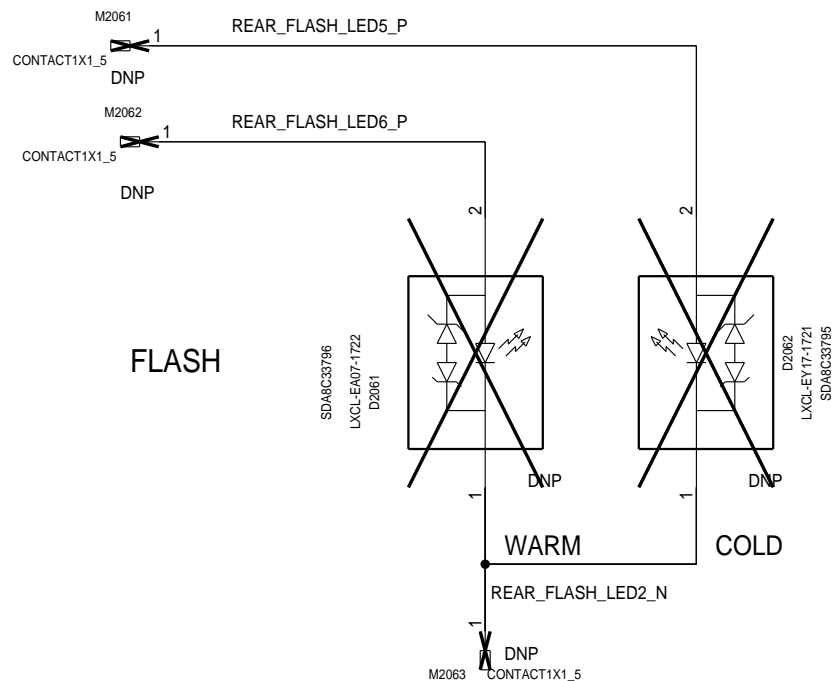
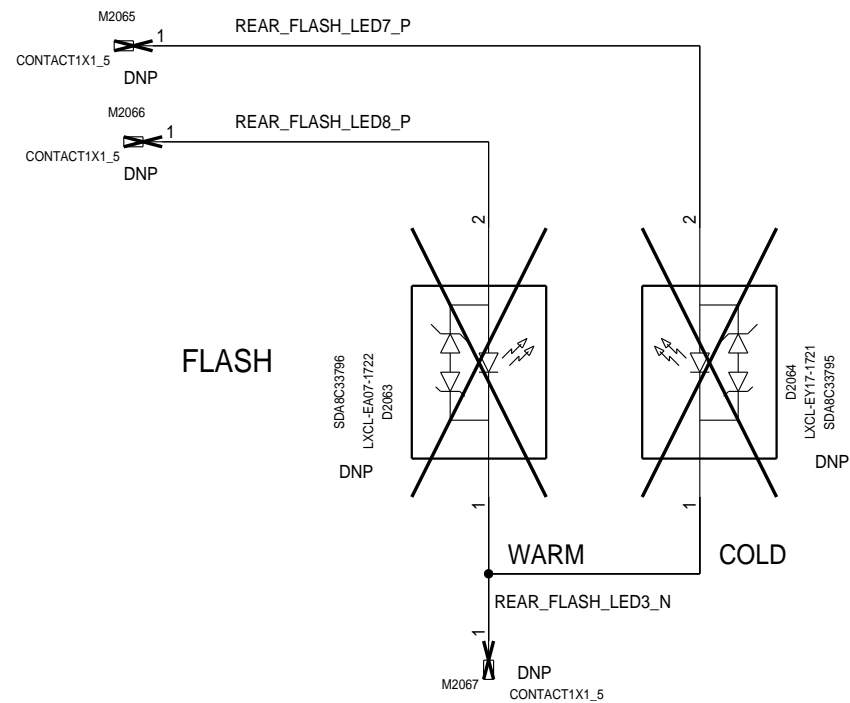
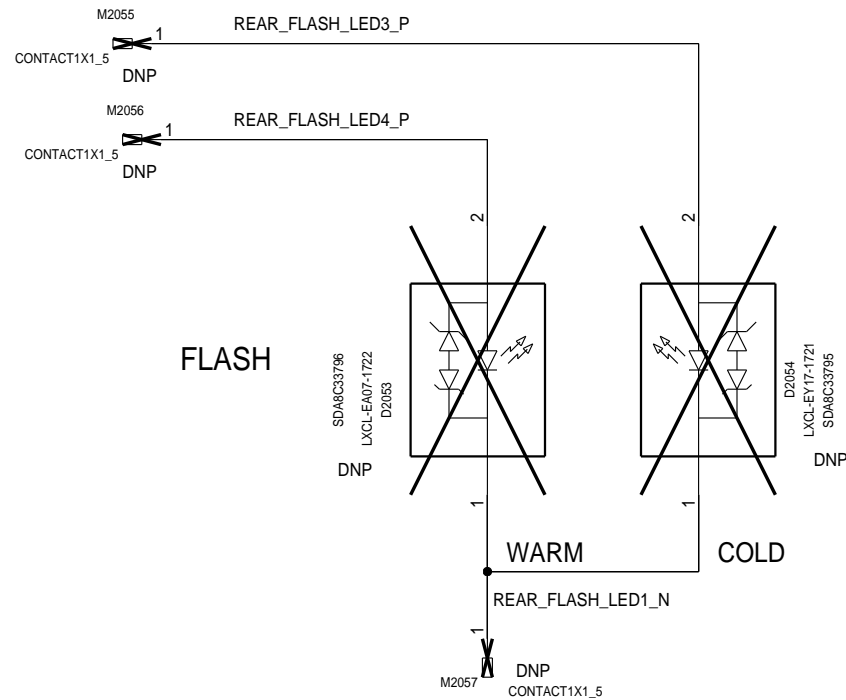
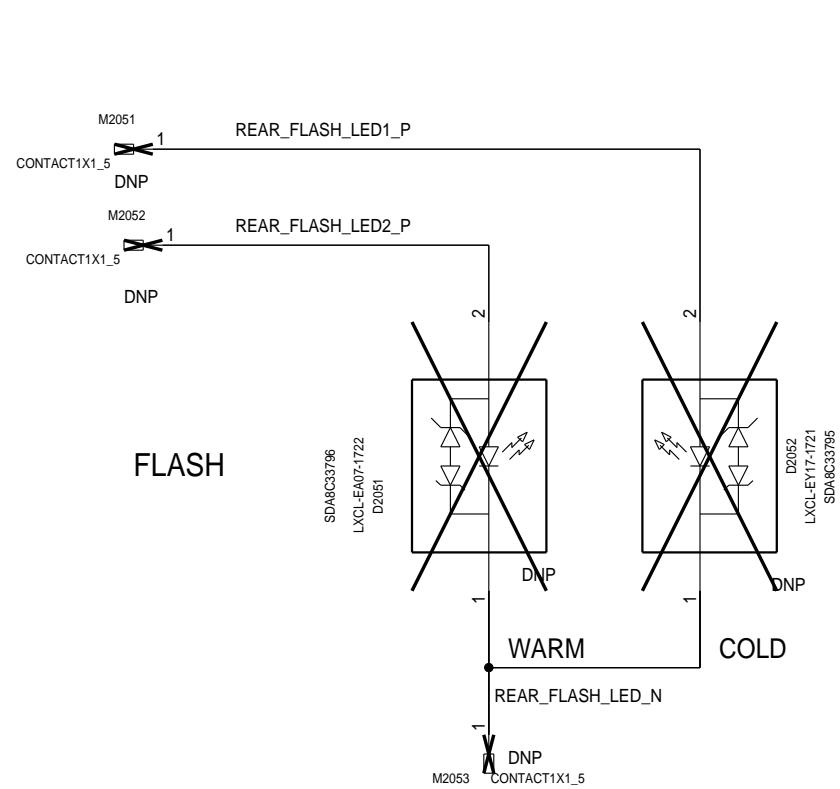
NOTE 2: UIM\_PWR BYPASS OF 0.33UF RECOMMENDED BY NFC IC SUPPLIER, PLACE NEAR CONNECTOR.

# UI: SDCARD

REF:4200-4499



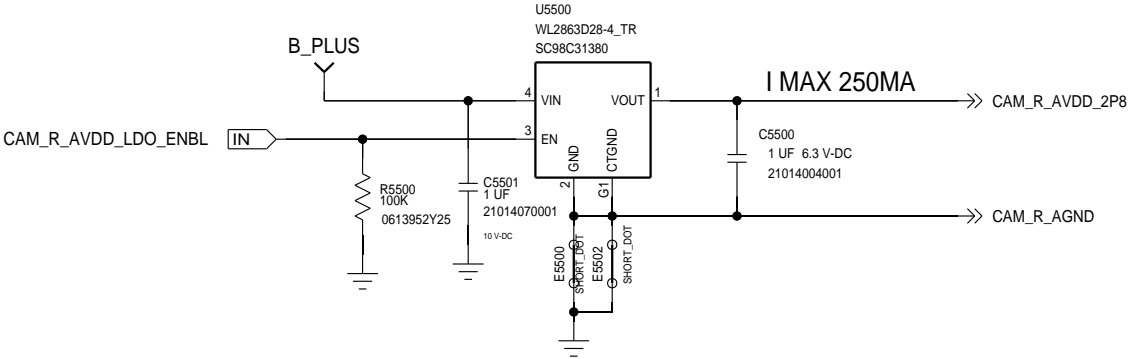
# CAM: REAR FLASH



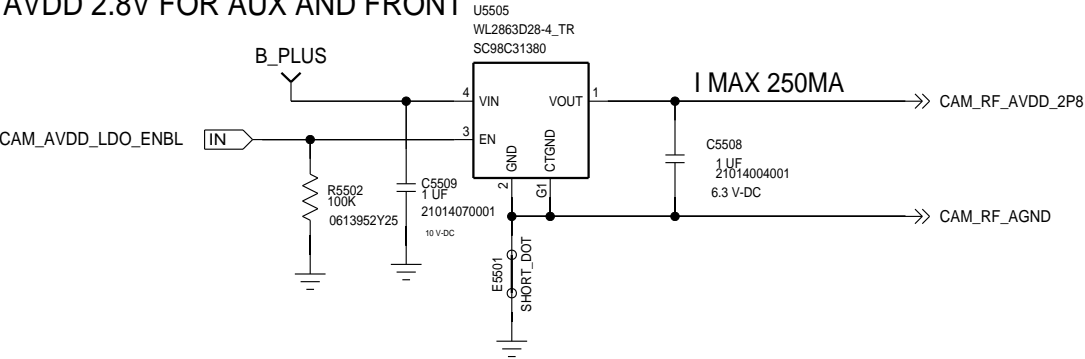
# CAM: POWER

REF:5500-5529

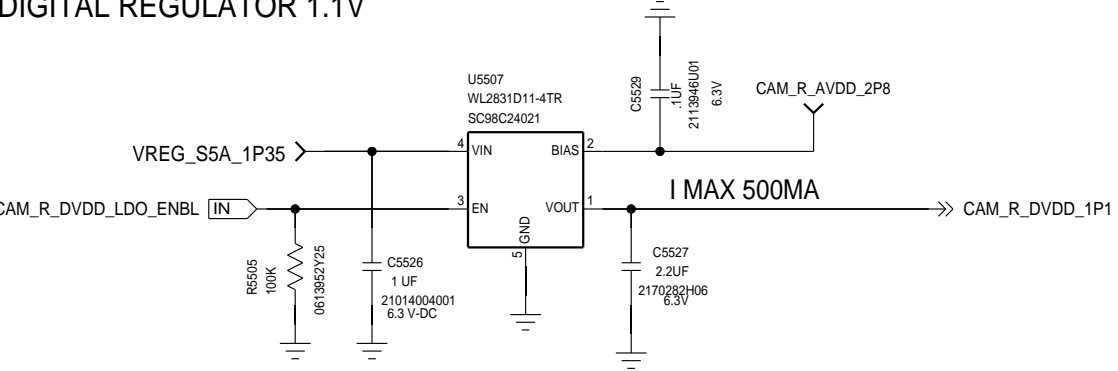
## AVDD 2.8V FOR REAR MAIN



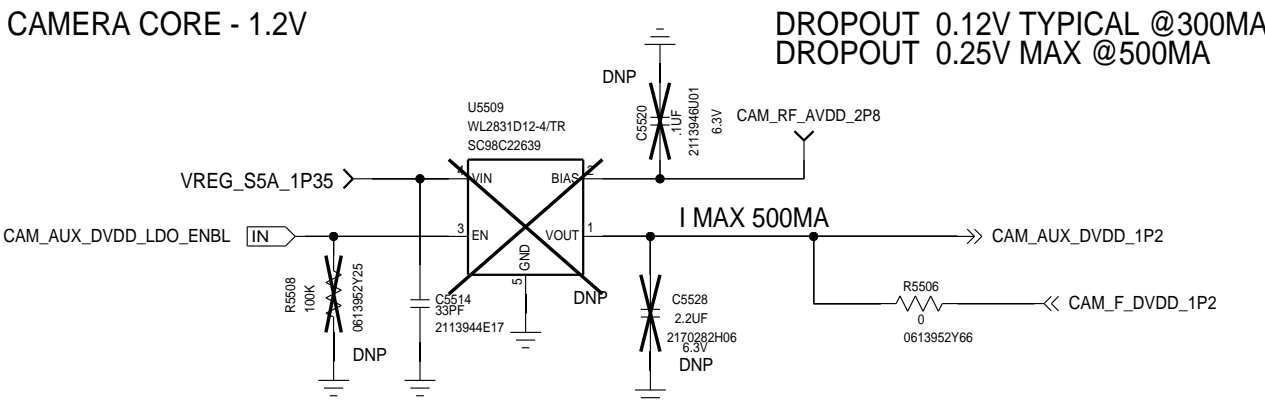
## AVDD 2.8V FOR AUX AND FRONT



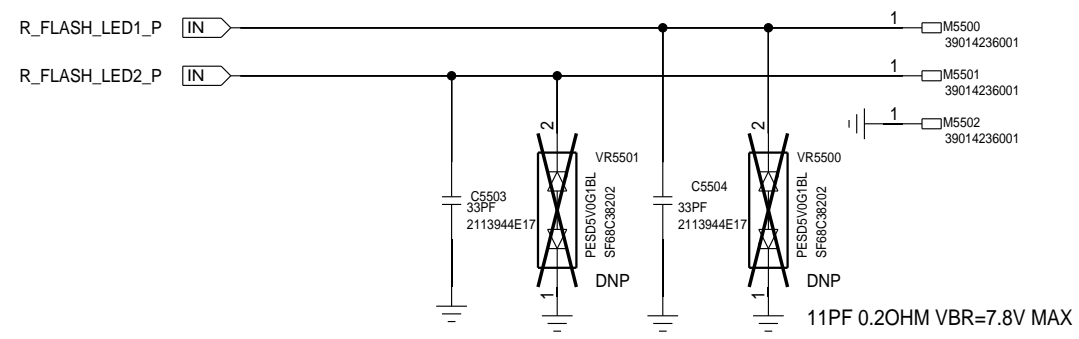
## DIGITAL REGULATOR 1.1V



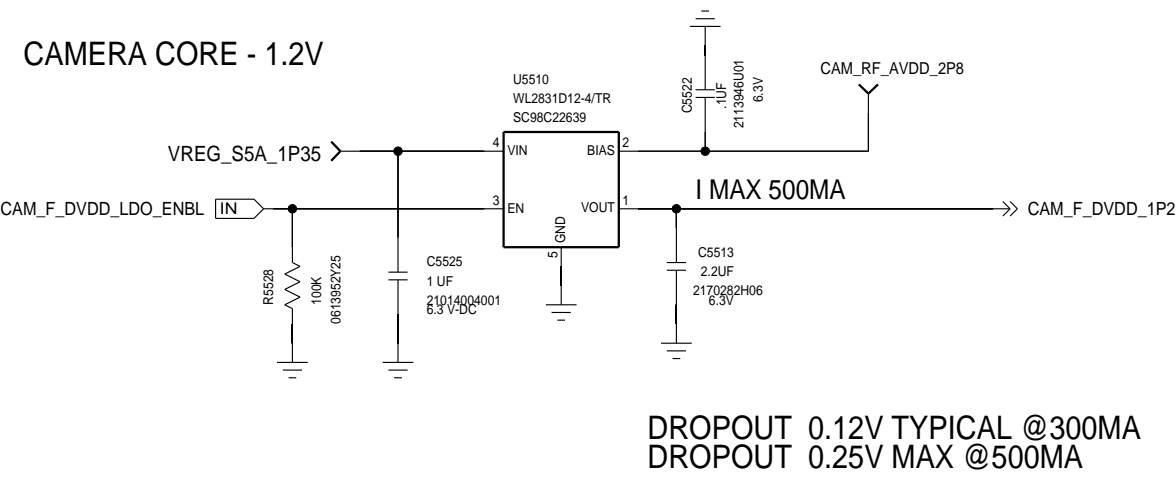
## CAMERA CORE - 1.2V



## REAR FACING FLASH REAR FLASH I MAX 1500MA FOR EACH



## CAMERA CORE - 1.2V



DROPOUT 0.12V TYPICAL @300MA  
DROPOUT 0.25V MAX @500MA



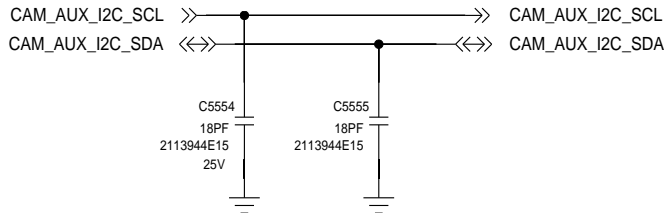
# CAM: FRONT CONNECTOR

REF 5550-5569

## FF CAMERA CONNECTOR

### I2C

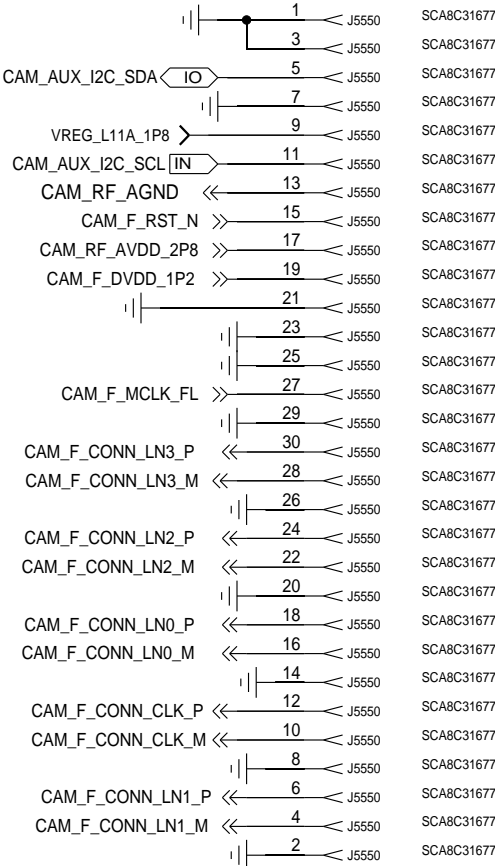
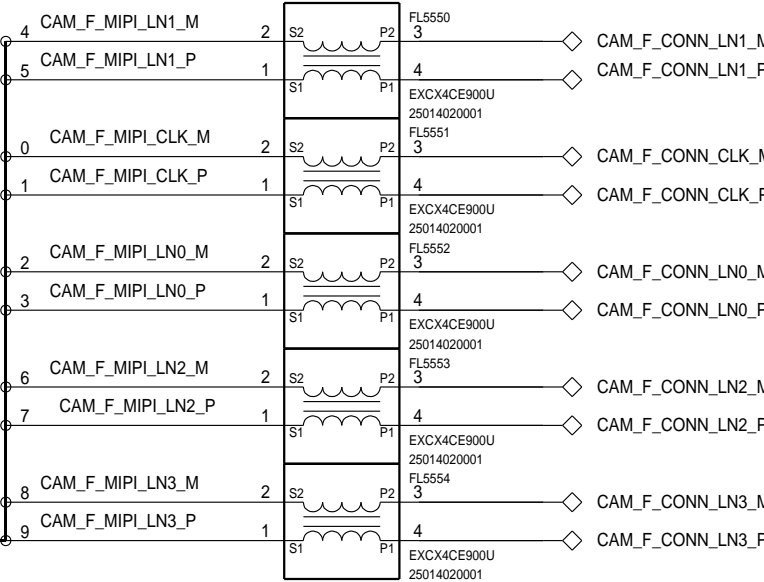
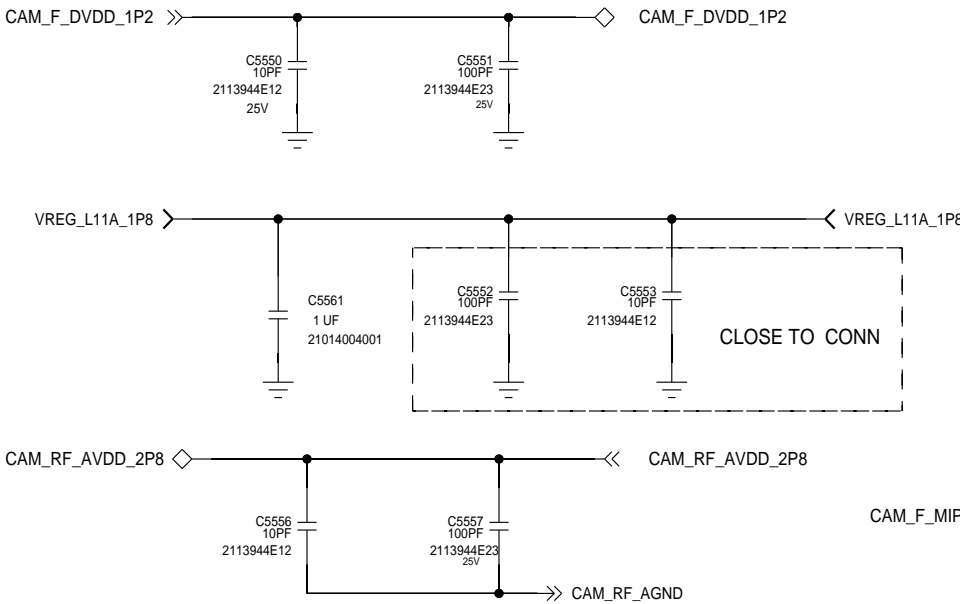
12M CAM I2C ADDR: 0X20(W), 0X21(R)



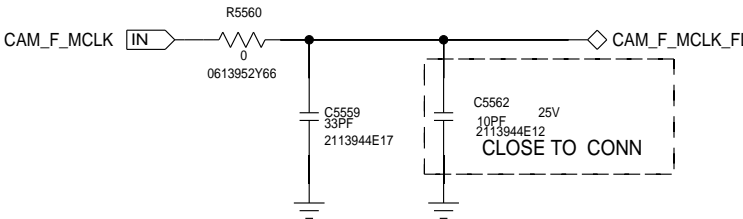
SENSOR: OV12A10  
12M, 1.25UM, 1/2.8", 4096X3072

CAM I2C ADDRESS  
FRONT OV12A10 CAM\_AUX\_I2C  
SENSOR: 0X20,0X21  
EEPROM: 0XA0,0XA1  
AUX S5K5E9YX04 CAM\_AUX\_I2C  
SENSOR: 0X20,0X21  
REAR MAIN IMX519 CAM\_I2C  
SENSOR: 0X34,0X35  
OIS: 0X48,0X49  
EEPROM: 0XA0,0XA1

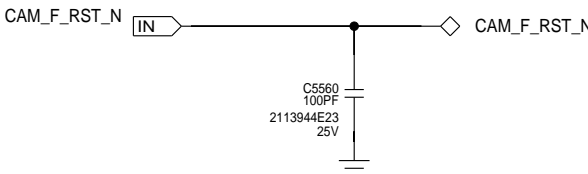
CAPACIORS BEADS ON THIS PAGE NEED BE PLACED CLOSE TO FRONT CAMERA CONNECTOR PIN.



### EMI/ESD FILTER



### XSHUTDOWN

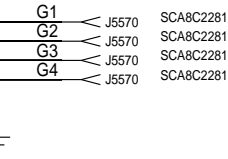
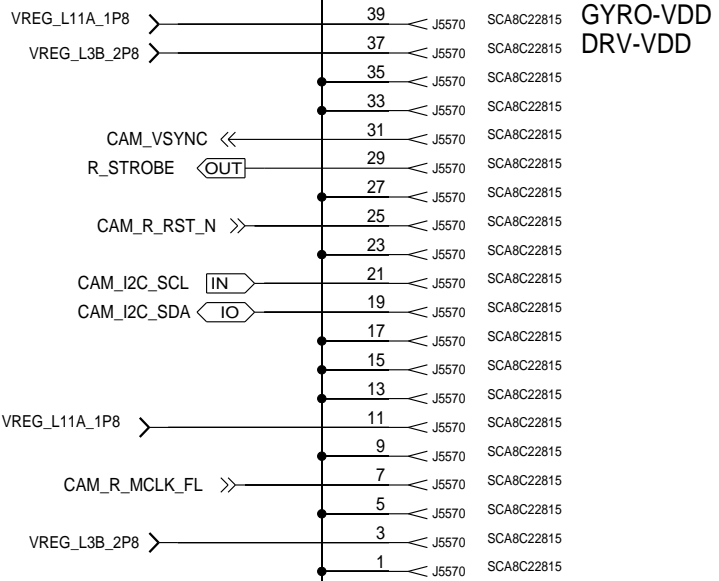
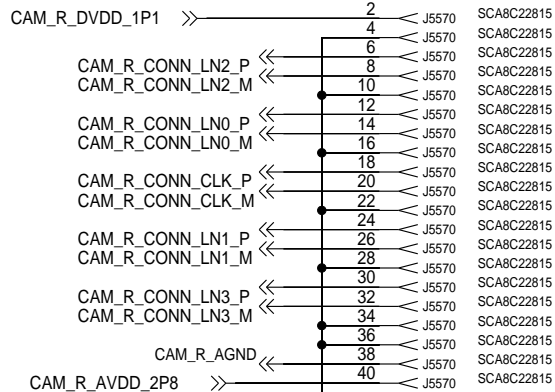
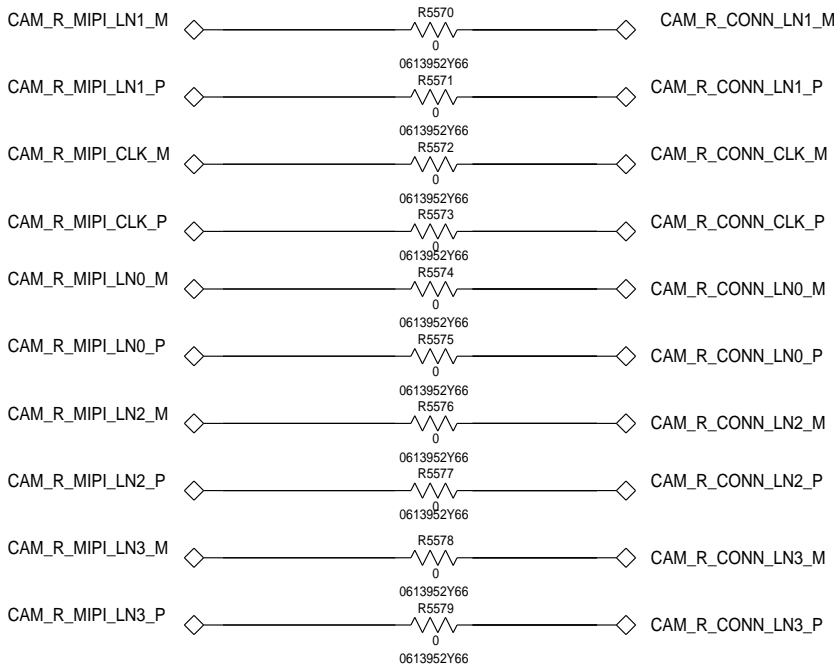
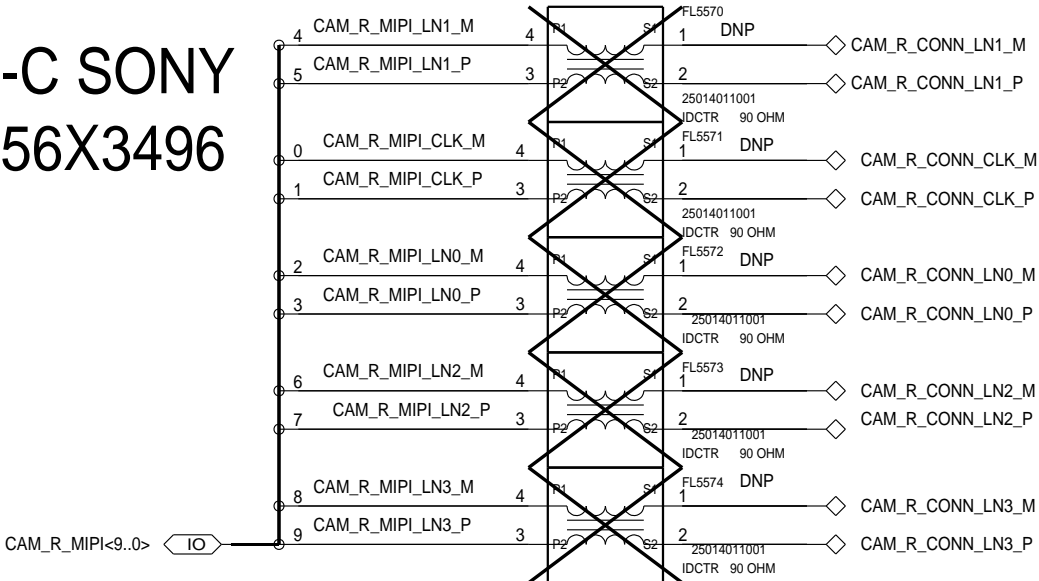
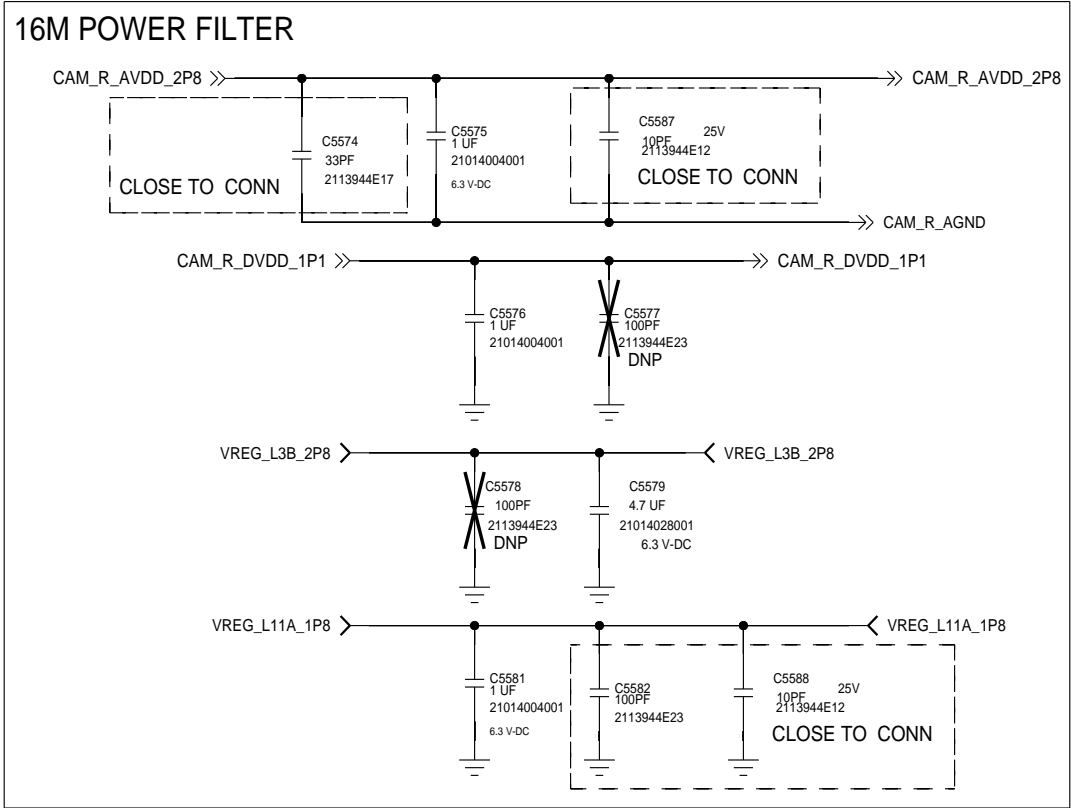
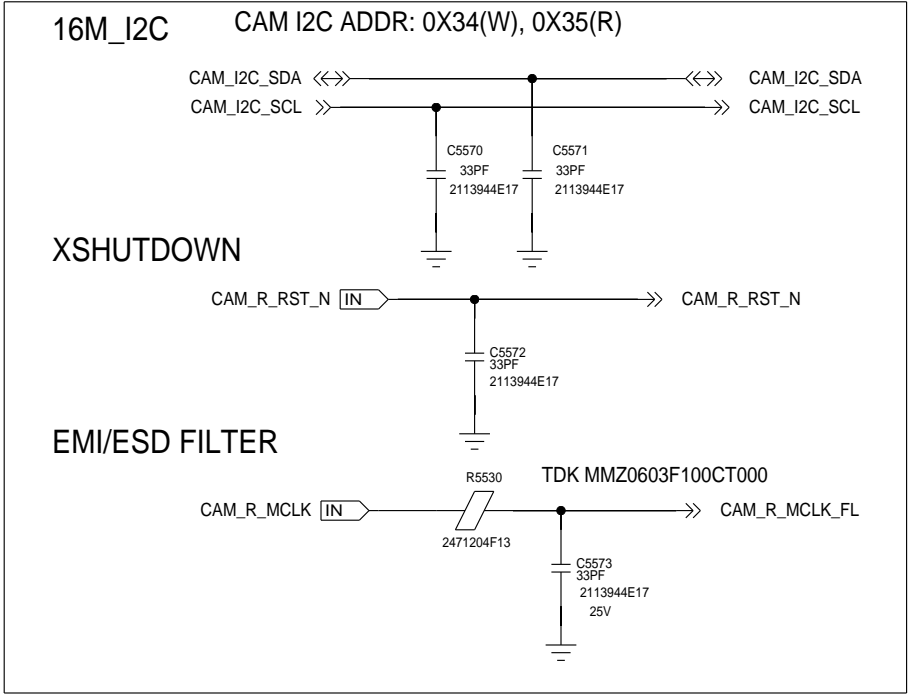


# cam: rear main connector

REF:5570-5589

## SENSOR: IMX519-AAQH5-C SONY

16M, 1.22UM, 1/2.534", 4656X3496



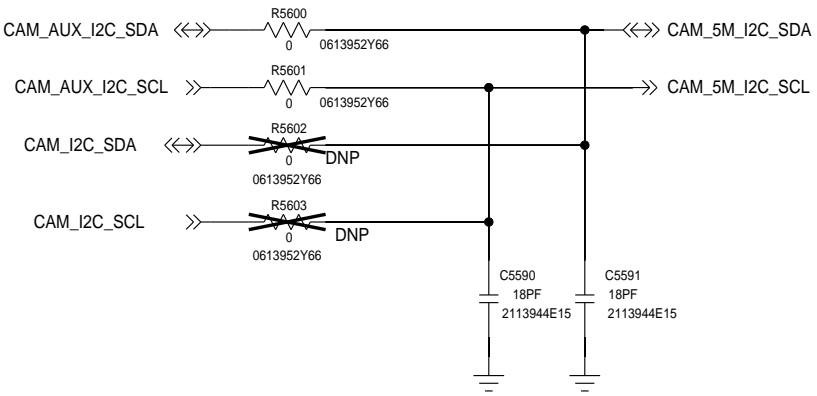
CAPACIORS BEADS ON THIS PAGE NEED BE PLACED CLOSE TO MAIN REAR CAMERA CONNECTOR PIN.

# CAM: REAR AUX CONNECTOR

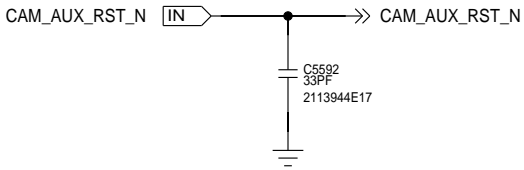
REF:5590-5610    5M CAM I2C ADDR: 0X20(W), 0X21(R)

SENSOR: S5K5E9YX04 SUMSANG  
5M, 1.12UM, 1/5", 2592X1944

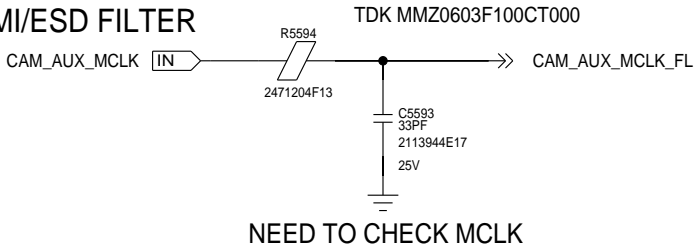
## AUX\_I2C



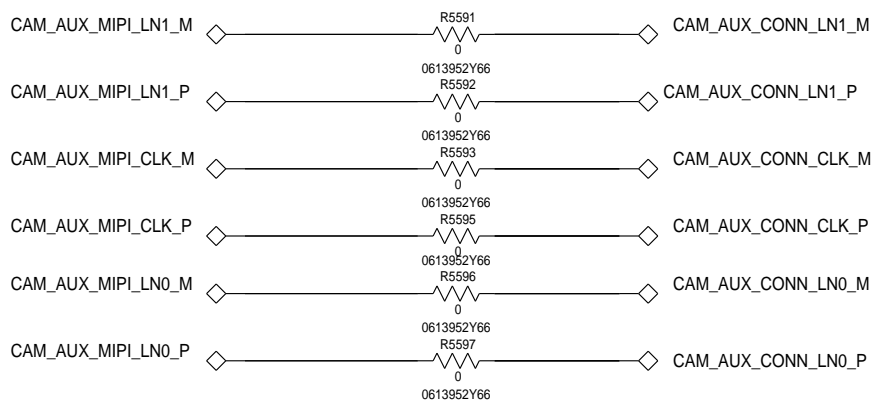
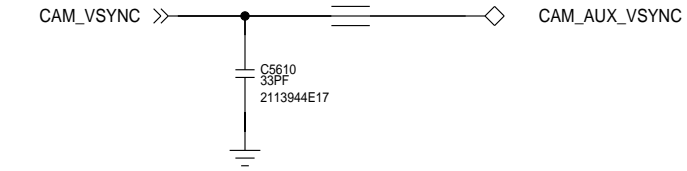
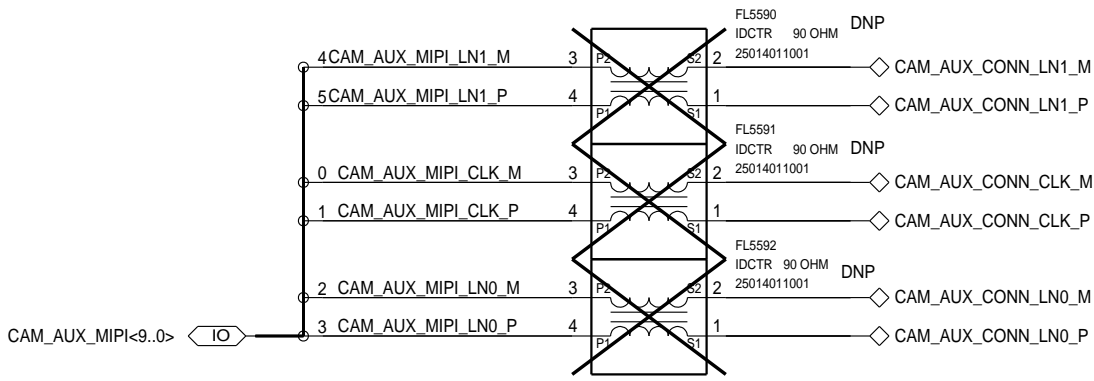
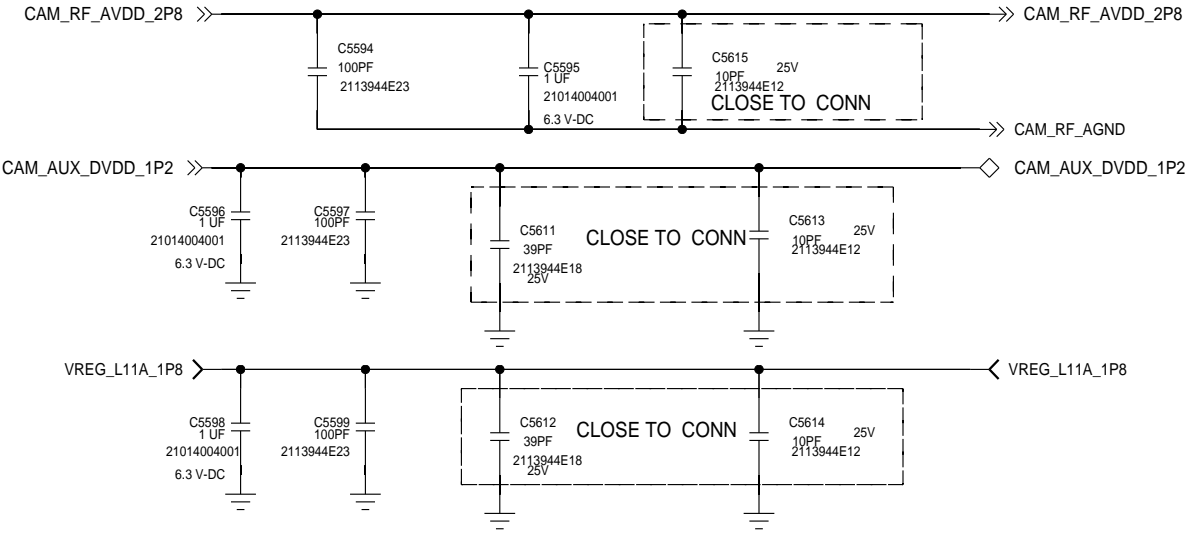
## XSHUTDOWN



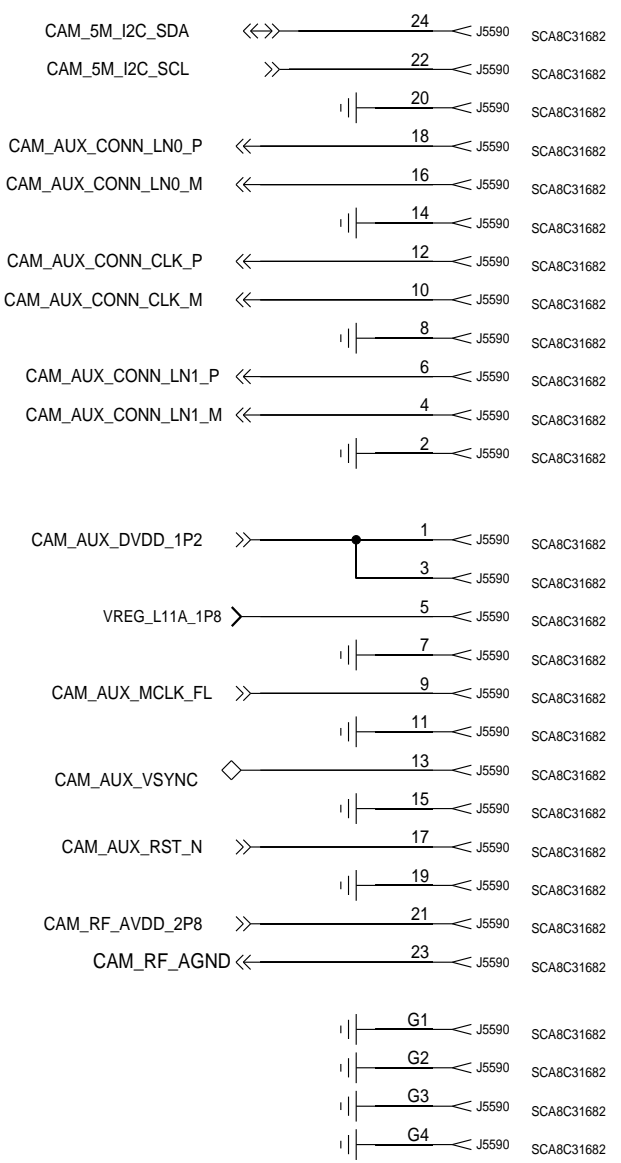
## EMI/ESD FILTER



## AUX POWER FILTER



CAPACIORS BEADS ON THIS PAGE NEED BE PLACED CLOSE TO AUX REAR CAMERA CONNECTOR PIN.



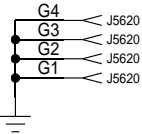
# DISP: CONNECTOR

REF:5620--5650

ATTENTION TO POWER SUPPLY

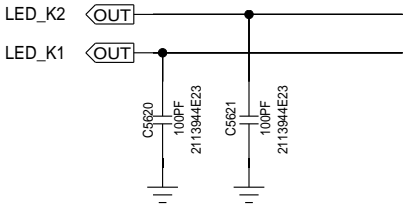
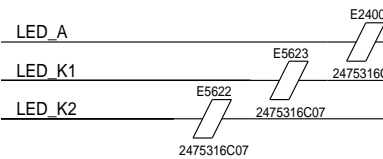
LCD BACKUP ID

1	J5620	SCA8C31679
LCD_ID 1	3	J5620
5	J5620	SCA8C31679
DATA 3N	7	J5620
DATA 3P	9	J5620
11	J5620	SCA8C31679
DATA 0N	13	J5620
DATA 0P	15	J5620
17	J5620	SCA8C31679
CLK_N	19	J5620
CLK_P	21	J5620
23	J5620	SCA8C31679
DATA 1N	25	J5620
DATA 1P	27	J5620
29	J5620	SCA8C31679
DATA 2N	31	J5620
DATA 2P	33	J5620
35	J5620	SCA8C31679
LED_PWM	37	J5620
LCD_TE_1	39	J5620
LCD_RST_N_1	40	J5620
IOVCC	38	J5620

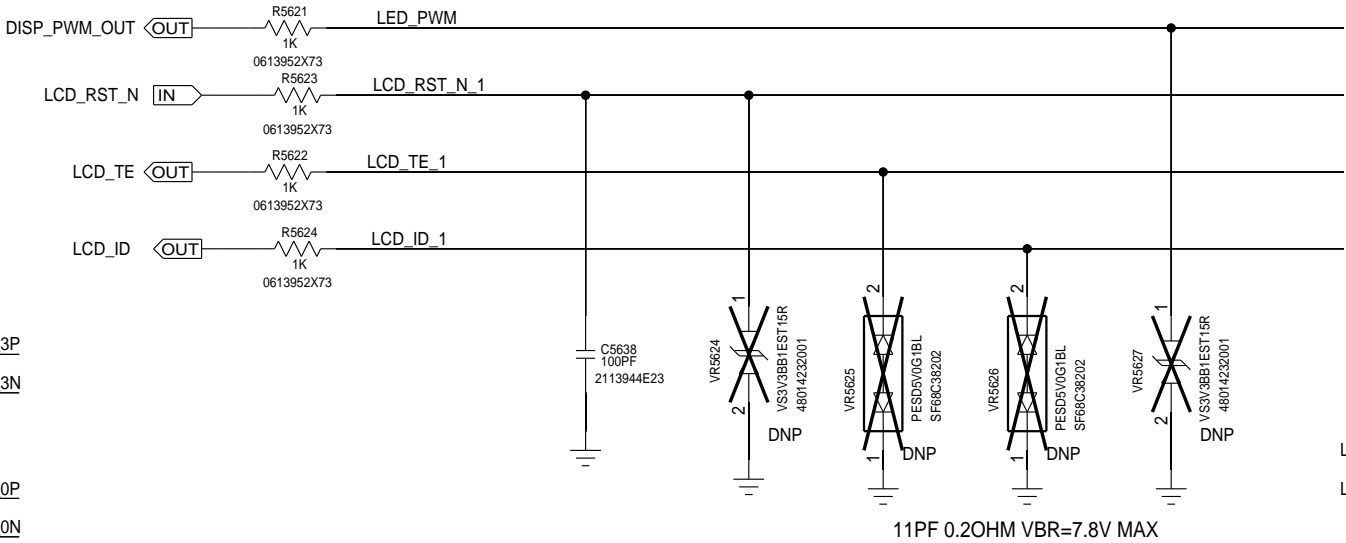
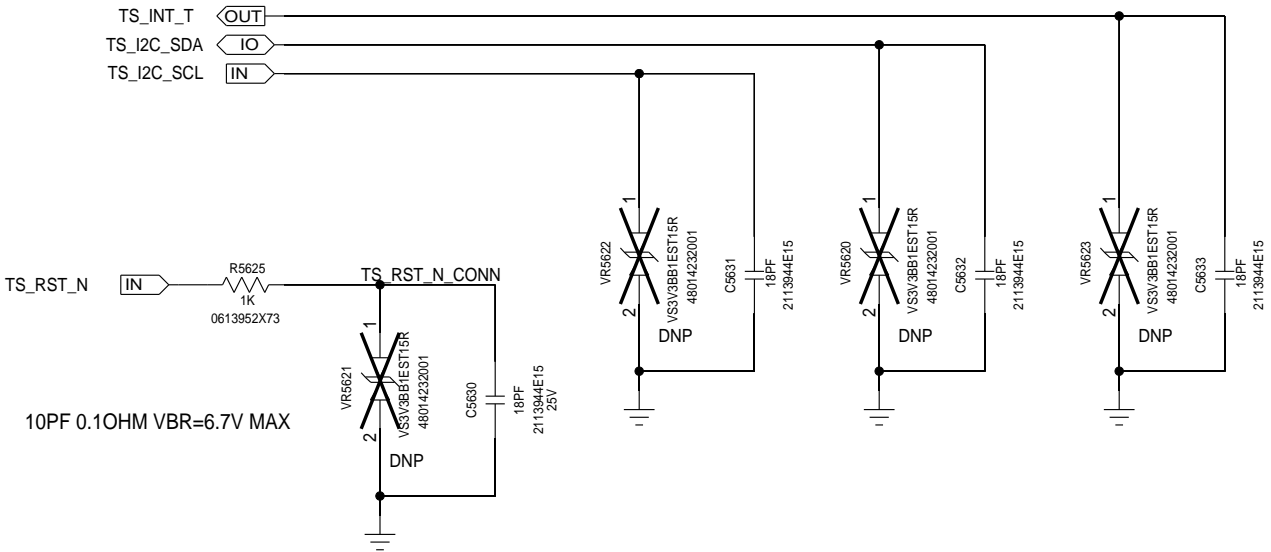
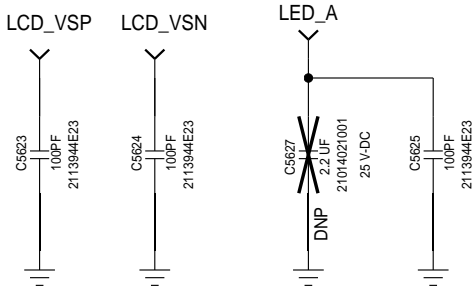
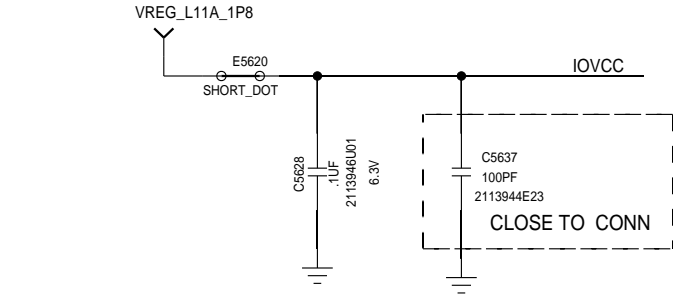


36	J5620	SCA8C31679
LCD_VSP	34	J5620
LCD_VSN	32	J5620
30	J5620	SCA8C31679
28	J5620	SCA8C31679
26	J5620	SCA8C31679
24	J5620	SCA8C31679
22	J5620	SCA8C31679
20	J5620	SCA8C31679
18	J5620	SCA8C31679
16	J5620	SCA8C31679
14	J5620	SCA8C31679
12	J5620	SCA8C31679
10	J5620	SCA8C31679
8	J5620	SCA8C31679
6	J5620	SCA8C31679
4	J5620	SCA8C31679
2	J5620	SCA8C31679

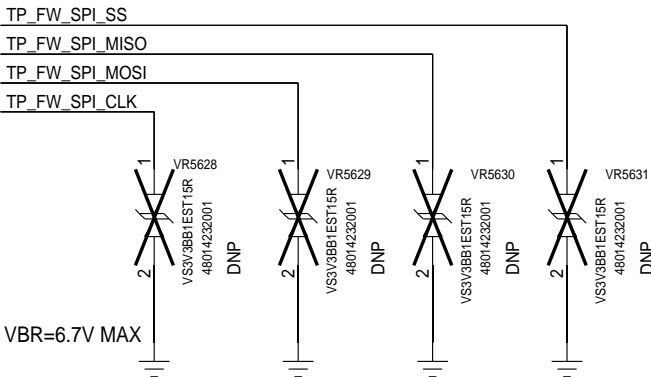
BLM03HG102SN1D



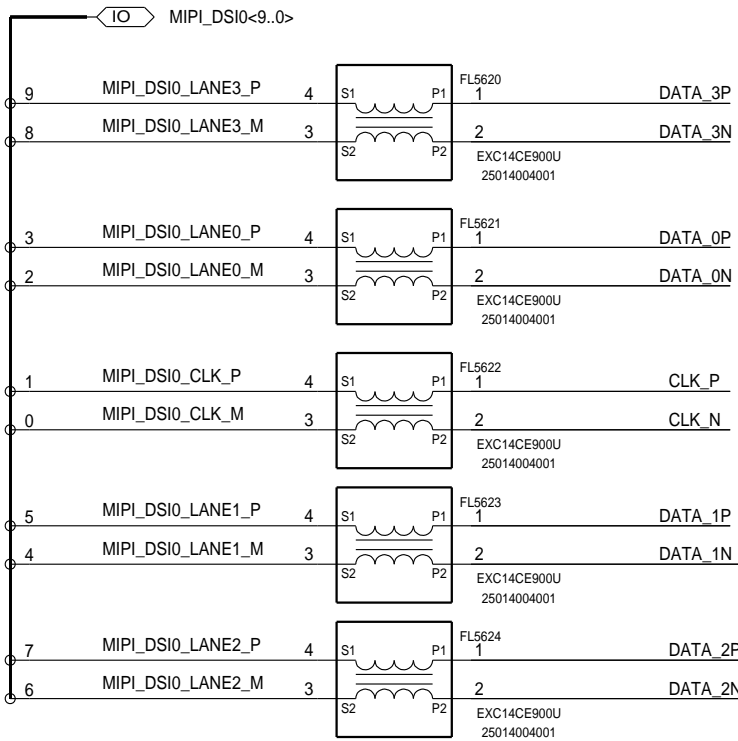
TP_FW_SPI_SS	IN	10	J5620	SCA8C31679
TP_FW_SPI_MISO	OUT	8	J5620	SCA8C31679
TP_FW_SPI_MOSI	IN	6	J5620	SCA8C31679
TP_FW_SPI_CLK	IN	4	J5620	SCA8C31679
		2	J5620	SCA8C31679



11PF 0.2OHM VBR=7.8V MAX



10PF 0.1OHM VBR=6.7V MAX

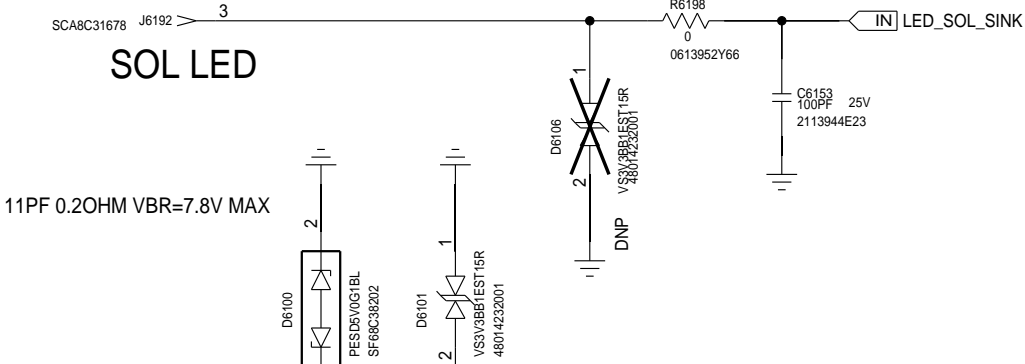


GND SHIELDED FOR MIPI DSI SIGNALS AS A GROUP  
ADD GND STITCHING VIAS FOR EACH SIGNAL GROUP (EBI,CSI,DSI) WHEN POSSIBLE TO MINIMIZE RETURN CURRENT LOOP

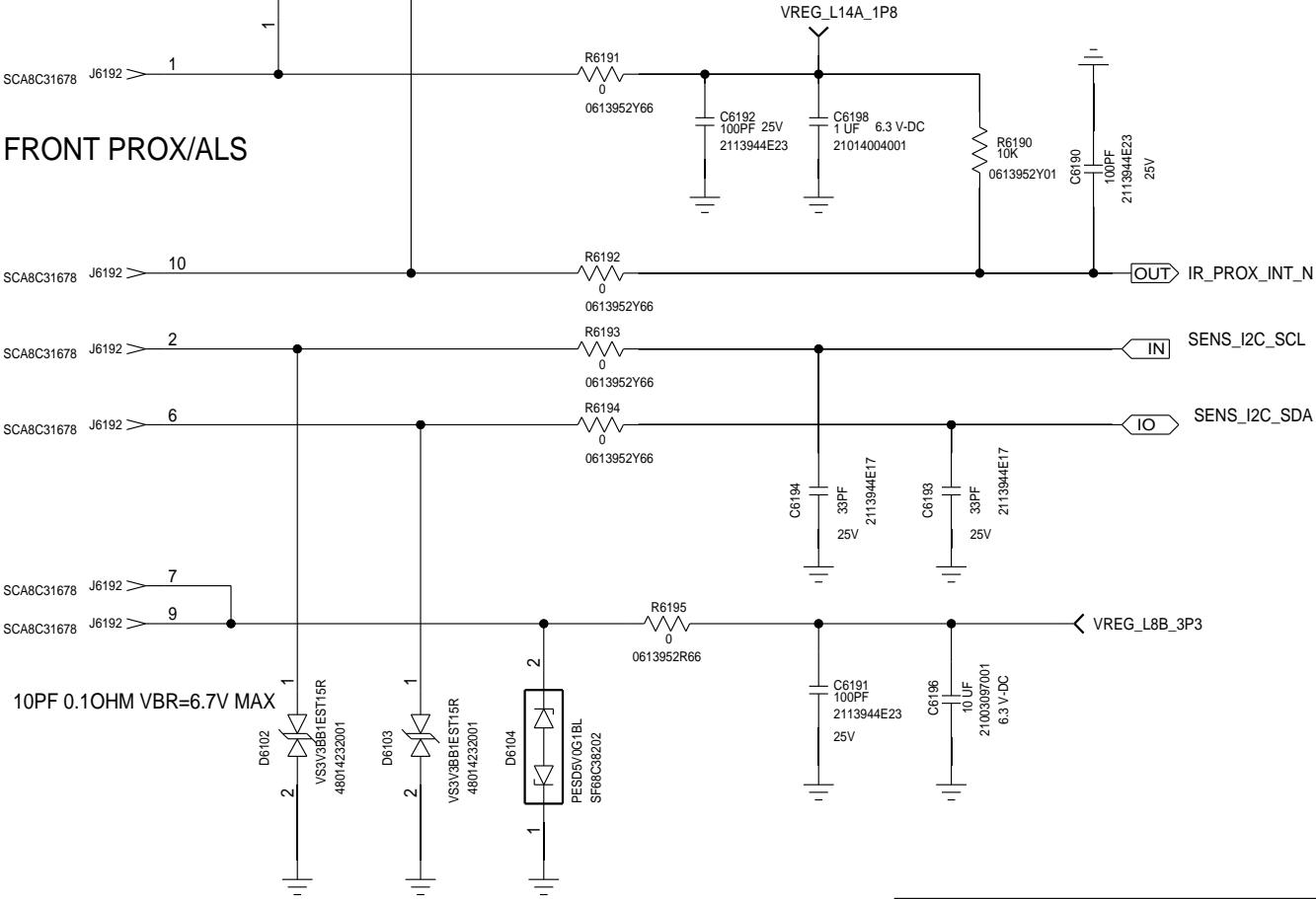
XMCS: SENSORS

REF: 6100-6199

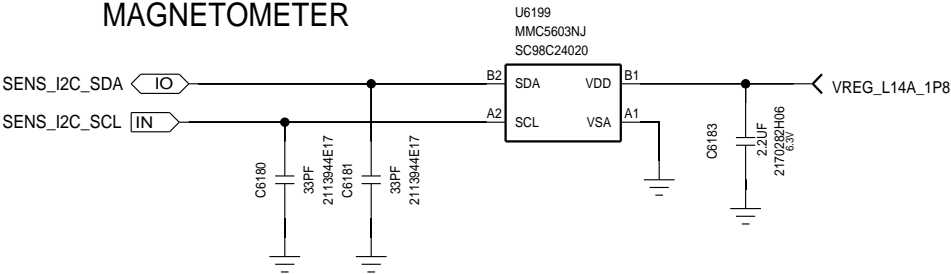
SOL LED



FRONT PROX/ALS



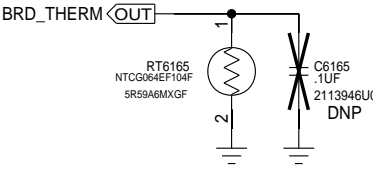
MAGNETOMETER



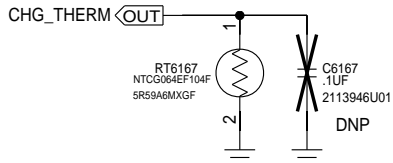
I2C ADDRESS  
MMC5603NJ 0X60,0X61  
CS47L35 0X34,0X35  
TMD3702V 0X49,0X4A

AWAY FORM NOISY SIGNALS AND HIGH POWER SOURCES

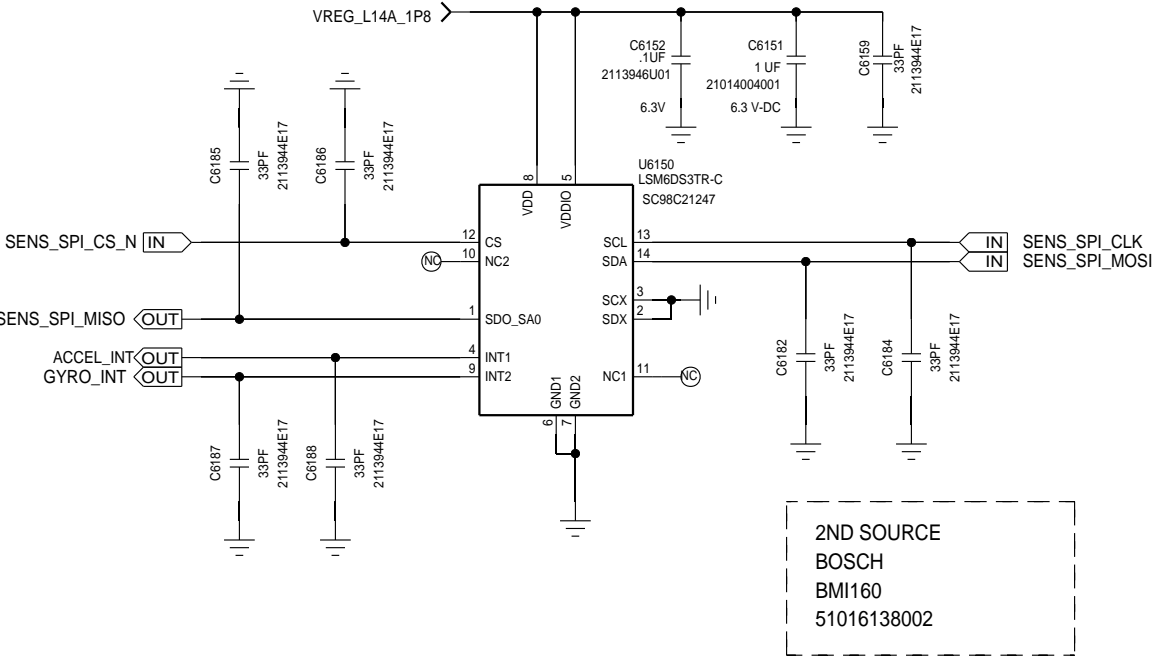
BOARD TEMP SENSOR



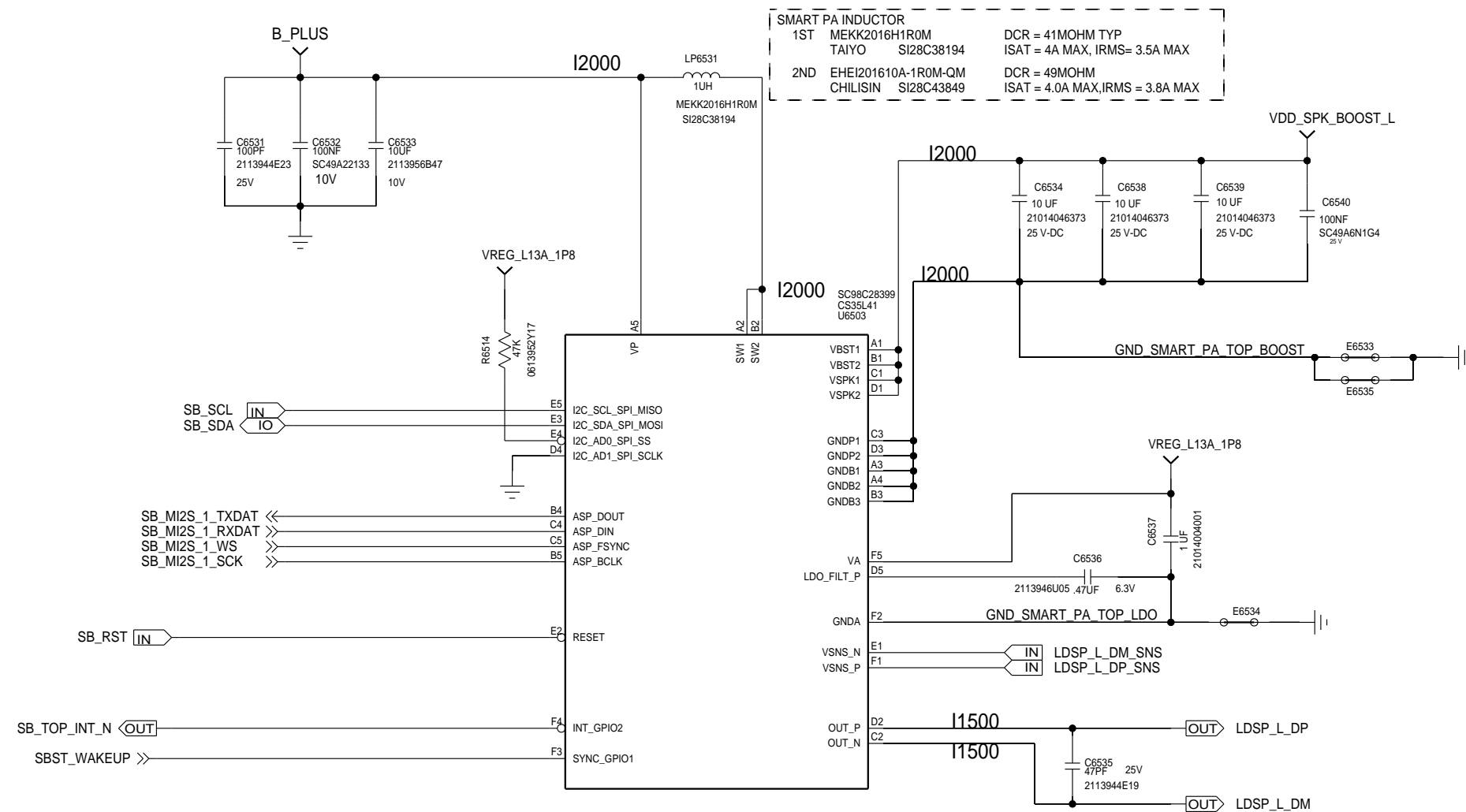
CHRG TEMP SENSOR



ACCELEROMETER+GYRO



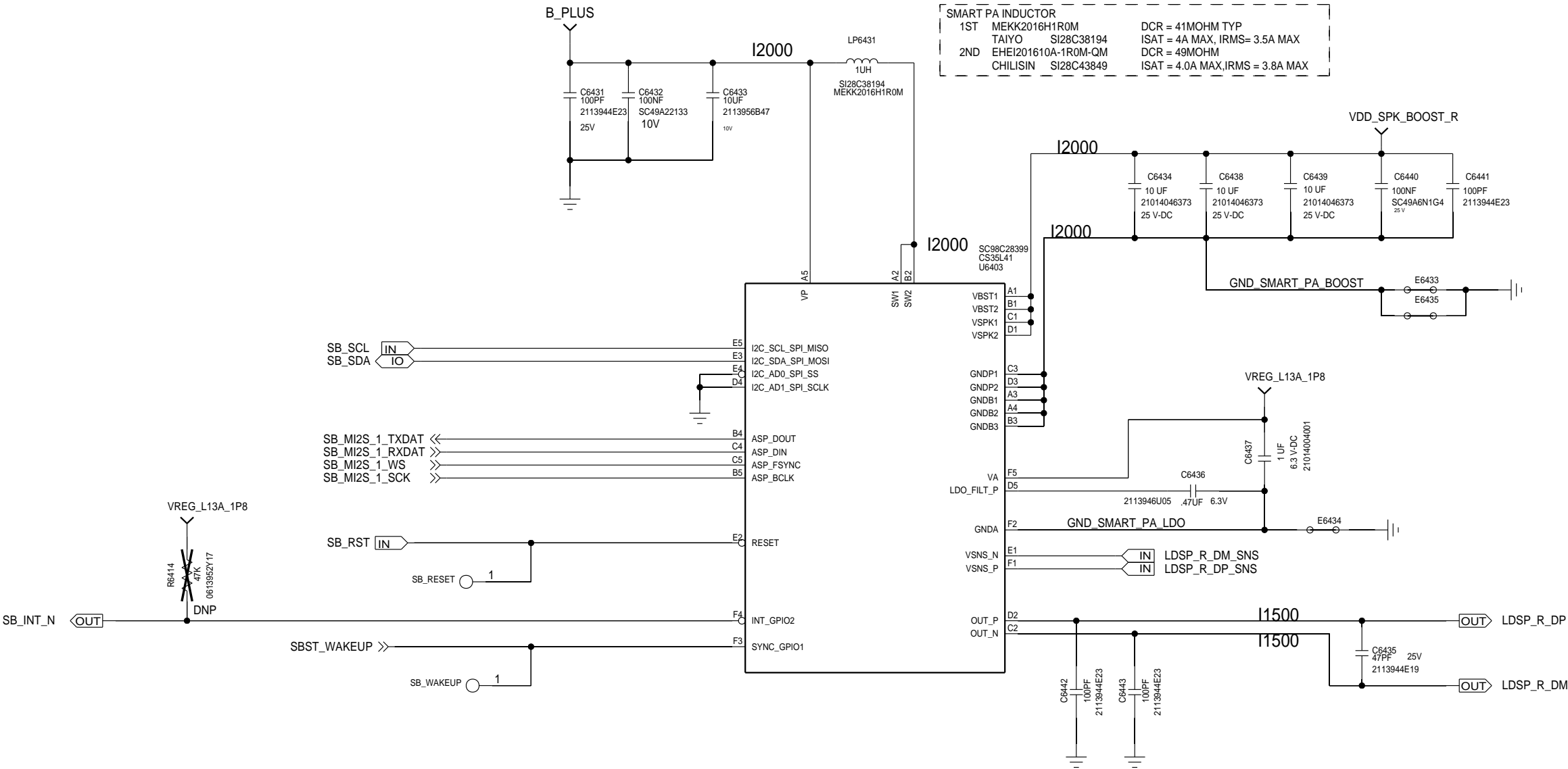
```
*** BOOST IC I2C ADD. 0X??
```



# XMCS: SMART BOOST

REF 6400-6499

\*\*\* BOOST IC I2C ADD. 0X??



**XMCS: CIRRUS CODEC**  
REF 6250-6299

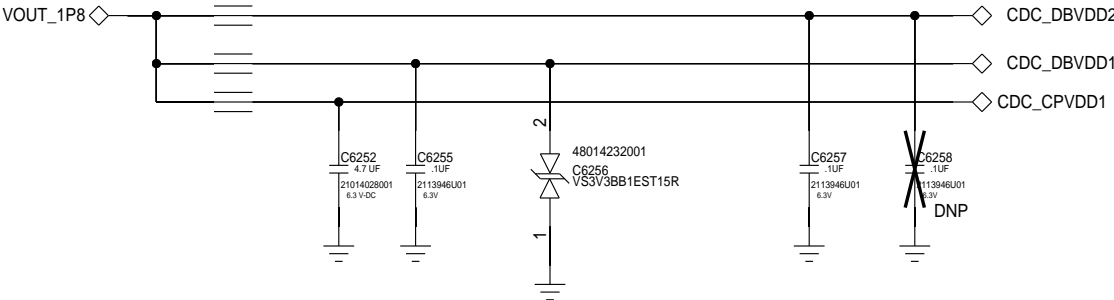
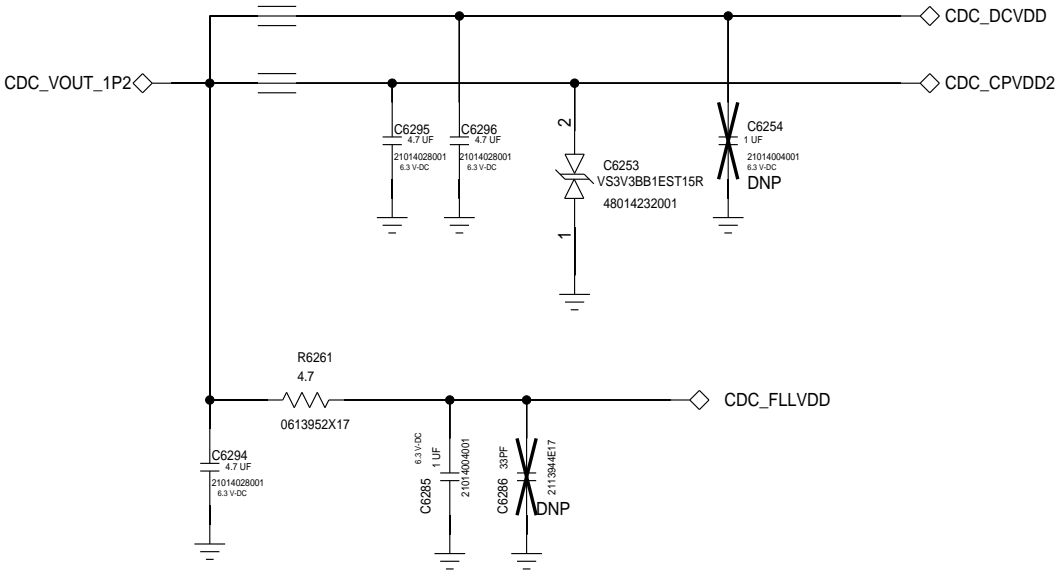
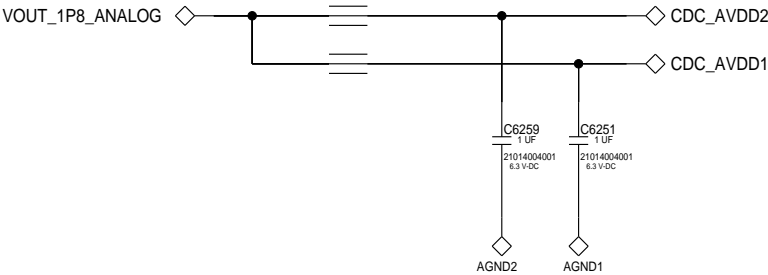
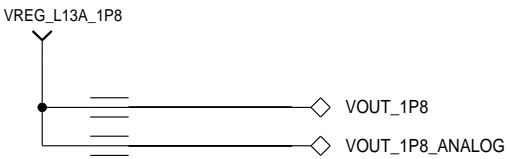
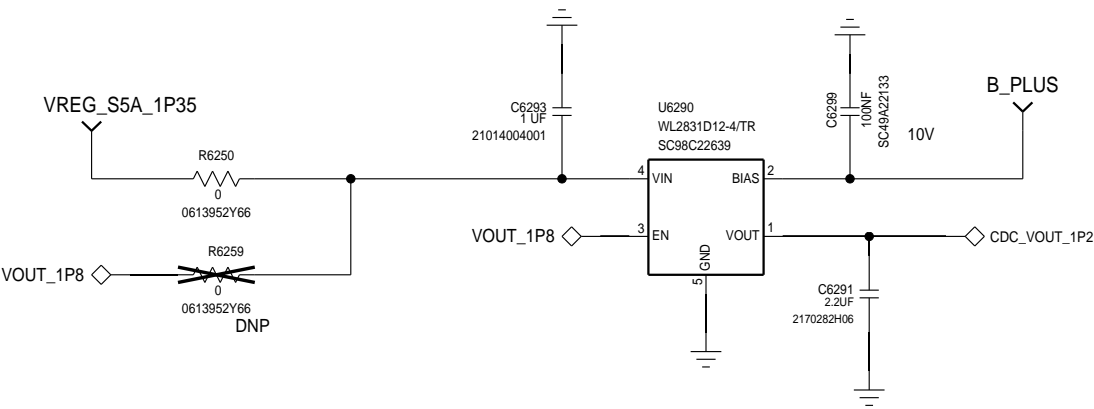


# XMCS: CODEC POWER

REF 6250-6299

## 1.2V POWER SUPPLY

DROPOUT 0.12V TYPICAL @300MA  
DROPOUT 0.25V MAX @500MA



# AFE: MICS


REF: 5700-5789


## PRIMARY MIC

REF: 5700-5709

GND SHIELD, AWAY FROM NOISY SIGNAL

PRI\_MIC\_BIAS 

PRI\_MIC\_DP 

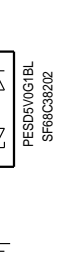
PRI\_MIC\_DM 

ROUTE AS DIFF PAIR, GND SHIELD.  
TRY TO AWAY FROM RFPA, CHARGER,  
SMPS,AUDIO SMART PA,DDR



PESDV0G1BL  
SF86C38202

DNP



PESDV0G1BL  
SF86C38202

DNP

C5701

100PF

211394E23

C5703

18PF

211394E15

DNP

E5700

SHORT\_DOT

AAC MIC  
BOTTOM MIC

E5700 LOCATED CLOSED TO MIC OUTPUT


MIC 2ND SOURCE  
KNOWLES  
SPV08A7LR5H-1  
SM48C31211


## SECONDARY MIC

REF:5750-5759

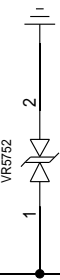
GND SHIELD, AWAY FROM NOISY SIGNAL

SEC\_MIC\_BIAS 

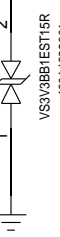
SEC\_MIC\_DP 

SEC\_MIC\_DM 

ROUTE AS DIFF PAIR, GND SHIELD.  
TRY TO AWAY FROM RFPA, CHARGER,  
SMPS,AUDIO SMART PA,DDR



VS3V38B1EST15R  
48014232001



VS3V38B1EST15R  
48014232001

C5750

100PF

211394E23

C5751

18PF

211394E15

DNP

E5750

SHORT\_DOT

TOP MIC

E5750 LOCATED CLOSED TO MIC OUTPUT

MK5750

CS7331P-CAZR

50014051004

C5753

18PF

211394E15

DNP

C5753

100PF

211394E23

C5759

100PF


211394E23


## TERTIARY MIC

REF: 5725-5730

GND SHIELD, AWAY FROM NOISY SIGNAL

TER\_MIC\_BIAS 

TER\_MIC\_DP 

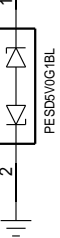
TER\_MIC\_DM 

ROUTE AS DIFF PAIR, GND SHIELD.  
TRY TO AWAY FROM RFPA, CHARGER,  
SMPS,AUDIO SMART PA,DDR



PESDV0G1BL  
SF86C38202

DNP



PESDV0G1BL  
SF86C38202

DNP

C5726

100PF

211394E23

C5730

18PF

211394E15

DNP

E5725

SHORT\_DOT

AAC MIC  
BOTTOM MIC

E5725 LOCATED CLOSED TO MIC OUTPUT

11PF 0.20HM VBR=7.8V MAX

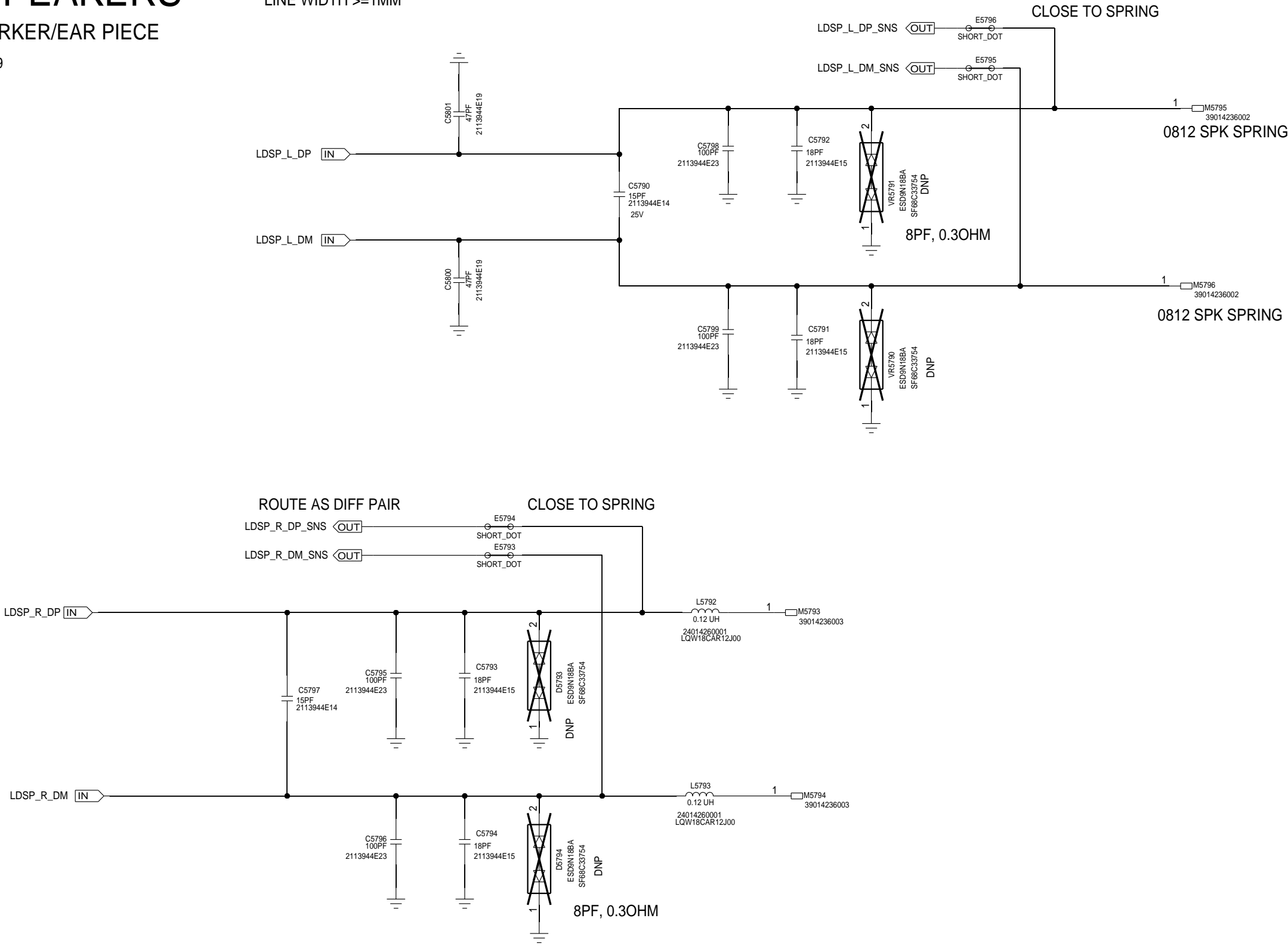
AFE: SPEAKERS

LOUDSPEAKER/EAR PIECE

REF:5790-5799

ROUTE AS DIFF PAIR  
LINE WIDTH >=1MM

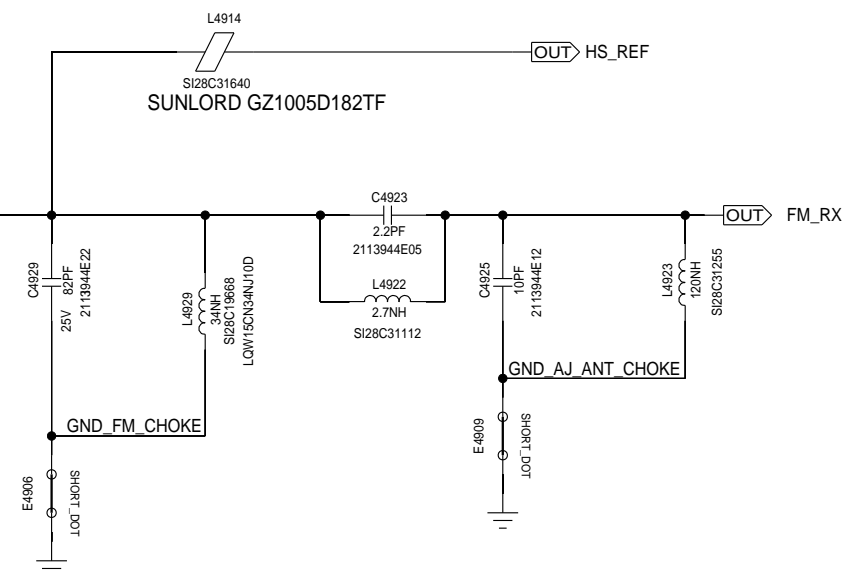
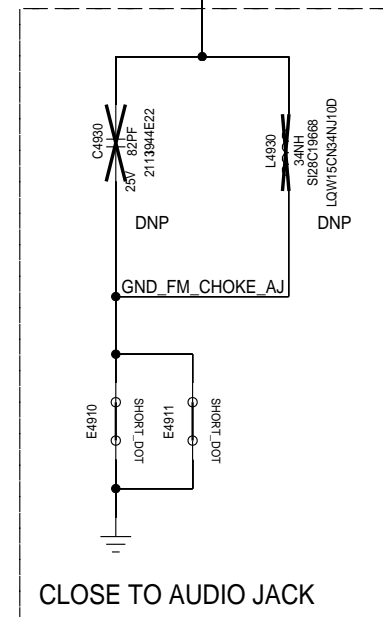
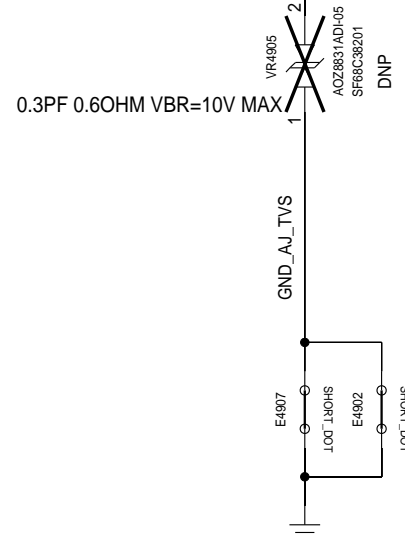
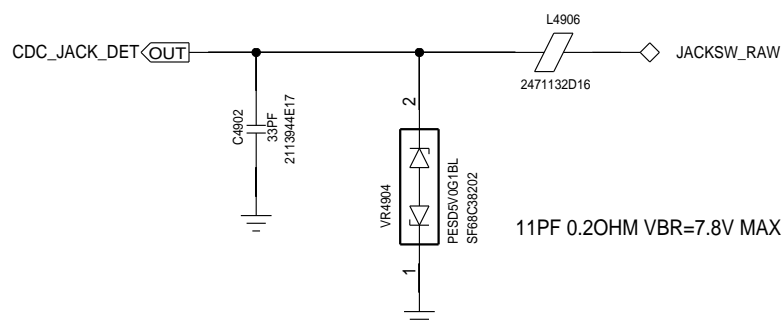
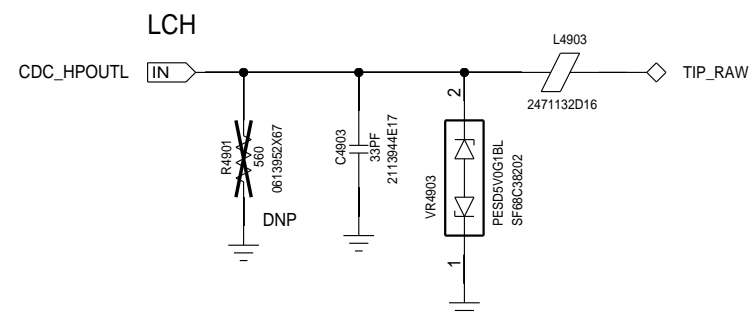
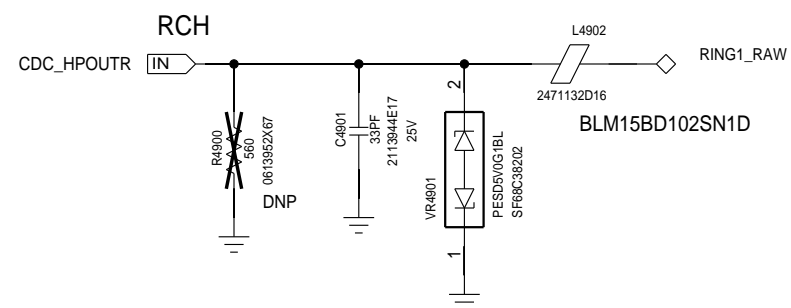
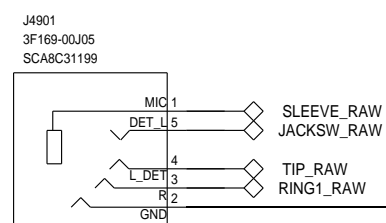
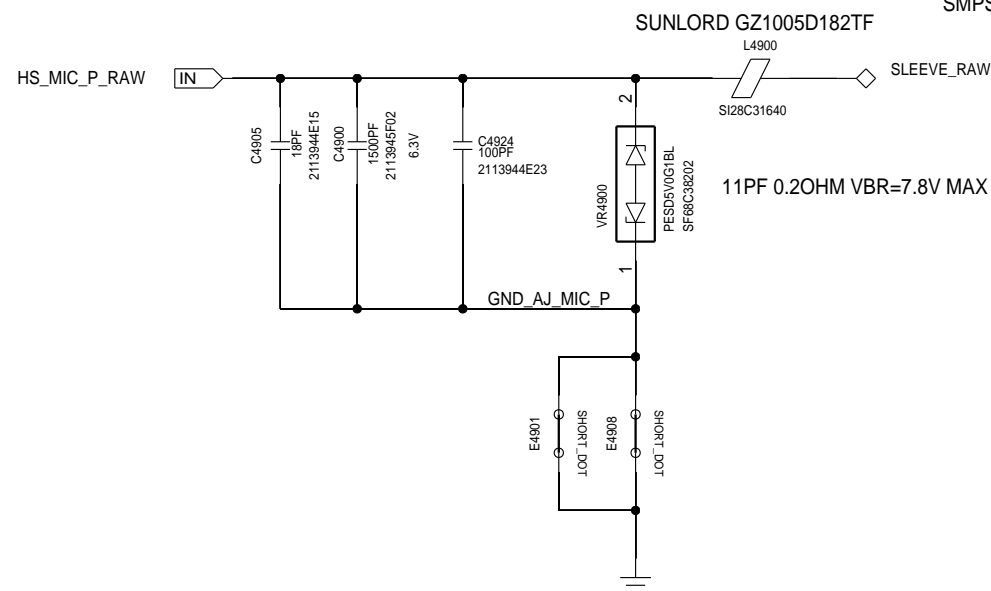
ADD STITCHING VIAS ALONGSIDE IF POSSIBLE



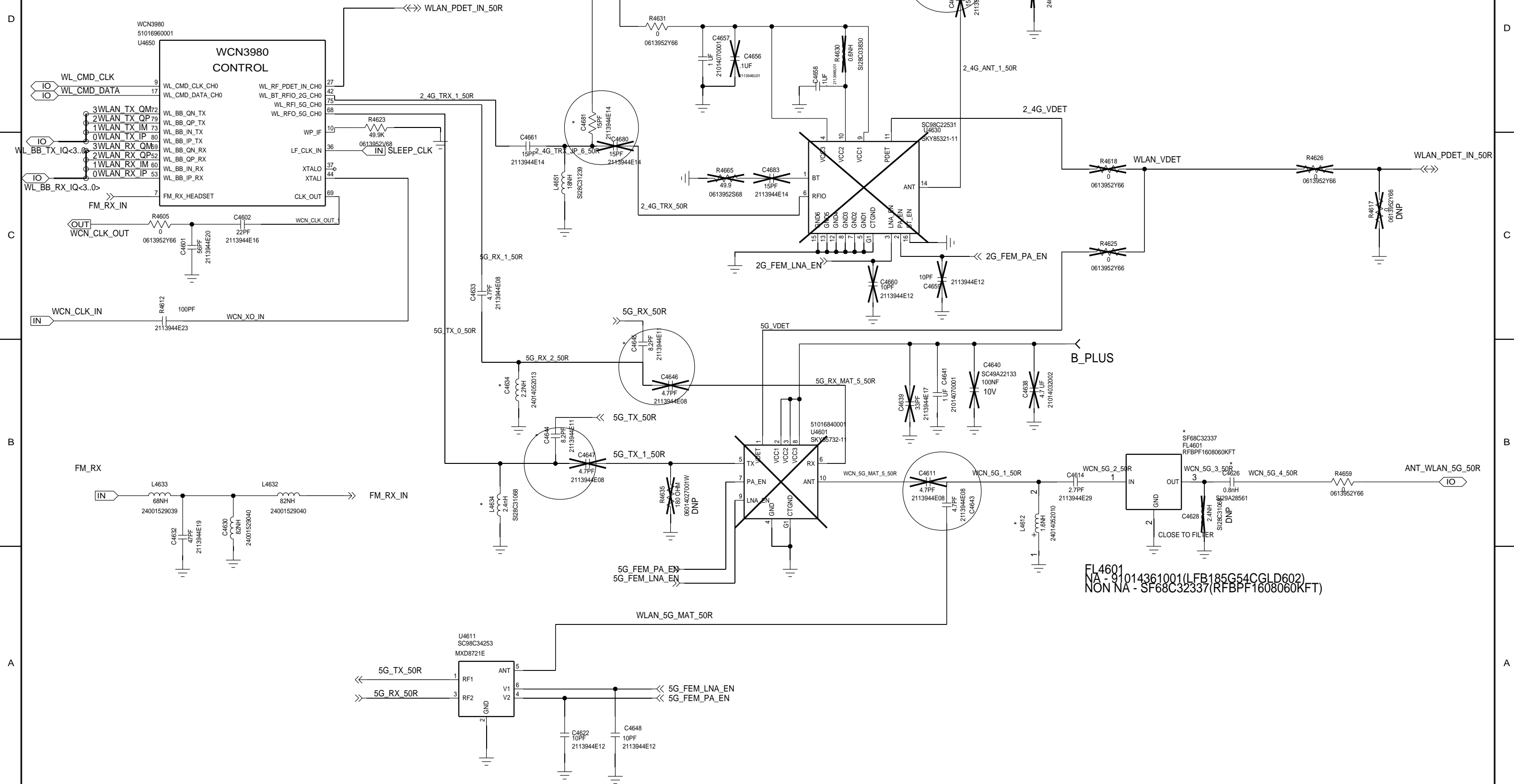
## AFE: 3.5MM HEADSET

REF:4900-4949

GND SHIELD,  
TRY TO AWAY FROM RFPA, CHARGER,  
SMPS,AUDIO SMART PA,DDR



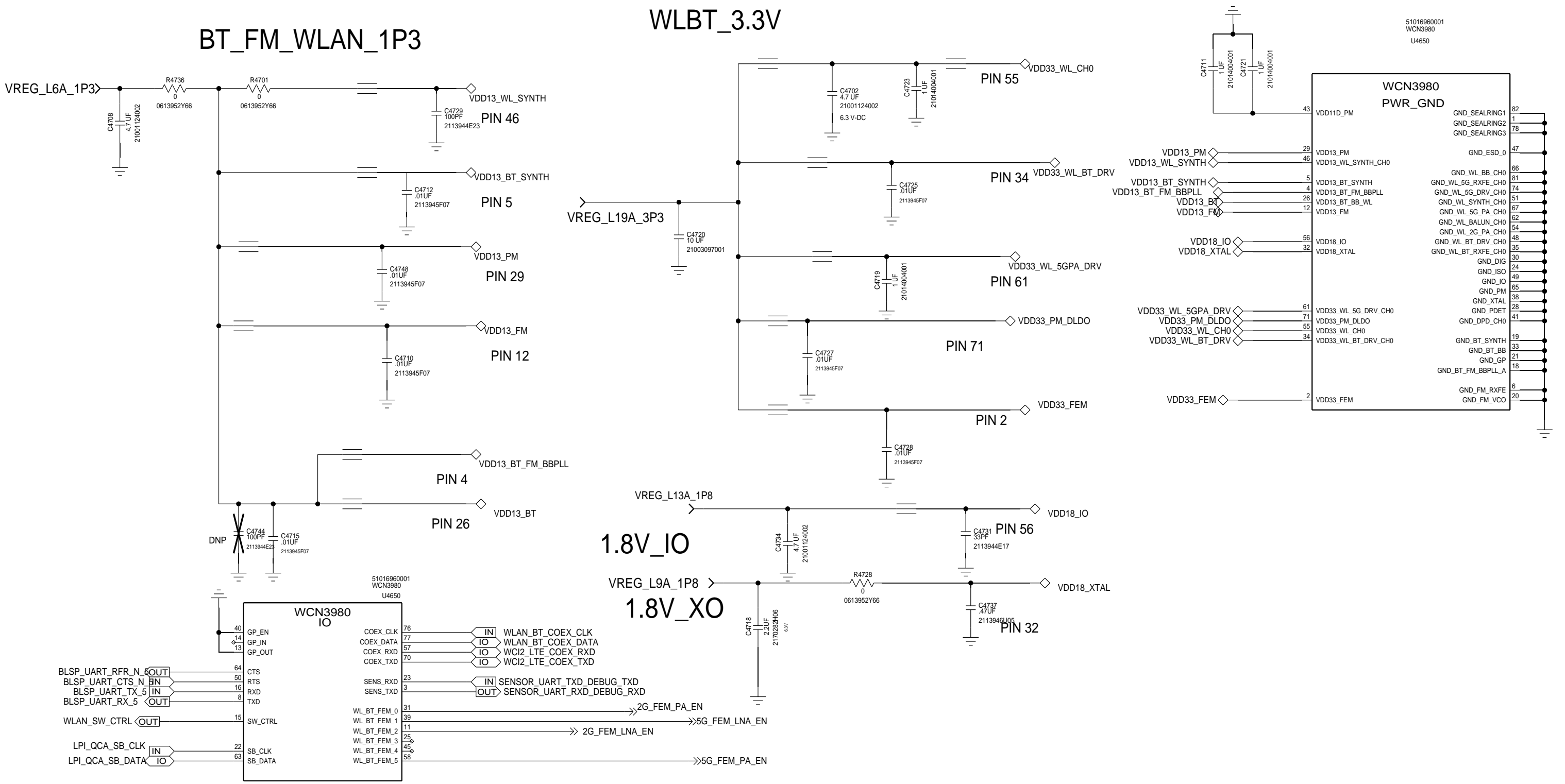
WCN3980 RF/CTRL  
REF 4600-4699



BASED ON 80-WL022-41 REV. A

# WCN3980 POWER AND GND,CONTROL

REF 4700-4799



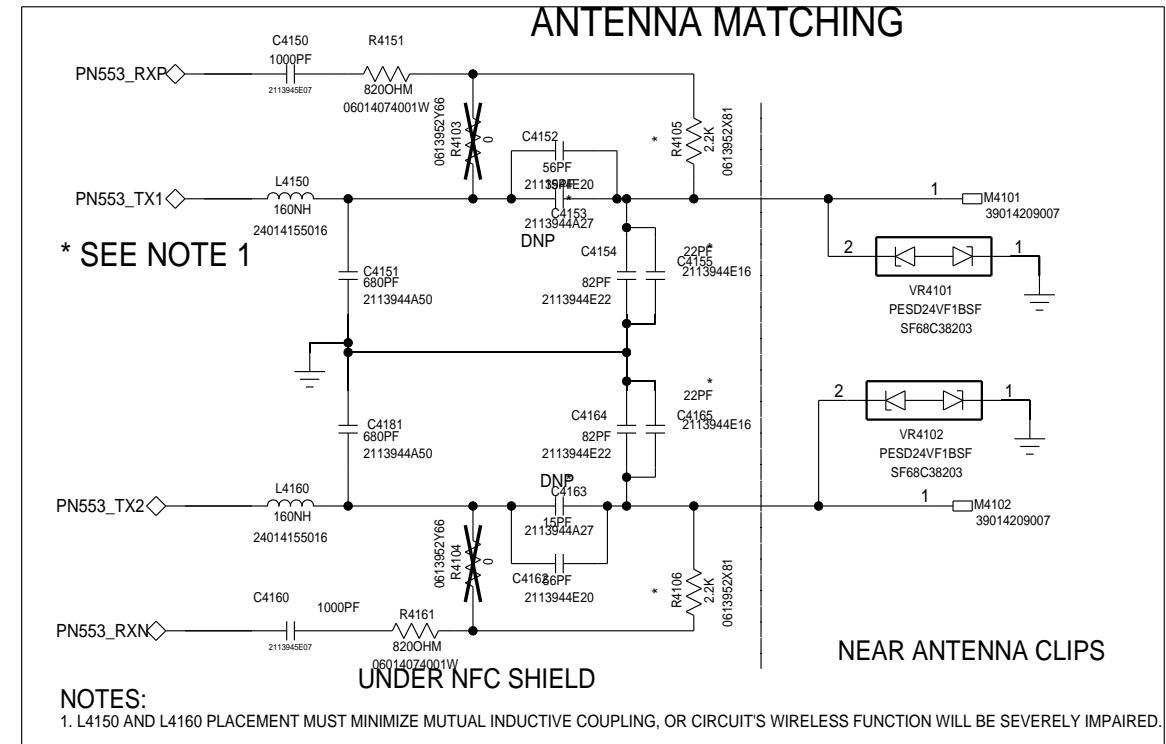
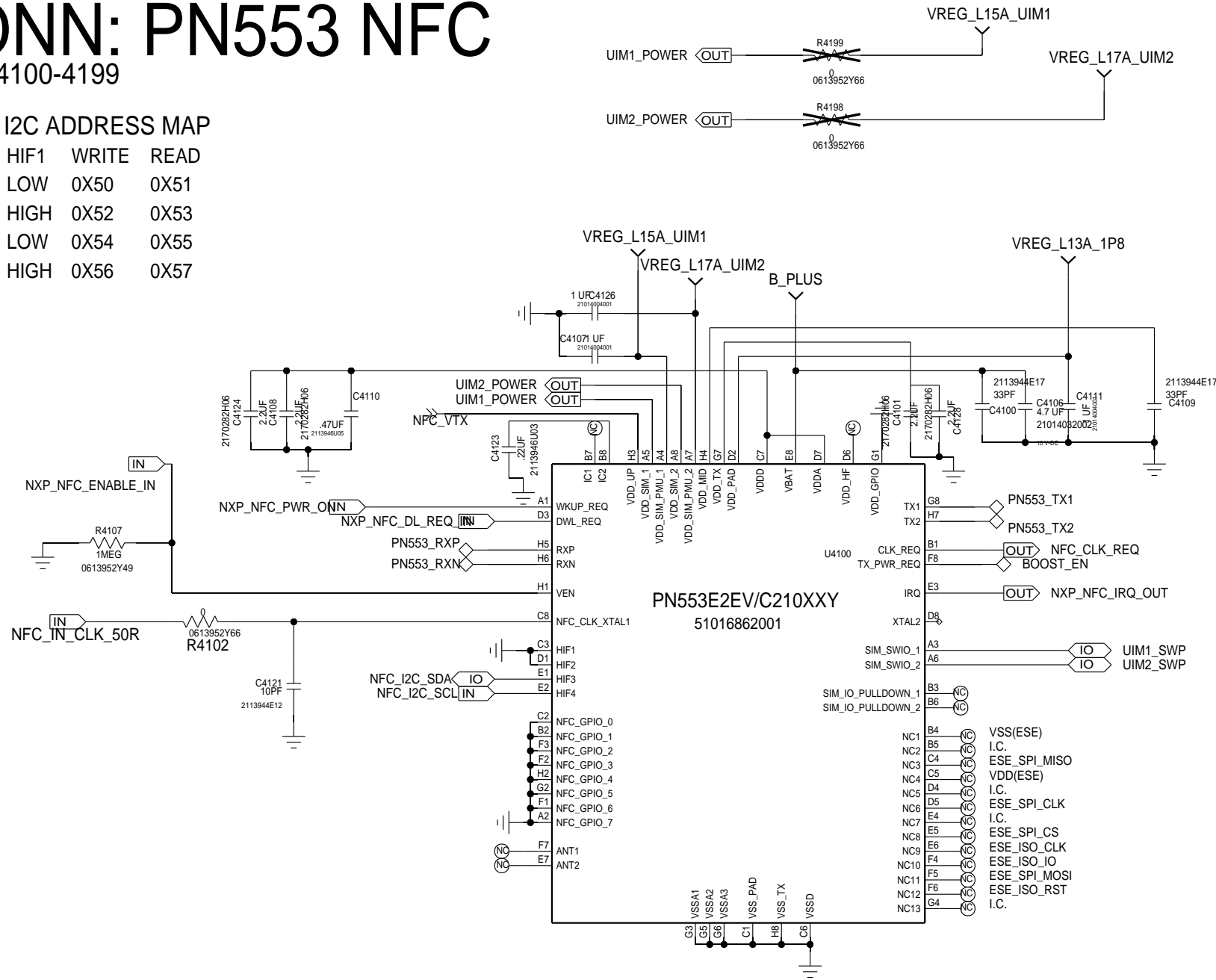
BASED ON 80-WL022-41 REV. A

# CONN: PN553 NFC

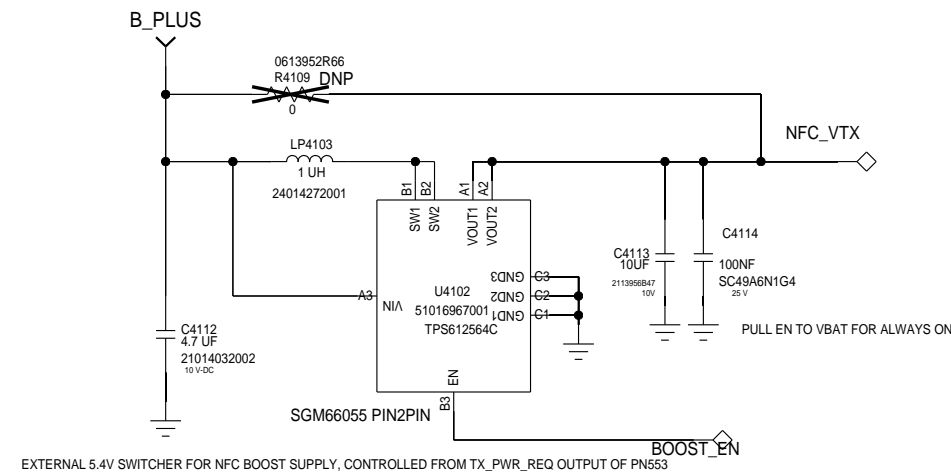
REF: 4100-4199

## PN553 I2C ADDRESS MAP

HIF2	HIF1	WRITE	READ
LOW	LOW	0X50	0X51
LOW	HIGH	0X52	0X53
HIGH	LOW	0X54	0X55
HIGH	HIGH	0X56	0X57



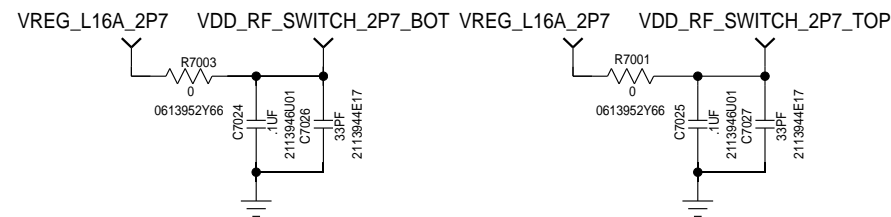
NOTES:  
1. L4150 AND L4160 PLACEMENT MUST MINIMIZE MUTUAL INDUCTIVE COUPLING, OR CIRCUIT'S WIRELESS FUNCTION WILL BE SEVERELY IMPAIRED.



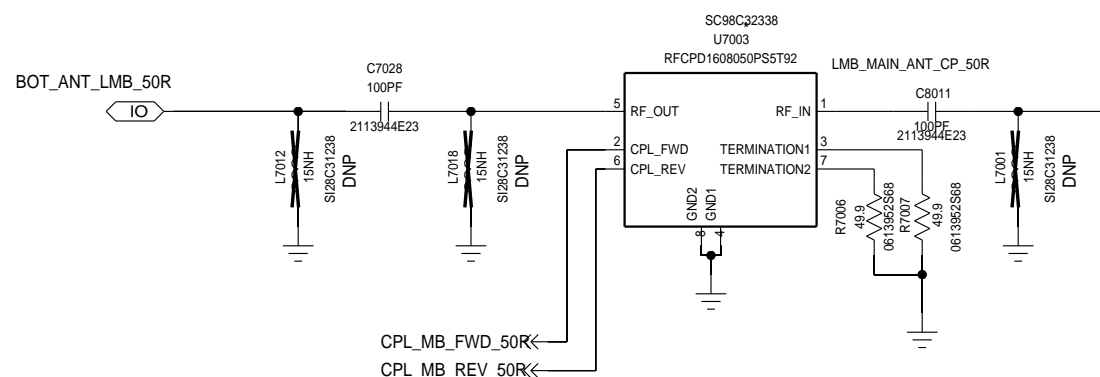
# RFFE:TRX ASM

## REF 7000-7099

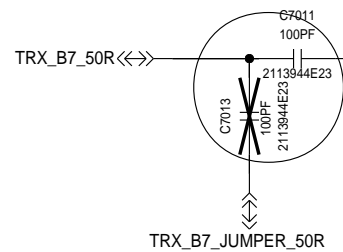
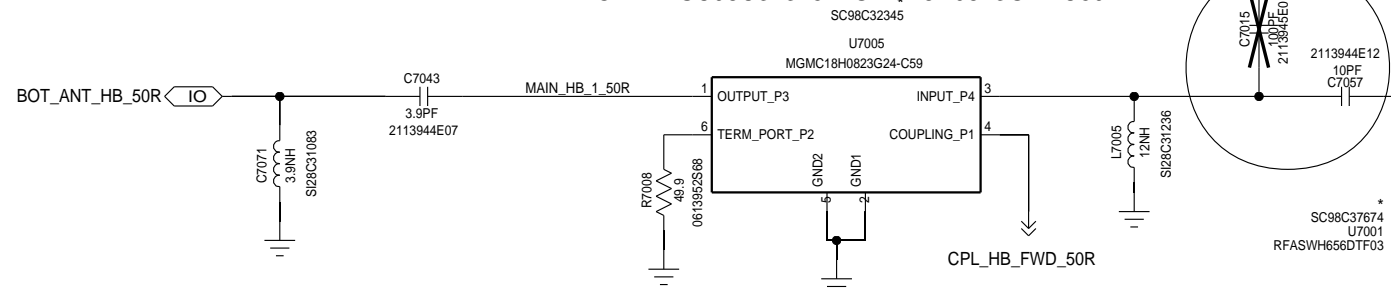
VDD\_SWITCH\_2P7 FOR RF SWITCH PWR SUPPLY



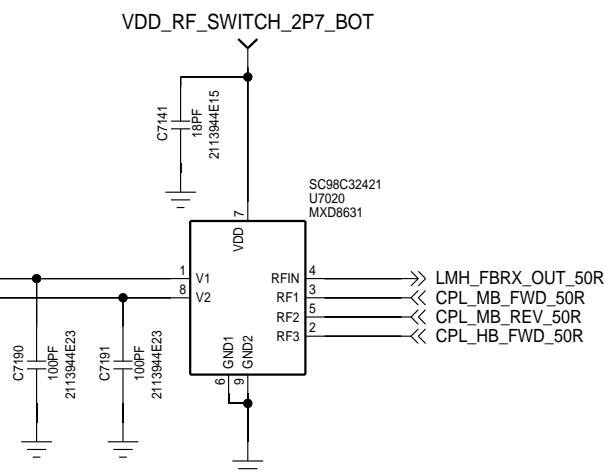
U7003  
NA USE SC98C21210 LDJ18829M28DG014  
NON NA SC98C32338 RFPCPD1608050PS5T92



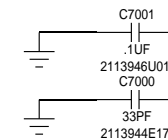
U7005  
NA USE 58014033001 LDJ18829M25AG006  
NON NA SC98C32345 MGMC18H0823G24-C59



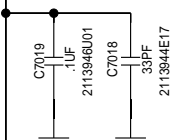
GRFC1\_GPIO93  
CTL1\_CPL\_FBRX  
CTL2\_CPL\_FBRX  
GRFC6\_GPIO98



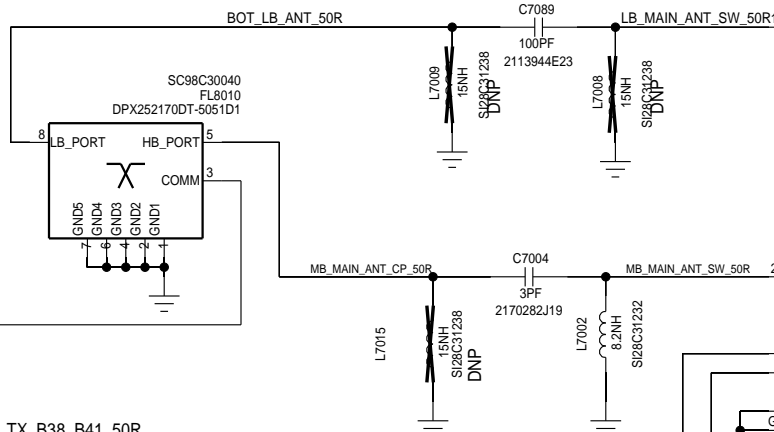
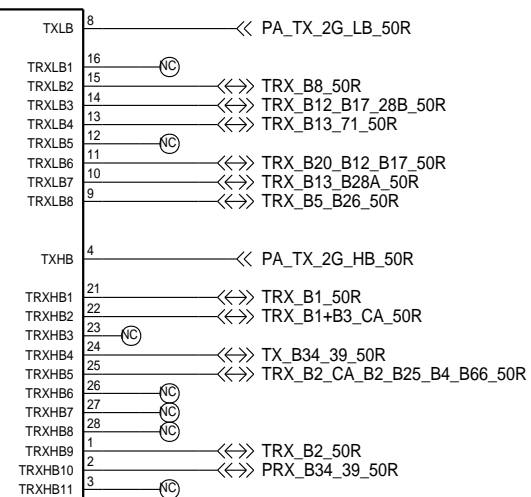
VREG\_L13A\_1P8\_RF



VDD\_RF\_SWITCH\_2P7\_BOT



SC98C30038  
LMSP5FQQ-J06  
U7000



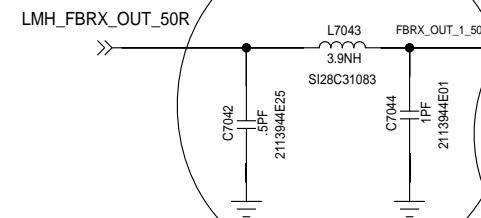
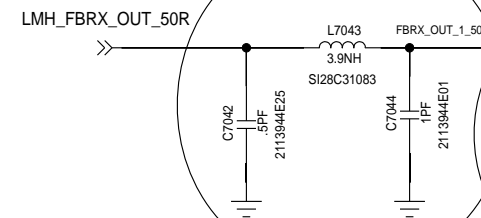
RFFE4\_CLK  
RFFE4\_DATA

U7001 BRAZIL+LAVERZION DNP  
TMO USE 5C99A6MWY7 RF1656TR13  
NON TMO SC98C37674 RFASWH656DTF03

TX\_B38\_B41\_50R  
PRX\_B40\_50R  
PRX\_B38\_B41\_50R

VDD\_RF\_SWITCH\_2P7\_BOT

VREG\_L13A\_1P8\_RF



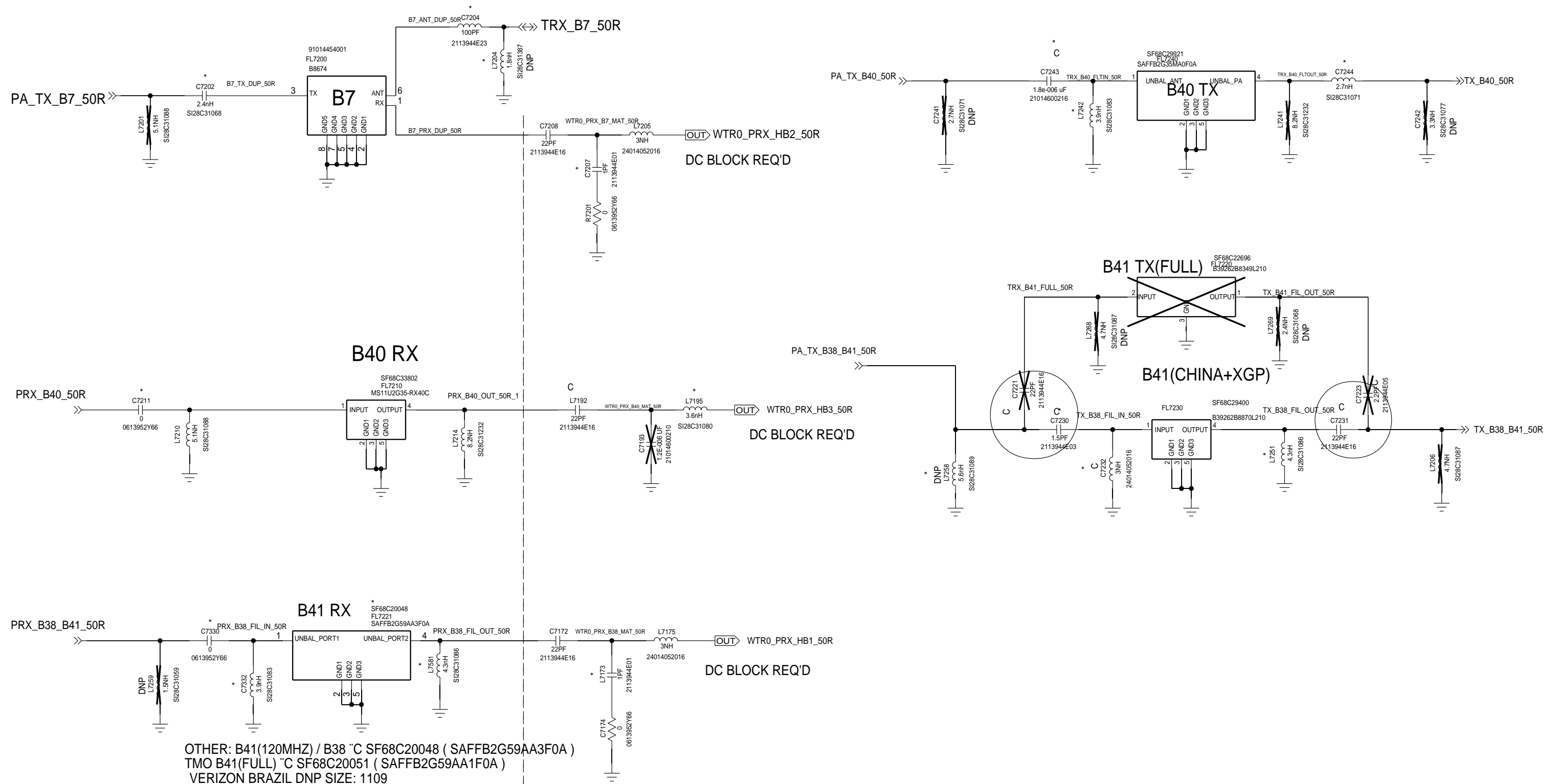
WTR\_FBRX\_P\_50R





# RFFE: HB\_TRX\_B7,38,40,41

## REF 7200-7299



OTHER: B41(120MHZ) / B38 "C SF68C20048 ( SAFFB2G59AA3F0A )  
TMO B41(FULL) "C SF68C20051 ( SAFFB2G59AA1F0A )  
VERIZON BRAZIL DNP SIZE: 1109

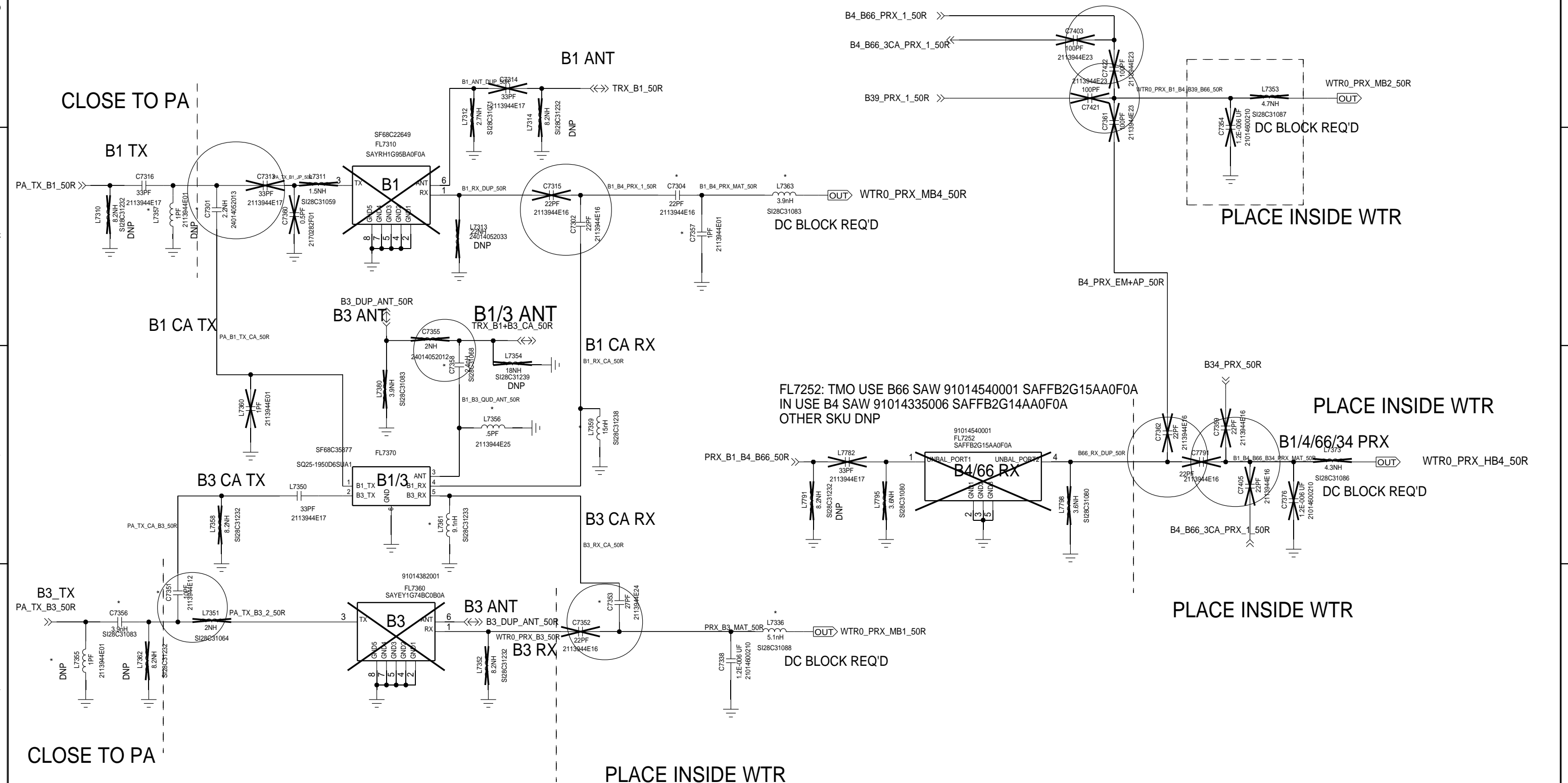
PLACE INSIDE WTR

D



# RFFE: MB\_TRX\_B1\_3\_B1/3 QUAD\_G1800

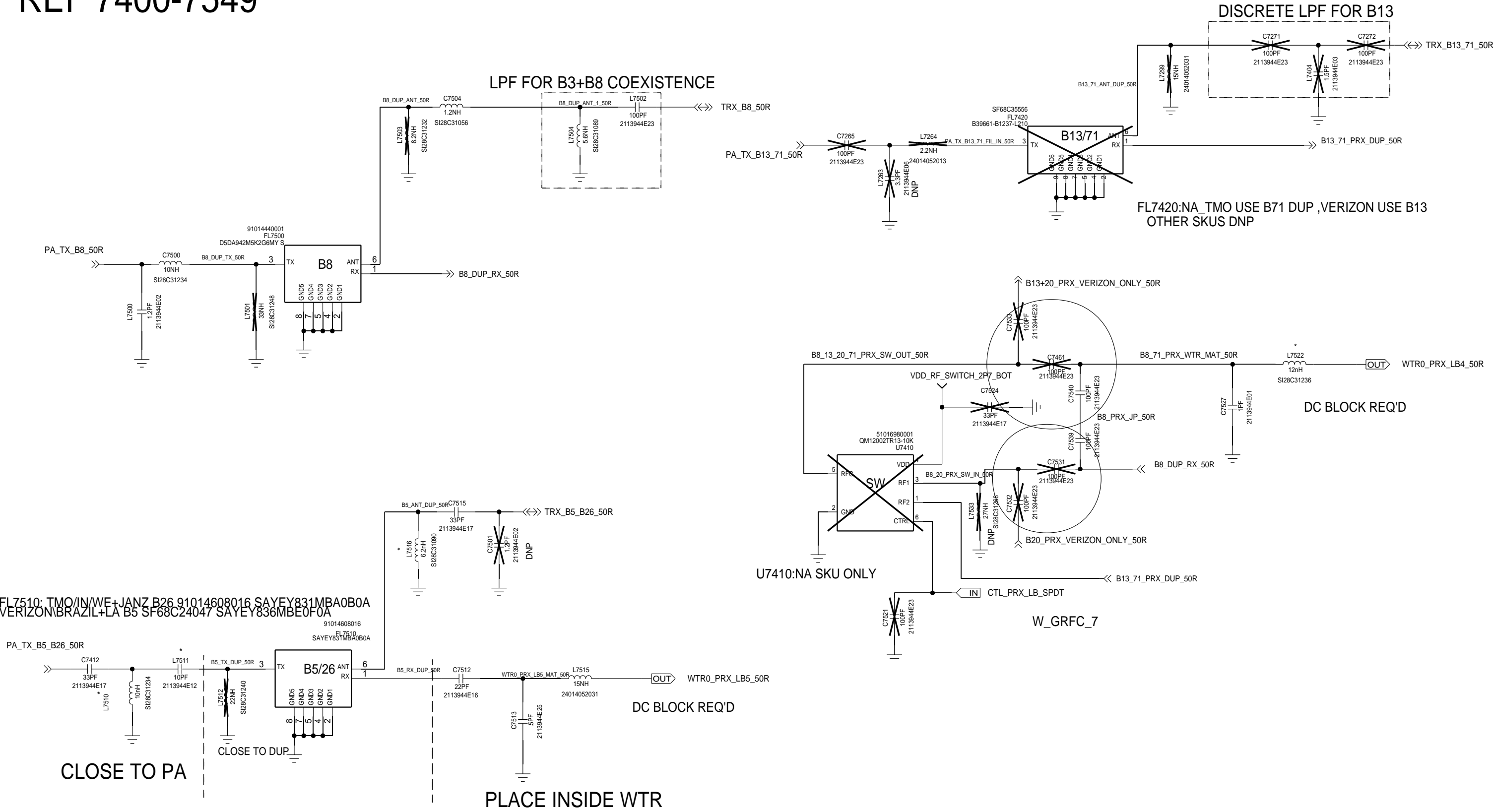
## REF 7350-7399



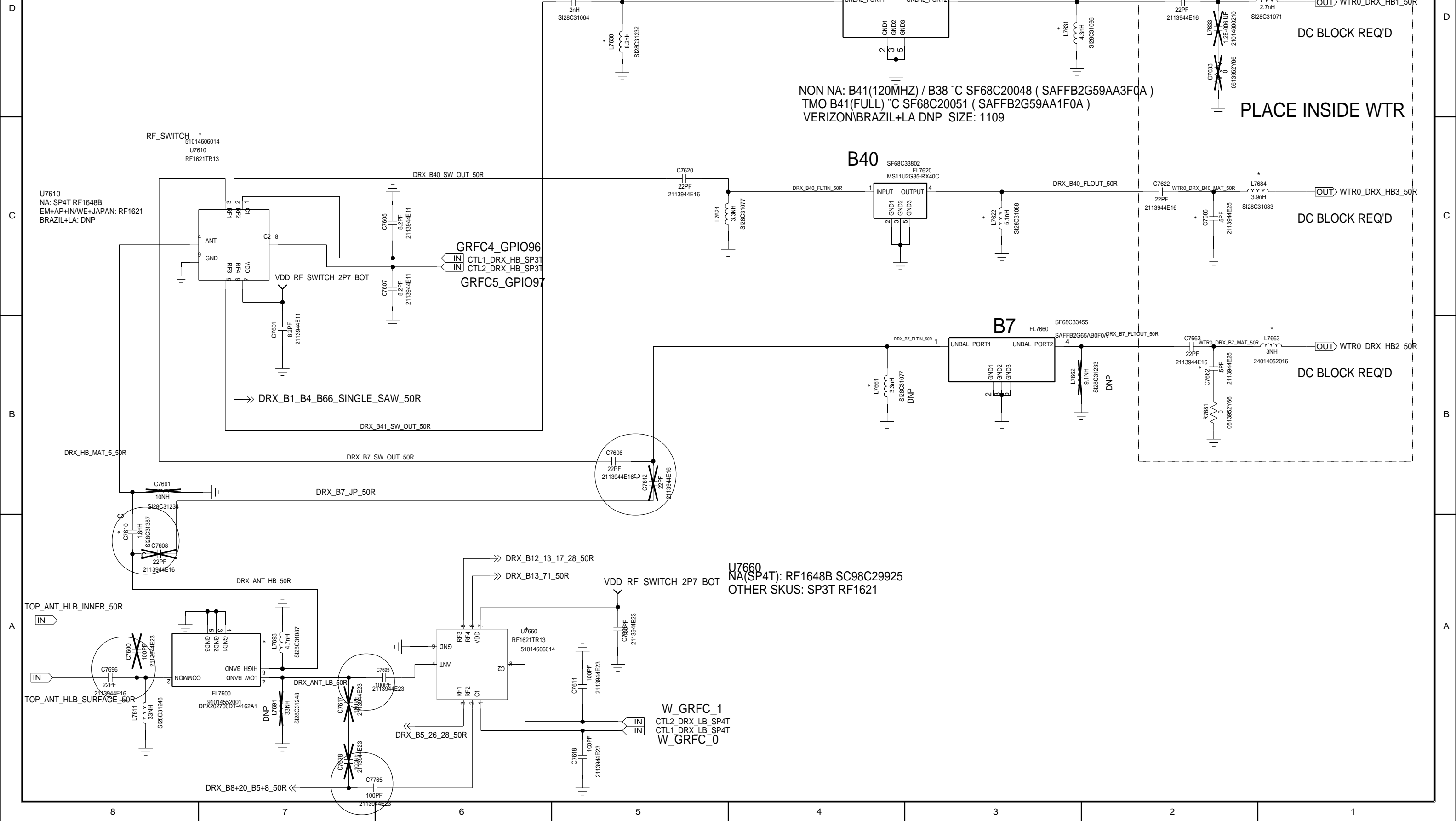
REF 7400-7499

PA\_TX\_B12\_B17\_28B\_50R >> [33PF L7471] [2113944F17] [B20 B28B\_TX\_DUP\_50R] [C7470 15NH] [24014052031] [3] [TX] [B12/17/28B] [ANT] [6] [1] [L7473] [7.50H] [S128C31231]

RFFE: LB\_TRX\_B5,8,26,71,G850/900  
REF 7400-7549



REF 7600-7699



RFFE: MB\_DRX\_B1,2,3,4,25,39,66

REF 7700-7749 B34/39 RX

FL7715:TMO\VERIZON B25+B66 91014518001 SAWFD1G96AC2F0A  
PRC B34+B39 91014335014 SAWFD1G90KA0F0A OTHER DNP

TMO\VERIZON\PRC ONLY

FL7710:VERZION DNP

TMO\BRAZIL+LA USE B3 FLITER 91014308001 SAFFB1G84AB0F0A  
OTHER SKUS USE B2 FLITER 91014335005 SAFFB1G96AB0F0A

DRX\_CA\_B1/B3/B2/B4/B25/B66\_50R >>

FL7700 :TMO USE B34+B39 DUAL SAW 91014335014 SAWFD1G90KA0F0A  
FL7700 :BRAZIL+LA USE B25+B66 DUAL SAW 91014518001 SAWFD1G96AC2F0A  
FL7700 :OTHER SKUS USE B1+B3 DUAL SAW 91014424001 SAWFD1G84AA0F0A

B25+B66 OR B1+B3 OR B34+B39

B66/B4 OR B34

B25/B2 OR B39

J7701 NA - 09014068001W(MM8030-2610RJ3)  
OTHER - SCA8C29399(818011998)

U7712 TMO/VERIZON/PRC(SP3T): RF1621 51014606014  
NON NA(SPDT): RF1621A 51016674001

GRFC2\_GPIO94

CTL1\_DRX\_MB\_SP3T

GRFC3\_GPIO95

>> DRX\_CA\_B1/B3/B2/B4/B25/B66\_50R  
>> DRX\_B34\_B39\_50R  
>> DRX\_B2/B3/BC1\_50R

WTR0\_DRX\_B34\_FIL\_OUT  
B1/4/66 OR B34

WTR0\_DRX\_B39\_FIL\_OUT

B2/25 OR B39

DRX\_B3/B2\_FLOUT\_50R

DRX\_B1\_B4\_B66\_SINGLE\_SAW\_FIL\_IN\_50R

DRX\_B1\_B4\_B66\_SINGLE\_SAW\_FIL\_OUT\_50R

DRX\_B66/4/1\_FLOUT\_50R

DRX\_B25/2/3\_FLOUT\_50R

WTR0\_DRX\_B39\_MAT\_5\_50R

WTR0\_DRX\_MB2\_50R

DC BLOCK REQ'D

DC BLOCK REQ'D

WTR0\_DRX\_MB3\_50R

DC BLOCK REQ'D

DC BLOCK REQ'D

WTR0\_DRX\_MB4\_50R

DC BLOCK REQ'D

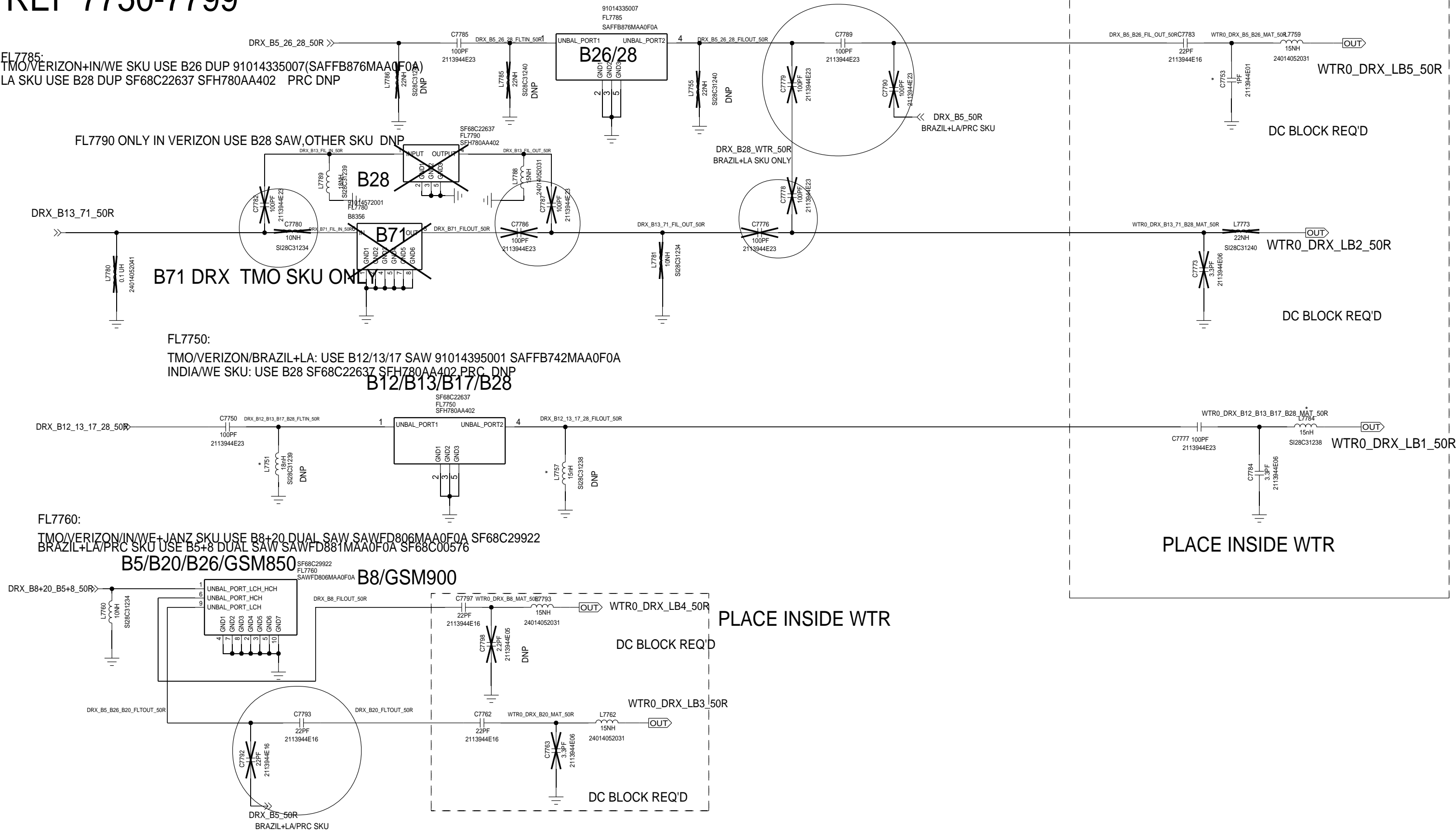
WTR0\_DRX\_MB1\_50R

DC BLOCK REQ'D

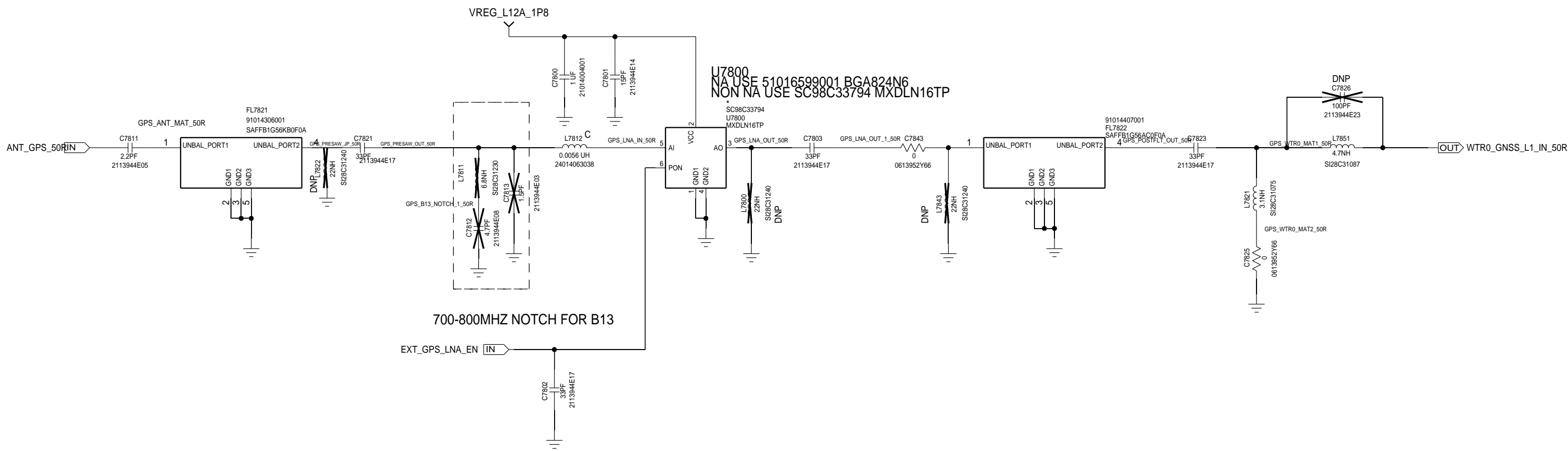
PLACE INSIDE WTR



REF 7750-7799



RFFE: GPS  
REF7800-7899



## D



B

A

D |

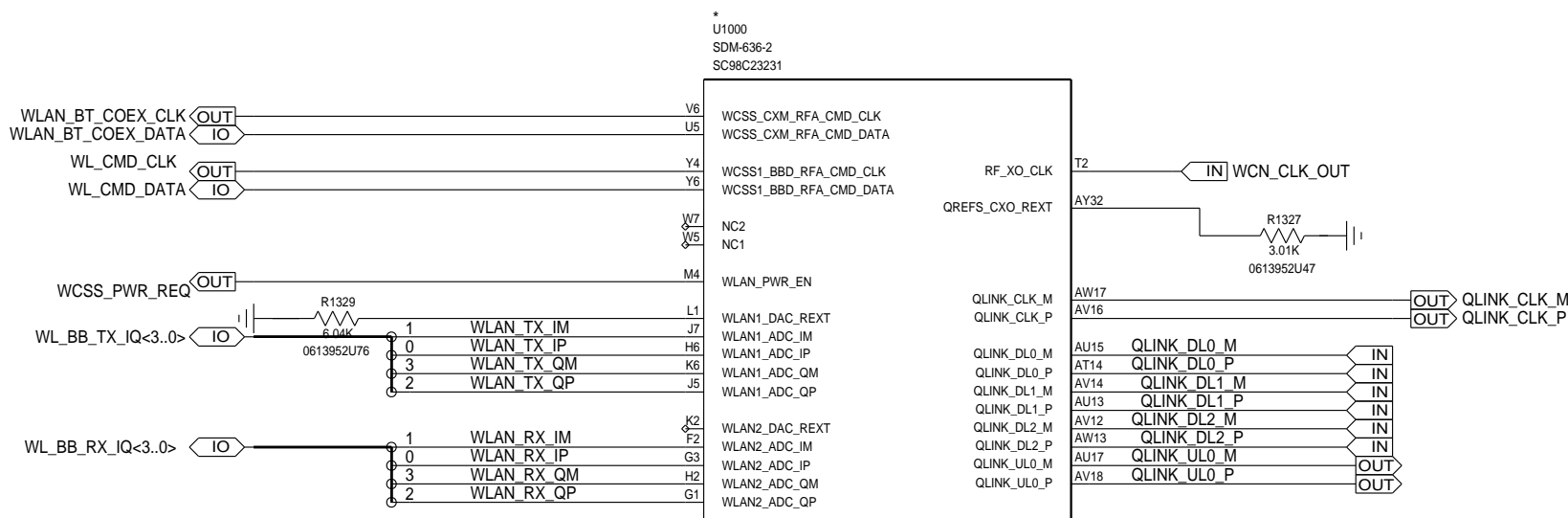
C

B |

A |

# MSM: RF INTERFACE

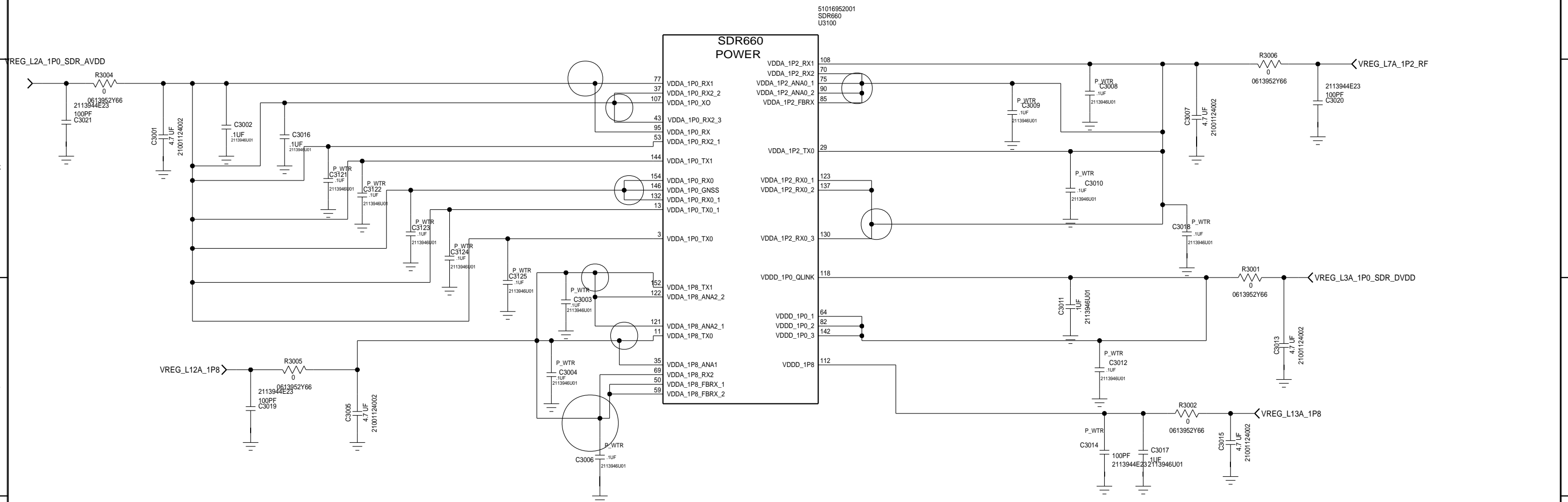
## REF 1200-1299



NEED TO APPLY LC FILTER FOR REDUCE B40 RX SPURS(QC 80-NP237-56)  
LC FILTER COULD BE PLACED ANYWHERE BETWEEN WTR BBRX\_IQ AND MSM BBRX\_IQ PINS  
BUT THE GND FOR SHUNT C SHOULD NOT BE SHARED WITH EITHER WTR OR MSM GND ON ANY LAYER.  
A DIRECT VIA FROM SHUNT C TO MAIN GND IS RECOMMENDED

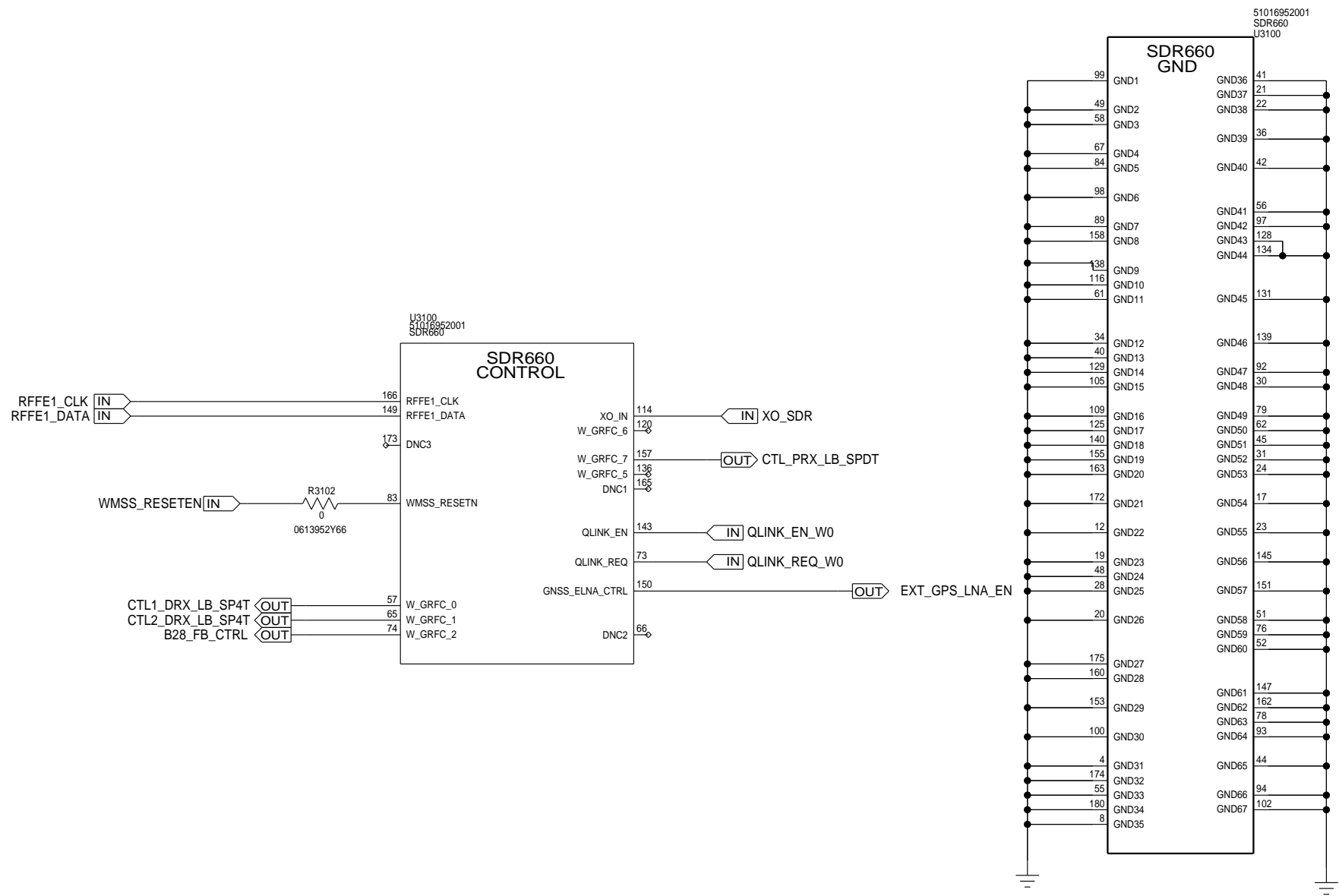
# SDR660: POWER

REF 3000-3099



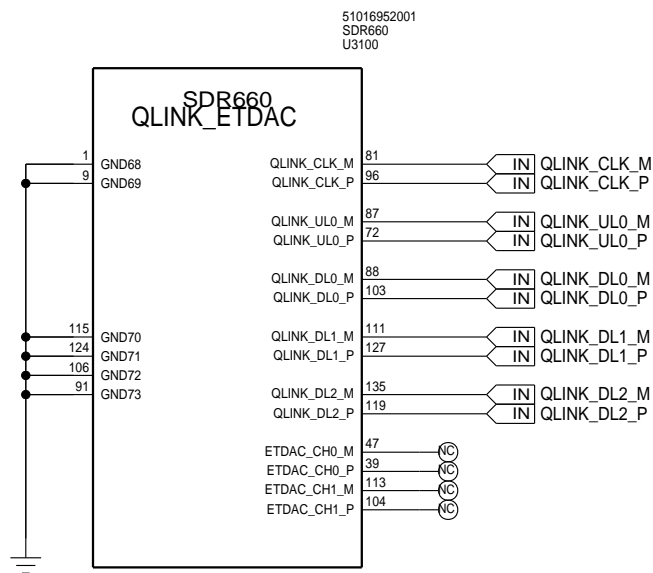
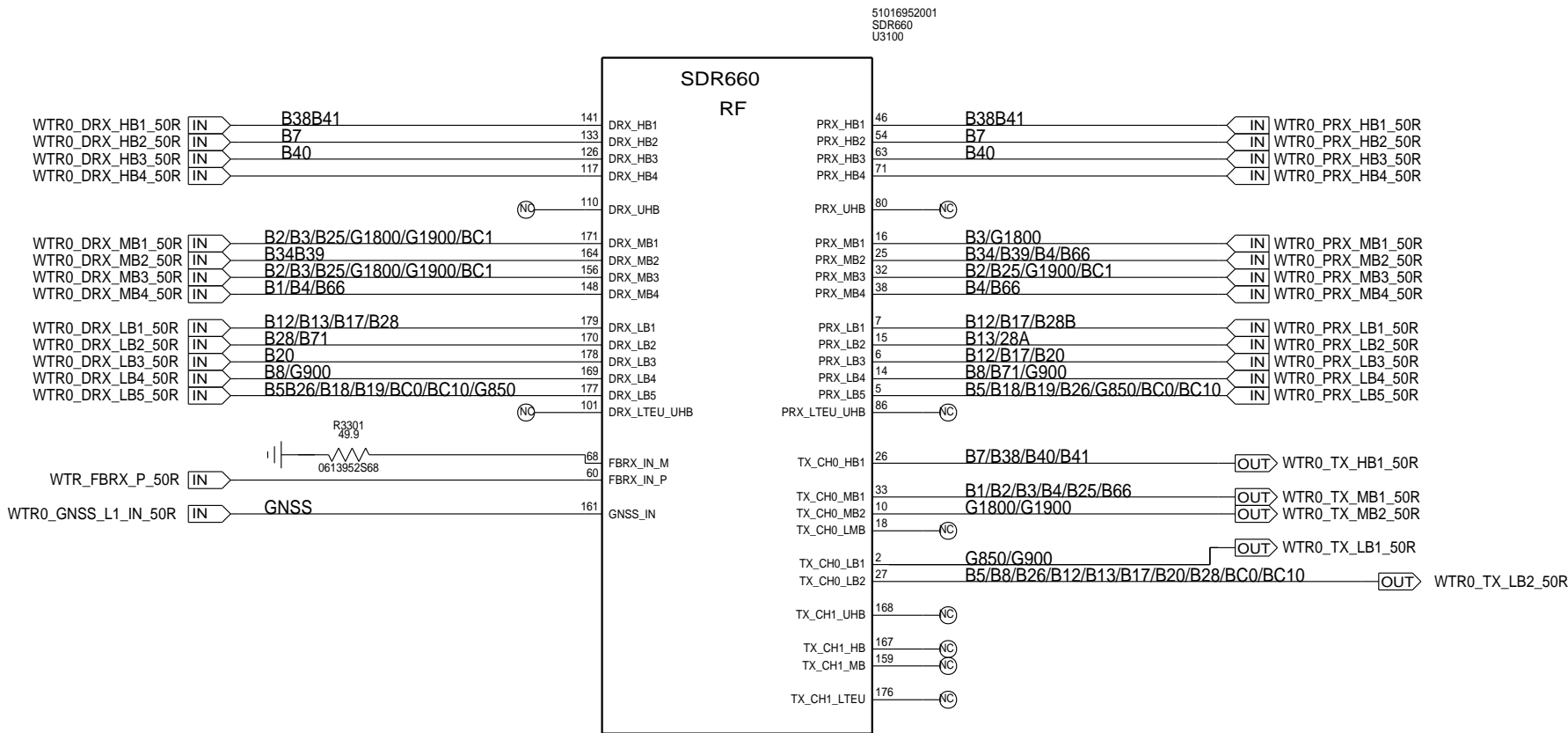
# SDR660: TX/PWR/GROUND

REF 3100-3199



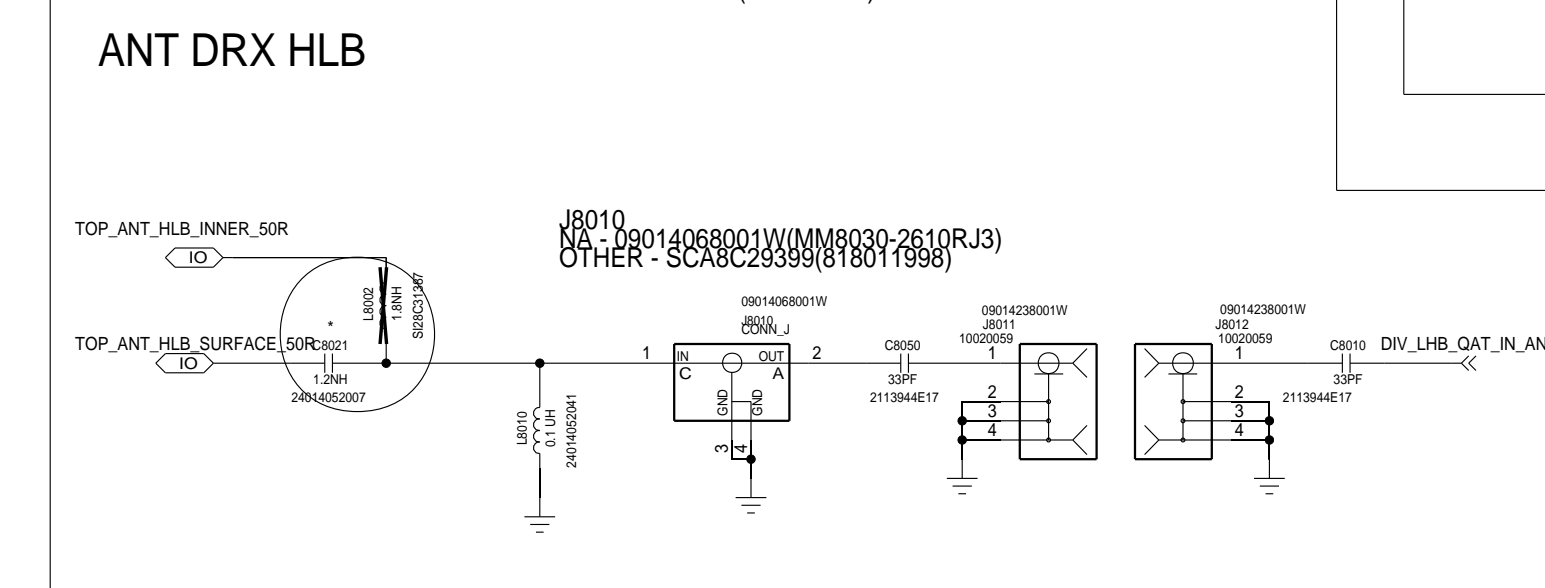
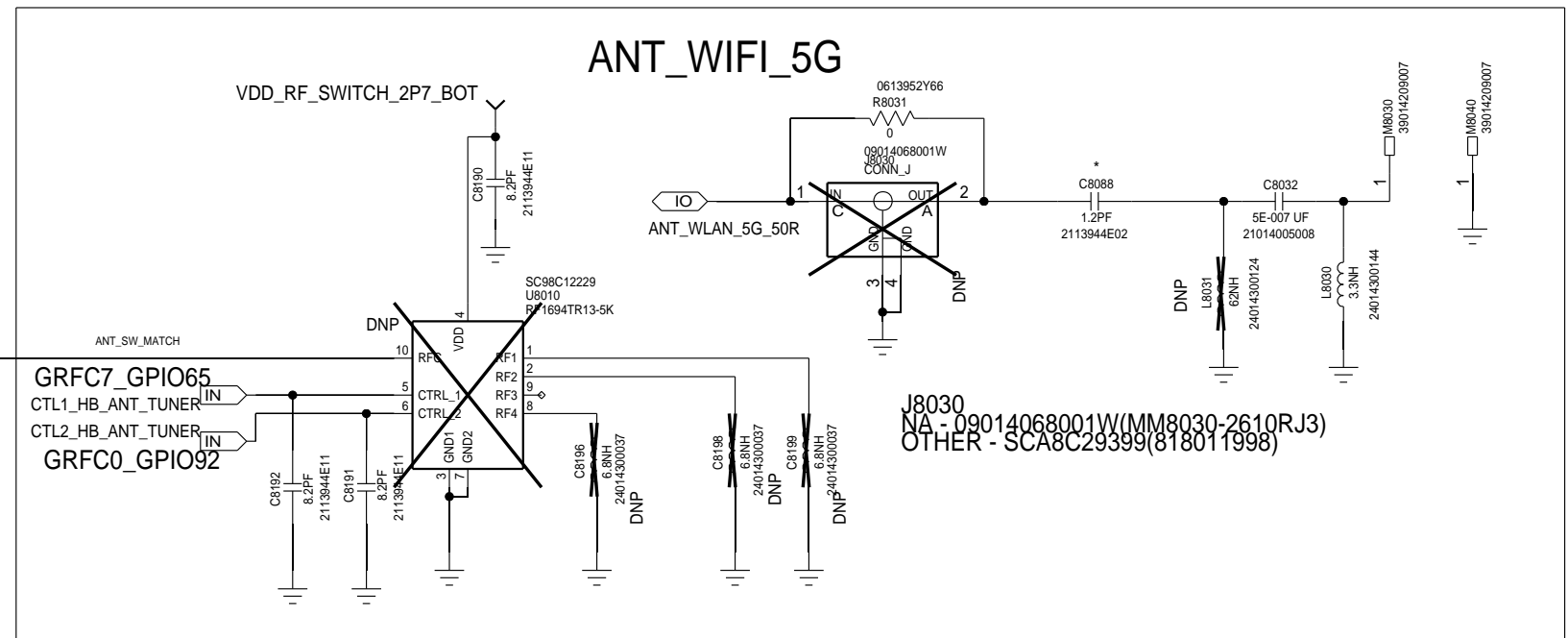
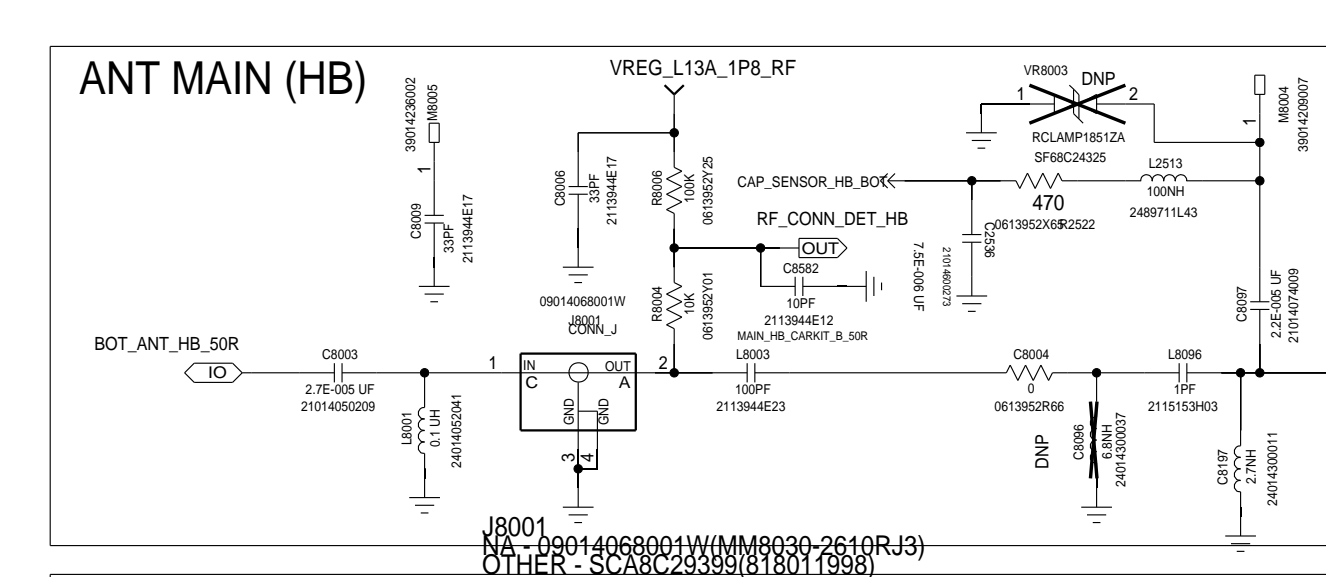
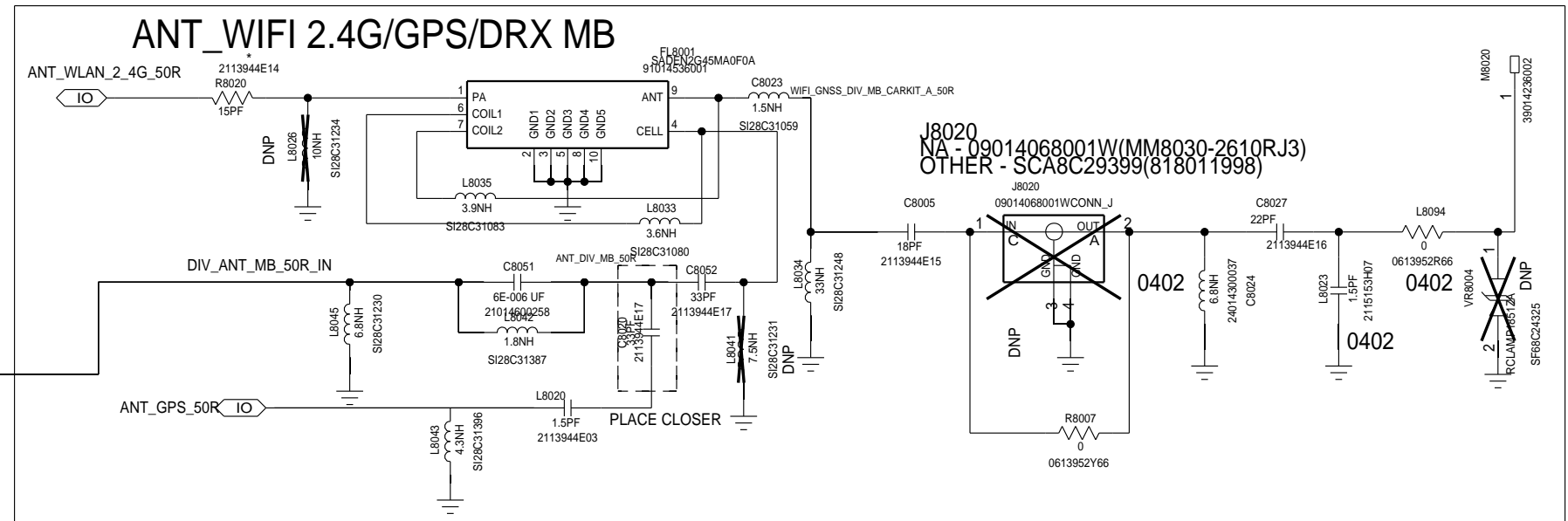
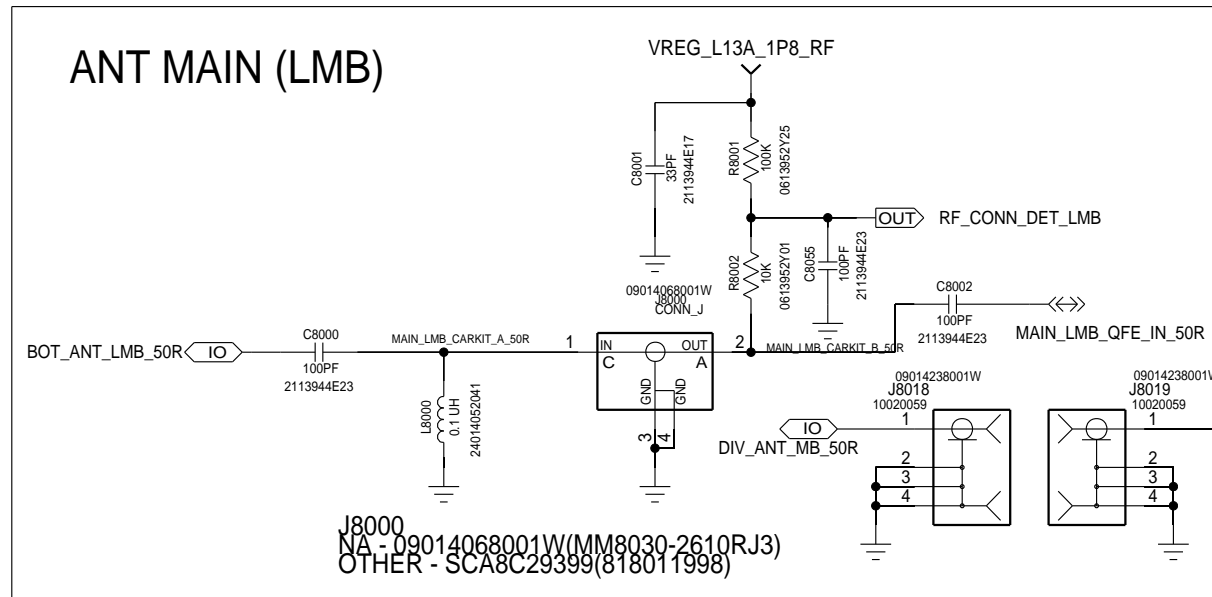
# SDR660: PRX/DRX/GPS

REF 3200-3299



8000:antennas

## REF 8000-8049





## D



**RFFE: CAP SENSOR**  
**REF 2510-2530**

The diagram illustrates the RFFE CAP SENSOR circuit, showing the connection of the CAP SENSOR (SC98C30422) to various input signals and power sources. The sensor is connected to the following signals:

- CAP\_SENSOR\_LMHB\_TOP\_REF
- CAP\_SENSOR\_LMB\_BOT\_REF
- CAP\_SENSOR\_HB\_BOT
- CAP\_SENSOR\_LMB\_BOT
- CAP\_SENSOR\_LMH\_TOP

The sensor's pins are connected as follows:

- CSIO4: Connected to CAP\_SENSOR\_LMHB\_TOP\_REF
- CSIO3: Connected to CAP\_SENSOR\_LMB\_BOT\_REF
- CSIO2: Connected to CAP\_SENSOR\_HB\_BOT
- CSIO1: Connected to CAP\_SENSOR\_LMB\_BOT
- CSIO0: Connected to CAP\_SENSOR\_LMH\_TOP
- VDD: Connected to VREG\_L8B\_3P3
- SCL: Connected to BLSP2\_I2C\_SCL
- SDA: Connected to BLSP2\_I2C\_SDA
- NIRQ: Connected to SAR\_SENSOR\_INT\_N
- GND: Connected to ground

The circuit includes several resistors and capacitors:

- R2515: 100 ohms, connected to CAP\_SENSOR\_HB\_BOT
- R2524: 100 ohms, connected to CAP\_SENSOR\_LMB\_BOT
- R2516: 100 ohms, connected to CAP\_SENSOR\_LMH\_TOP
- R2511: 560 ohms, connected to CAP\_SENSOR\_LMB\_BOT
- R2512: 560 ohms, connected to CAP\_SENSOR\_LMB\_BOT
- R2510: 10K ohms, connected to VREG\_L13A\_1P8
- R2517: 10K ohms, connected to VREG\_L8B\_3P3
- C2529: 33pF, connected to VREG\_L8B\_3P3
- C2530: 33pF, connected to VREG\_L8B\_3P3
- C2531: 33pF, connected to VREG\_L8B\_3P3
- C2532: 1 uF, connected to VREG\_L8B\_3P3

The sensor is powered by VREG\_L8B\_3P3 and VREG\_L13A\_1P8. The output signals are BLSP2\_I2C\_SCL, BLSP2\_I2C\_SDA, and SAR\_SENSOR\_INT\_N.