



## Revision History

REV	DESCRIPTION
A	EVT1.2 release. The detailed change refer to item 29 to 34 on page <Change list>.

## Board Change History

SCHEMATIC MCN	HW VERSION	REVISION	DESCRIPTION OF CHANGE
LD20-PF494-11	EVT1.0	A	Preliminary release for early reference.
		B	The detailed change refer to item 1 to 8 on page <Change list>.
		C	The detailed change refer to item 9 to 18 on page <Change list>.
LD20-PF494-12	EVT1.1	A	The detailed change refer to item 19 to 26 on page <Change list>.
		B	The detailed change refer to item 27 to 28 on page <Change list>.
LD20-PF494-13	EVT1.2	A	The detailed change refer to item 29 to 34 on page <Change list>.



SDM450 GPIO Configuration For QRD450+PMI632			
GPIO_0		GPIO_41	DTV_EN
GPIO_1		GPIO_42	ACC_GYRO_INT1
GPIO_2	FD_I2C_SDA	GPIO_43	ALSP_INT
GPIO_3	FD_I2C_SCL	GPIO_44	
GPIO_4	UART_TX_2	GPIO_45	ACC_GYRO_INT2
GPIO_5	UART_RX_2	GPIO_46	
GPIO_6	SAR_SPA_I2C_SDA	GPIO_47	
GPIO_7	SAR_SPA_I2C_SCL	GPIO_48	FP_INT
GPIO_8	FP_RST_N	GPIO_49	UIM_BATT_ALARM
GPIO_9	CC_INT	GPIO_50	CAM_DVDD_EN
GPIO_10	TF_I2C_SDA	GPIO_51	UIM1_DATA
GPIO_11	TF_I2C_SCL	GPIO_52	UIM1_CLK
GPIO_12	FP_LDOEN_3V3	GPIO_53	UIM1_RESET
GPIO_13		GPIO_54	UIM1_PRESENT
GPIO_14	SENSOR_I2C_SDA	GPIO_55	UIM2_DATA
GPIO_15	SENSOR_I2C_SCL	GPIO_56	UIM2_CLK
GPIO_16	NFC_DISABLE	GPIO_57	UIM2_RESET
GPIO_17	NFC_IRQ	GPIO_58	UIM2_PRESENT
GPIO_18	NFC_LCD_I2C_SDA	GPIO_59	SPK/REC_SW_EN
GPIO_19	NFC_LCD_I2C_SCL	GPIO_60	
GPIO_20	SENSOR_SPI_MOSI	GPIO_61	LCD_RST
GPIO_21	SENSOR_SPI_MISO	GPIO_62	
GPIO_22	SENSOR_SPI_CS0_N	GPIO_63	SPKPA_INT
GPIO_23	SENSOR_SPI_CLK	GPIO_64	TF_RST
GPIO_24	LCD_TE	GPIO_65	TF_IN
GPIO_25	LCD_ID1	GPIO_66	LCD_ID0
GPIO_26	CAM_MCLK0	GPIO_67	CDC_FDM_RX0_COMP
GPIO_27	CAM_MCLK1	GPIO_68	CDC_FDM_RX1_COMP
GPIO_28		GPIO_69	CDC_FDM_CLK
GPIO_29	CAM_I2C_SDA0	GPIO_70	CDC_FDM_FSYNC
GPIO_30	CAM_I2C_SCL0	GPIO_71	CDC_FDM_TX
GPIO_31	CAM_I2C_SDA1	GPIO_72	CDC_FDM_RX0
GPIO_32	CAM_I2C_SCL1	GPIO_73	CDC_FDM_RX1
GPIO_33		GPIO_74	CDC_FDM_RX2
GPIO_34	FLASH_STROBE_NOW	GPIO_75	BT_SSB1
GPIO_35	CAM_AVDD_EN	GPIO_76	WL_CMD_DATA_2
GPIO_36	FP_LDOEN_1V8	GPIO_77	WL_CMD_DATA_1
GPIO_37	FORCED_USB_ROOT	GPIO_78	WL_CMD_DATA_0
GPIO_38	GPIO38_DTV_EINT	GPIO_79	WL_CMD_SET
GPIO_39	REAR_MCAM_PWDN	GPIO_80	WL_CMD_CLK
GPIO_40		GPIO_81	PM_SSB1
		GPIO_122	

GPIO_123		GPIO_134	BOARD_ID2
GPIO_124	RFFE5_CLK	GPIO_135	DTV_SPI_CLK
GPIO_125	RFFE5_DATA	GPIO_136	DTV_SPI_CS_N
GPIO_126		GPIO_137	DTV_SPI_MOSI
GPIO_127		GPIO_138	DTV_SPI_MISO
GPIO_128	SPK/REC_SW_IN	GPIO_139	USB_SS_SEL
GPIO_129		GPIO_140	
GPIO_130	FRONT_CAM_RST	GPIO_141	
GPIO_131	BOARD_ID1		
GPIO_132			
GPIO_133	SDCARD_DET_N		

PMI632 GPIO/MPP Configuration			
GPIO_1	TYPE_C_CON_THERMAL	GPIO_5	
GPIO_2		GPIO_6	HERCULA_FWM
GPIO_3	QUITE_THERMAL	GPIO_7	
GPIO_4	FLASH_STROBE_NOW	GPIO_8	

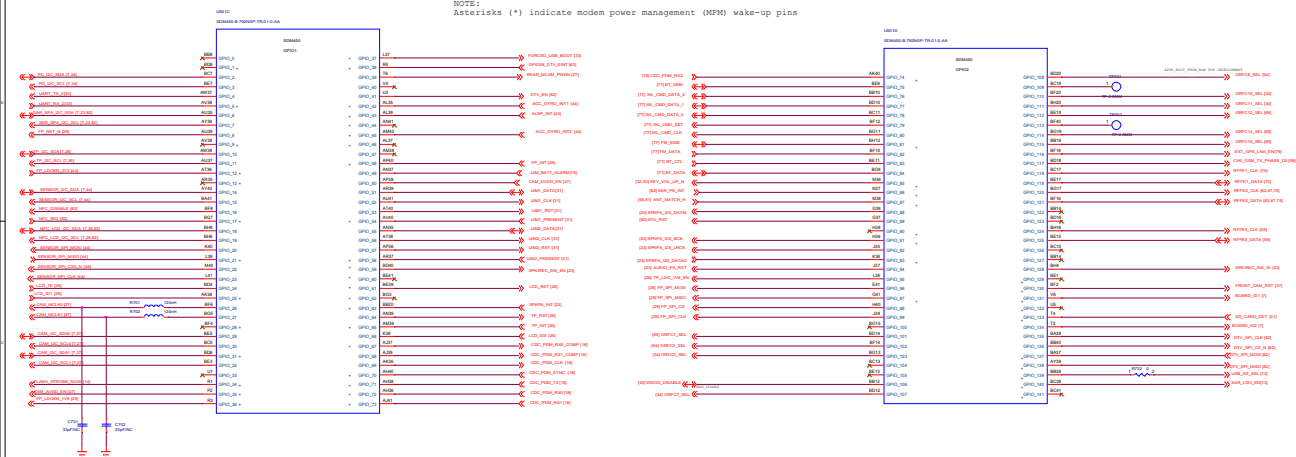
PM8953 GPIO/MPP Configuration			
GPIO_1		MPP_1	VDD_FX_BIAS_MPP_1
GPIO_2	NFC_CLK_REQ	MPP_2	CHG_THERM
GPIO_3	UIM_BATT_ALARM	MPP_3	VREF_DAC_MPP_3
GPIO_4	WLED_EN	MPP_4	MMN_THERM
GPIO_5			
GPIO_6	VIB_RST_N		
GPIO_7			
GPIO_8	LCD_BL_FWM		

GPIO TABLE



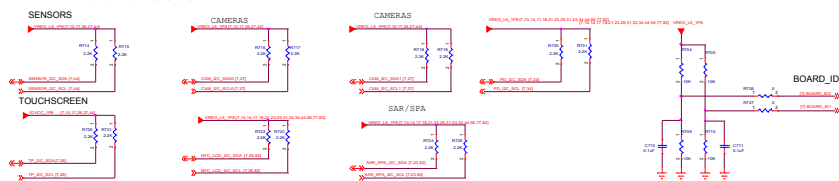


NOTE:  
Asterisks (\*) indicate modem power management (MPM) wake-up pins



Change pull up resistors is 2.2K

## I2C PULL-UP RESISTORS



NOTE:  
Ensure SW sets these GPIOs (Sensor, CTP and Camera I2C bus) to inout pull down when the peripherals are powered off to eliminate leakage.

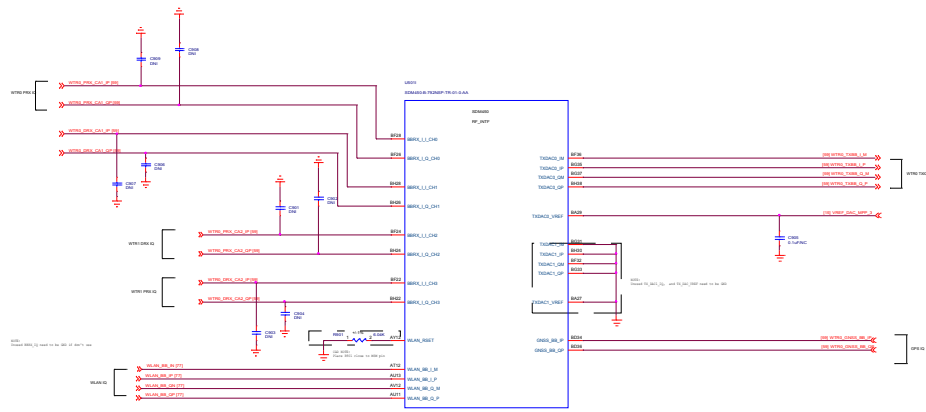
Q100-07	TABLE 100-100
Q100-08	WALL STUDS
Q100-09	JOIST END Plate top
Q100-10	JOIST JOISTS
Q100-11	JOIST JOISTS

BASE_00075(0-1)	BASE_00075
00010	0001 $\rightarrow$ 0002 $\rightarrow$ 0003.0
00011	0002 $\rightarrow$ 0003 $\rightarrow$ 0003.0
00012	0003 $\rightarrow$ 0004.0
00013	0002.0

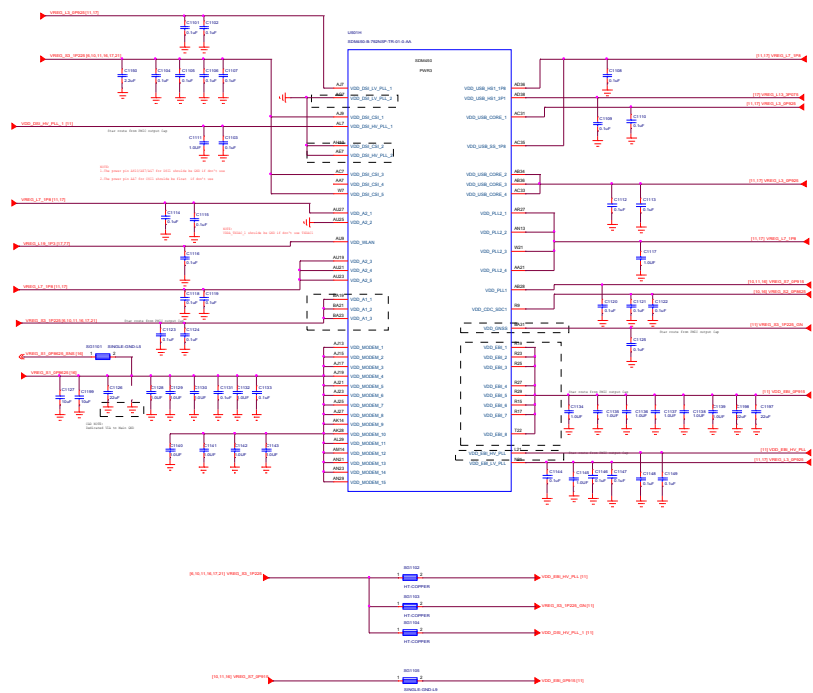
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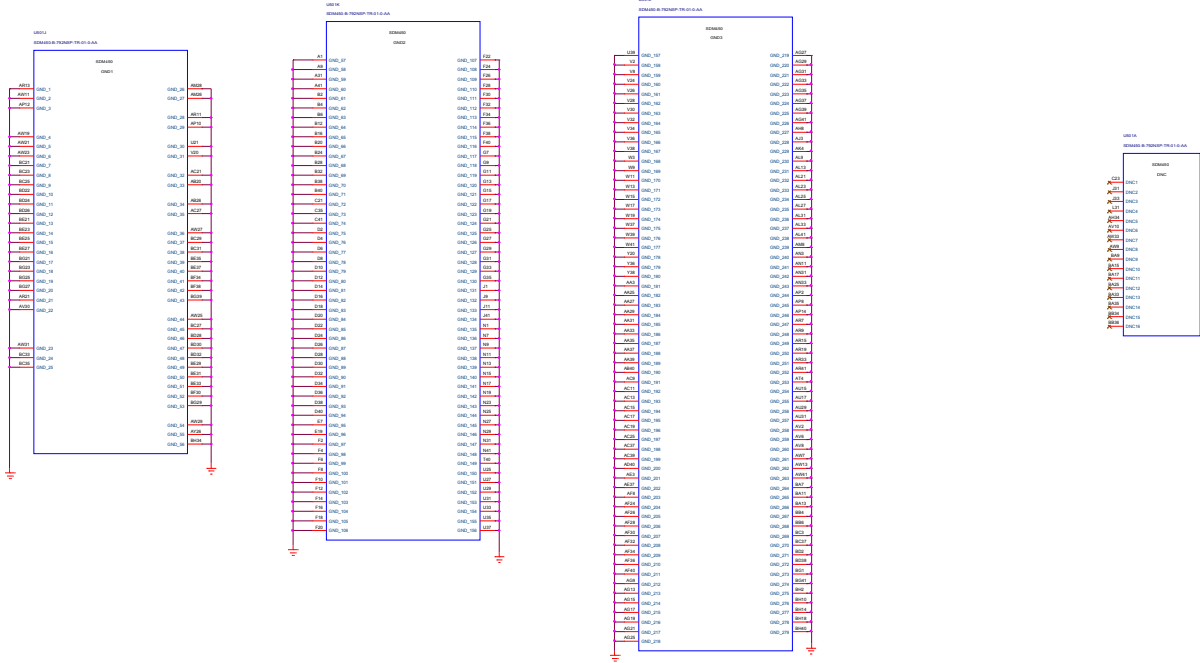




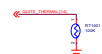
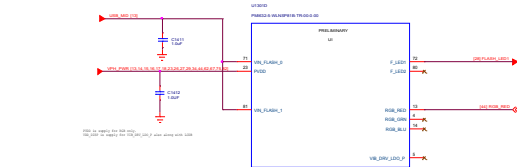










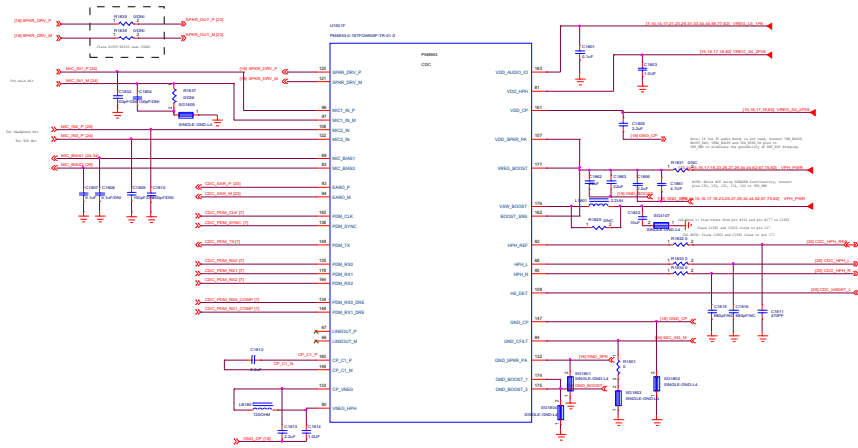






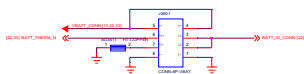




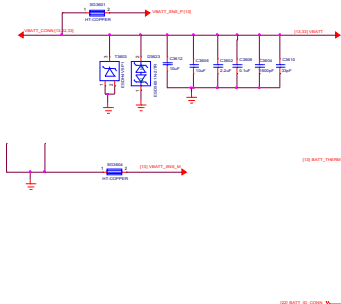
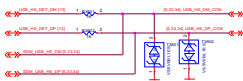




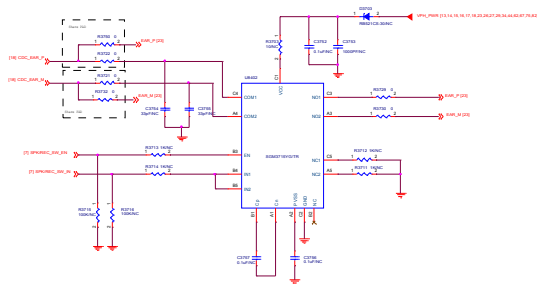
Battery Connector



USB\_IF



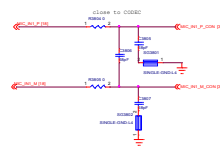
200PFC06 200PFC06: 2G3706/2G3707 place near SPK conn.



Mic2

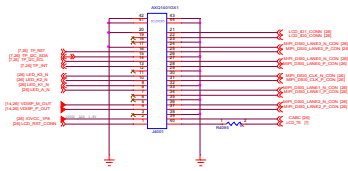


Mic1

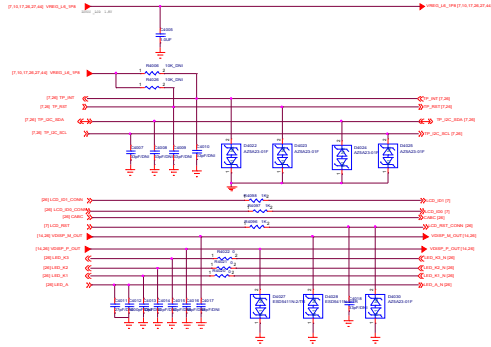
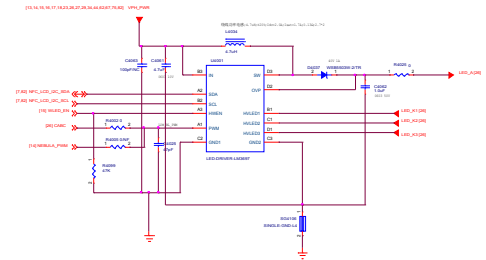


Note: EURO/US compatible design,  
Default US type

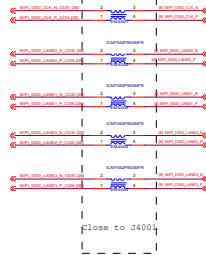
## LCD-Connector



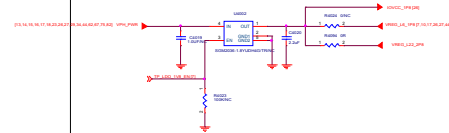
## LCD-BACKLIGHT



## Common Mode Filter



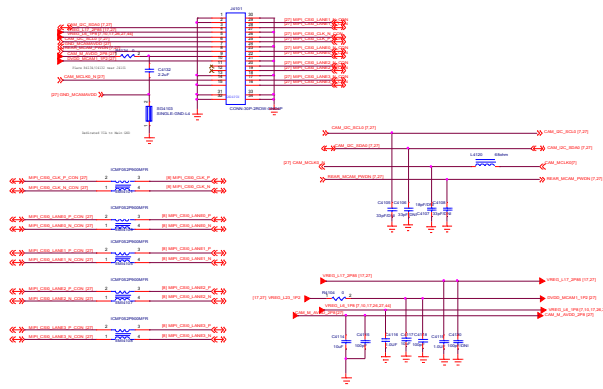
## TP\_1V8



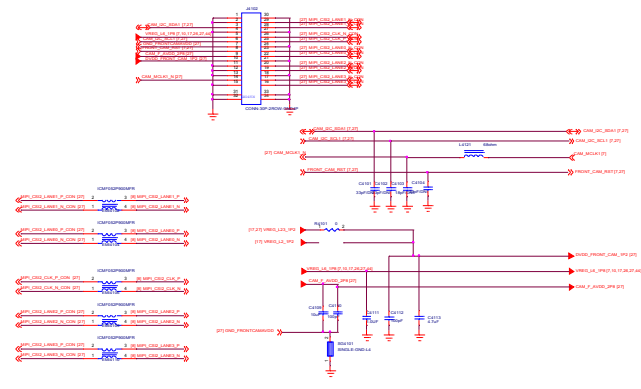


## Main Camera

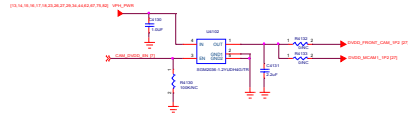
CAD note: Ensure the DGND to main-GND directly and robustly



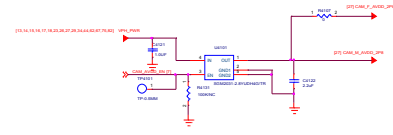
## Front Camera



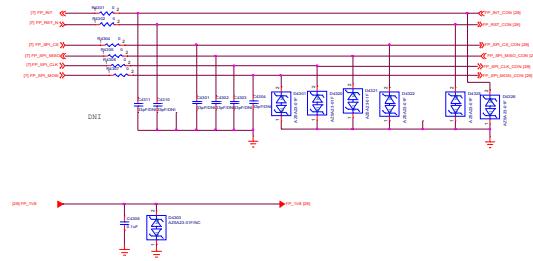
## CAM DVDD 1P2



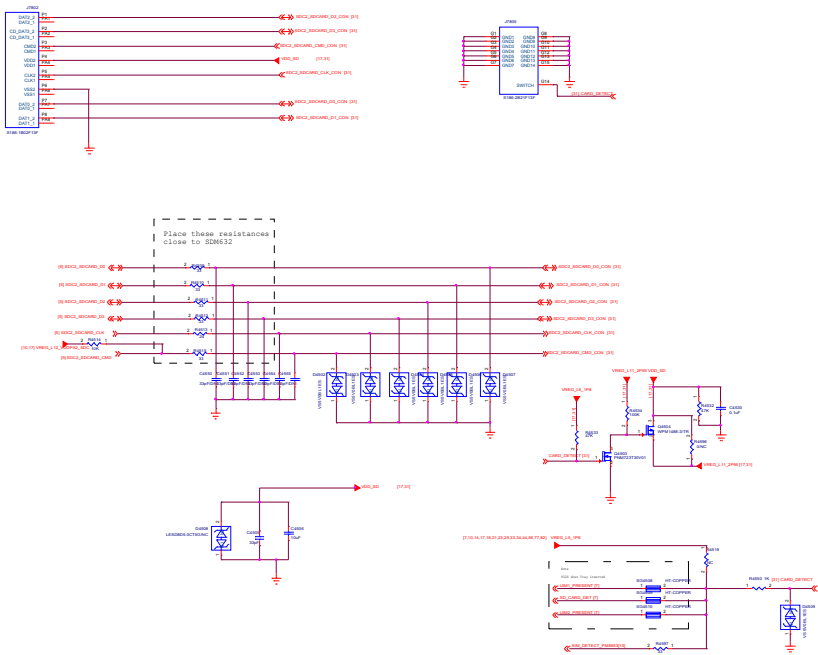
## CAM AVDD 2P8



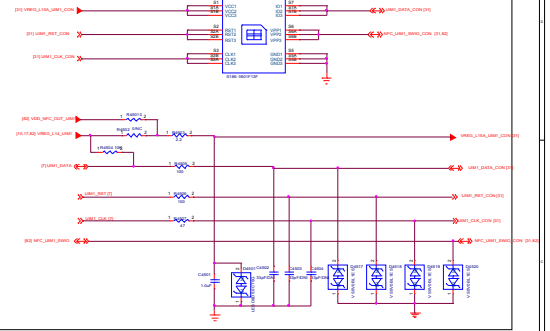




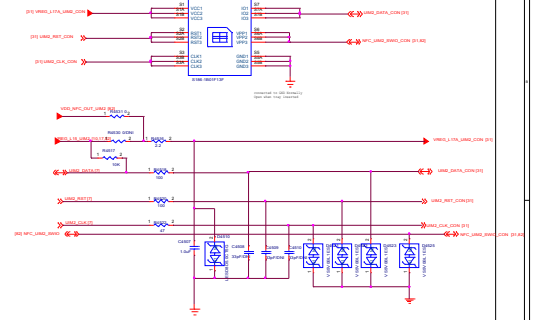
## TF CARD



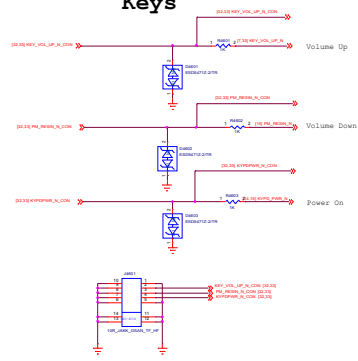
## USIM1



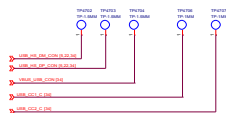
## USIM2



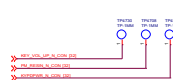
Keys



## USB



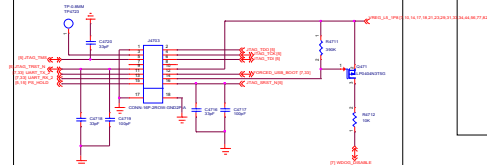
## KEYS



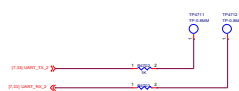
## DOWNLOAD



## JTAG CONNECTOR



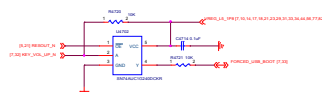
## UART



## BATTERY



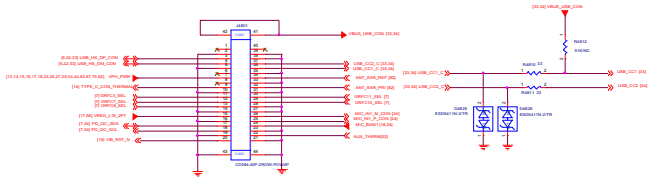
## LOGIC



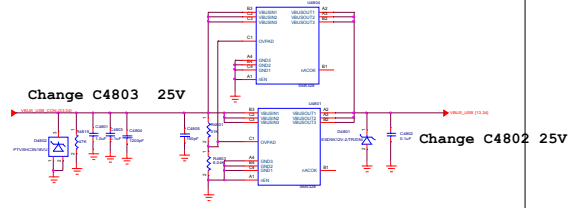
## GND



## Sub Board Connector

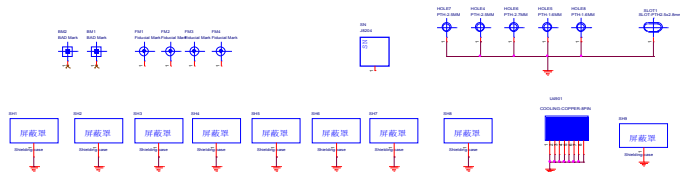


## OVP



## 防 烧 M O S







[illegible]

# Magnetic Sensor

The image displays three circuit diagrams for the LEDA\_3V3 power supply, showing the input, LDO, and output stages.

**Top Diagram: Input Stage**

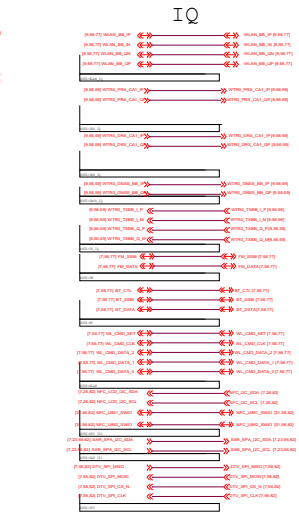
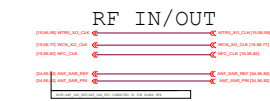
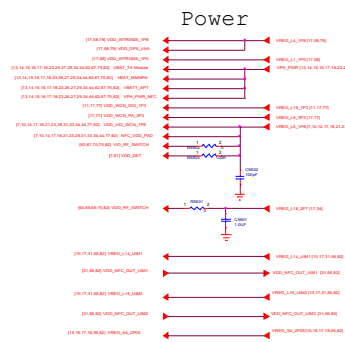
- Input: 3.3V, 100nF bypass capacitor.
- Output: 3.3V, 100k pull-up resistor.
- Component: 100nF bypass capacitor.

**Middle Diagram: LDO Stage**

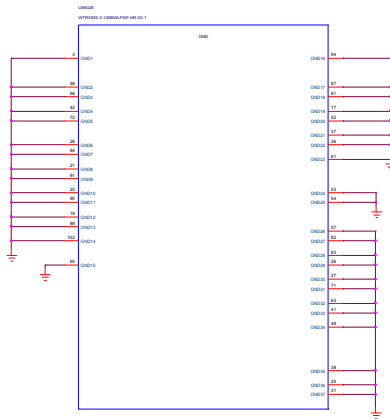
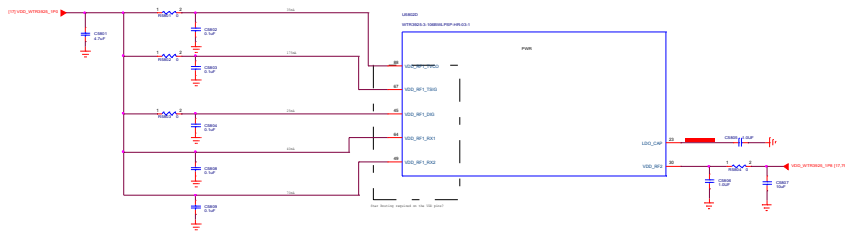
- Input: 3.3V, 100nF bypass capacitor.
- Output: 3.3V, 100k pull-up resistor.
- Component: LDO (Low Dropout Regulator).

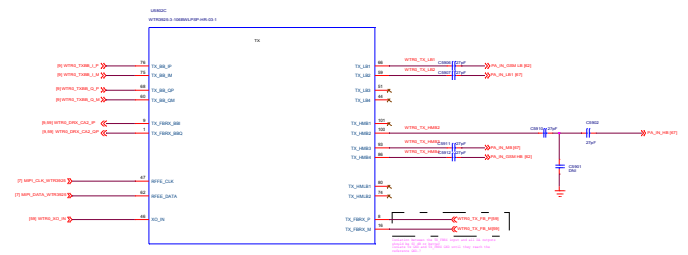
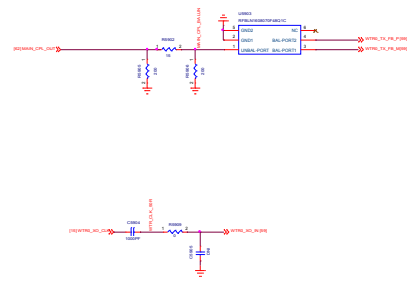
**Bottom Diagram: Output Stage**

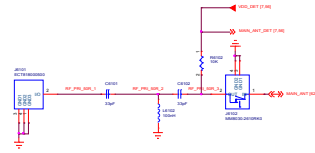
- Input: 3.3V, 100nF bypass capacitor.
- Output: 3.3V, 100k pull-up resistor.
- Component: 100nF bypass capacitor.



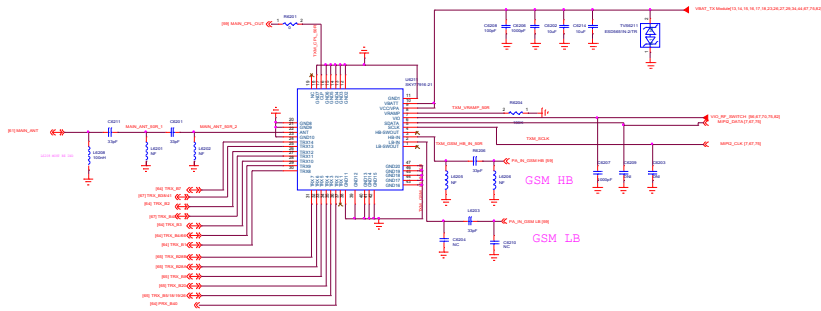




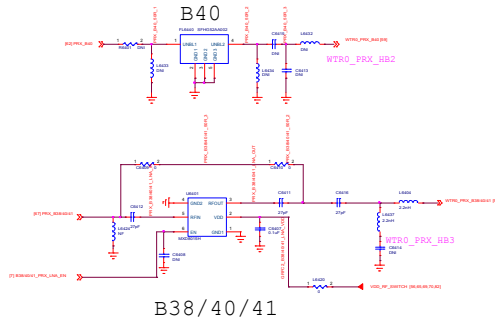
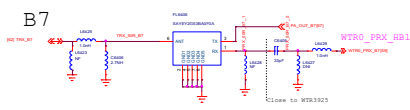
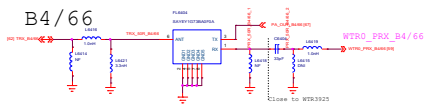
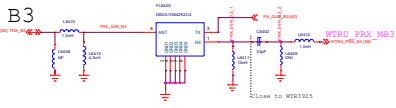
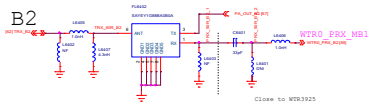
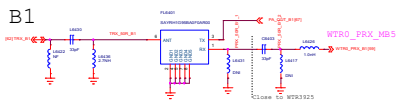




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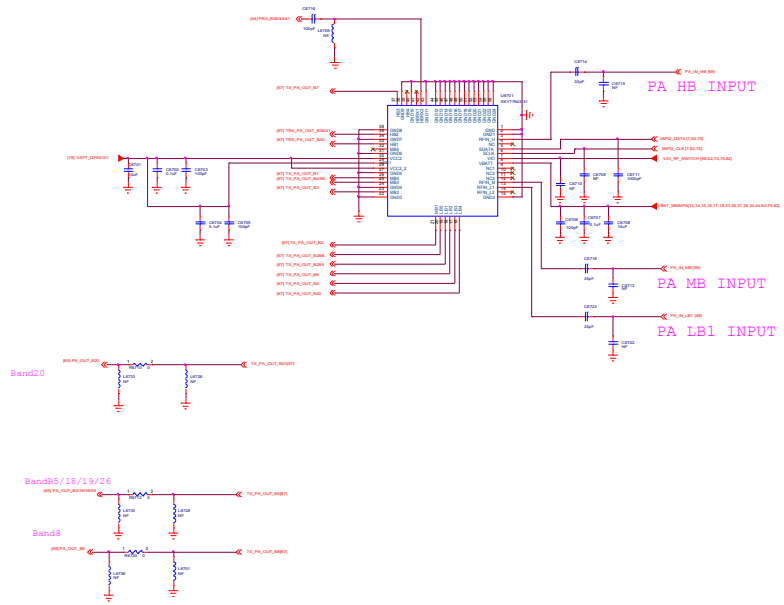
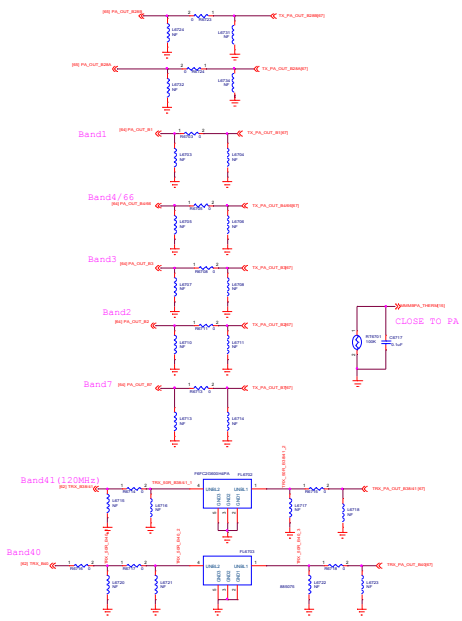


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REVISION	1.0	1.0
DATE	1.0	1.0
1.0	1.0	1.0

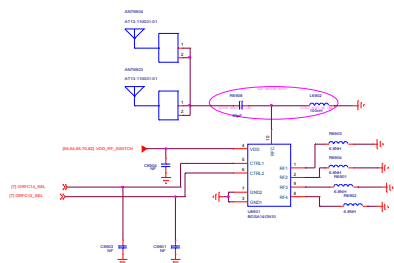
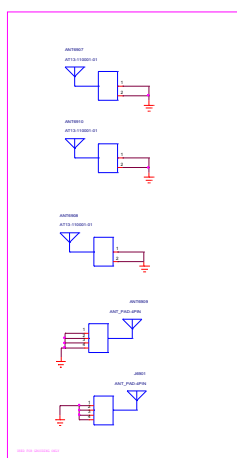
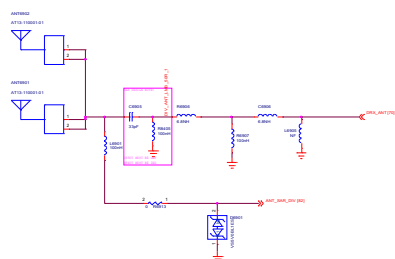










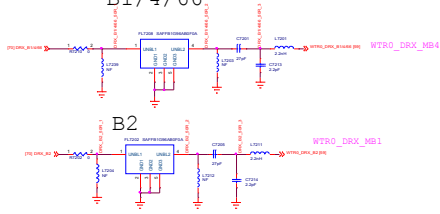
A dashed rectangular box containing the text "RF Port".

Power

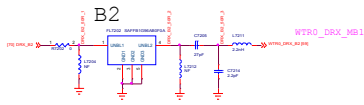
Title: <b>DESIGN SYSTEM with CAT-5/6/7E</b>		REV: <b>1/0</b>
DOCUMENT NO.: <b>WINTECH-01-001-SCH01</b>		Rev: <b>0</b>
DEPARTMENT: <b>Shanghai Hardware</b>	DESCRIPTION: <b>WIP-0001</b>	
<b>WINTECH</b>		



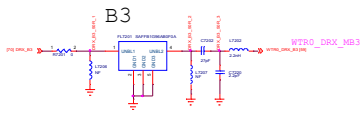
B1/4/66



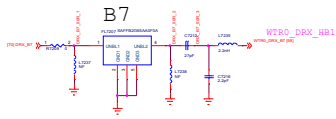
B2



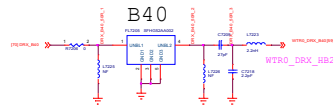
B3



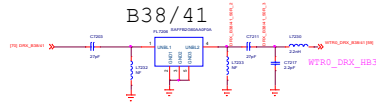
B7

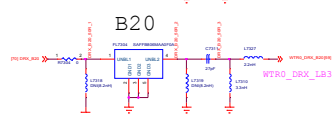
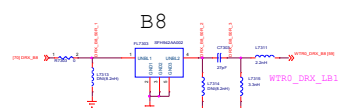
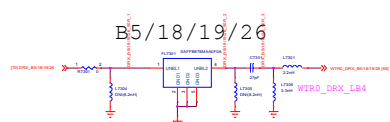
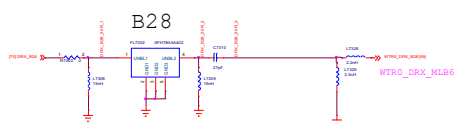



B40



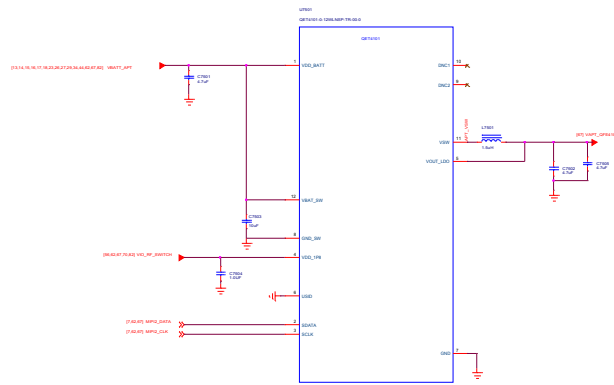
B38/41







RF Port



Job #	SEARCH-1107088 sub-CAT-010000	Alt: YES
DOCUMENT NO.:	WINGTECH-BE-439-SC0001	Rev: 0
DEPARTMENT:	Shanghai Hardware	DESCRIPTION: WWP-0000
<b>WINGTECH</b>		
Project	2010-05-14	Sheet: 01



